



Modifications of 9202/076 Issue 1 – DCR # 321

1. Change #1 :

For MH1RT ASIC family, Atmel proposes its packages **with lid connected to ground**.

Reason of change :

The note 'lid is connected to ground' is not specified in the detail specification 9202/076.

Description of change:

In §1.7.1 MQFPF196, §1.7.2 MQFPF256, §1.7.3 MQFPT352, §1.7.4 MCGA349 and §1.7.5 MCGA472, Add note 3. lid is connected to ground.

2. Change #2 :

§1.4.1 ESCC component number
'XYZ' has to stay a code with 3 digits allocated to Atmel.

Reason of change :

In 9202/076, 'X' is a single letter allocated by ESCC executive, and only 2 digits ('YZ') are allocated to Atmel to identify the ASIC.

Atmel needs 3 digits to identify its ASIC : 'X' indicates the country, and 'YZ' is a chrono. For instance 'FPK' is Atmel identification for 'HBRISC2'.

Description of change:

replace

- *Manufacturer Specific ASIC Identification: XYZ (as applicable) where:*

X : Single letter allocated by the ESCC Executive to the manufacturer and each complete code registered with the ESCC Executive Secretariat.

YZ : Individual 2 character code allocated by the Manufacturer to a specific ASIC design.

By:

- Manufacturer Specific ASIC Identification: XYZ.

3. Change #3 :

§1.5. Maximum ratings : Clarification expected

Reason of change :

To avoid confusion between supply pin and other input pins

Description of change:

Add in line 'supply current', row 'remarks': 'Each supply pin'

4. Change #4 :

§1.5. Maximum ratings : Mismatch about notes

Reason of change :

Note 3 is not suitable for 'thermal resistance'



Note 4 is not complete.

Description of change:

- Add note 3

3. Rth(j-c) is given for the smallest available matrix which corresponds to the worst case (highest junction-to-case resistance).

- Rename Note 3 in Note 4 (suitable for 'soldering temperature'), add note 4 is applicable for MQFP packages.

5. Change #5 :

§1.7.4 MCGA349

'A' is missing in bottom view description.

6. Change #6 :

§2.3.1. Room temperature electrical measurements : VIL, VIH, VT

Reason of change :

Information is missing to be homogeneous with Atmel MH1RT datasheet.

Description of change:

§2.3.1.1 (VDD=2.5V)

Add:

Low level input voltage	VIL	-	CMOS buffers VDD=2.3V		0.3 VDD	V
High level input voltage	VIH	-	CMOS buffers VDD=2.7V	0.7 VDD		V
Threshold Schmitt trigger input voltage	VT+	-	Note 5	1.06	1.61	V
Threshold Schmitt trigger input voltage	VT-	-	Note 5	0.78	1.25	V
Hysteresis	Delta VT	-	Note 5	0.25		V

§2.3.1.2 (VDD=3V)

Add:

Low level input voltage	VIL	-	CMOS buffers VDD=2.7V		0.8	V
High level input voltage	VIH	-	CMOS buffers VDD=3.3V	2		V
Threshold Schmitt trigger input voltage	VT+	-	Note 5	1.25	1.93	V
Threshold Schmitt trigger input voltage	VT-	-	Note 5	0.90	1.42	V
Hysteresis	Delta VT	-	Note 5	0.31		V



§2.3.1.3 (VDD=3.3V)

Add:

Low level input voltage	VIL	-	CMOS buffers VDD=3V		0.8	V
High level input voltage	VIH	-	CMOS buffers VDD=3.6V	2		V
Threshold Schmitt trigger input voltage	VT+	-	Note 5	1.40	2.08	V
Threshold Schmitt trigger input voltage	VT-	-	Note 5	0.99	1.51	V
Hysteresis	Delta VT	-	Note 5	0.37		V

§2.3.1.4 (VDD=2.5, 3 or 3.3V VCC=5V)

Add:

Low level input voltage	VIL	-	PICV, PICV5 buffers VDD min		0.8	V
High level input voltage	VIH	-	PICV, PICV5 buffers VDD max	2		V
Threshold Schmitt trigger input voltage	VT+	-	Note 5	1.40	2.08	V
Threshold Schmitt trigger input voltage	VT-	-	Note 5	0.99	1.51	V
Hysteresis	Delta VT	-	Note 5	0.37		V

7. Change #7 :

§2.3.2. Notes to electrical measurements

Reason of change :

Notes 6 and 7 are no more applicable in final version

Description of change:

Remove notes 6 and 7.