ESCC

DOCUMENT CHANGE REQUEST

DCR number 954	Changes required for: General	Originator: Steve Jeffery
Date: 2016/02/05	Date sent: 2015/10/13	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title:	Integrated Circuits, Silicon Monolithic, CMOS, Cell-Based Array Based on Type ATC18RHA		
Number:	9202/080	Issue:	3

Other documents affected:

Page:

27 to 31 inclusive

Paragraph:

2.3.1.1, 2.3.1.2, 2.4

Original wording:

See relevant paragraphs of the current published spec.

Proposed wording:

For 2.3.1.1:
1. add and 47 to 52 to the title, i.e. ...(Variants 01 to 23 and 47 to 52).
2. Change the IOZL maximum limit from -1 to 1.

For 2.3.1.2:
1. add and 53 to 58 to the title, i.e. ...(Variants 24 to 46 and 53 to 58).
2. Change the IOZL maximum limit from -1 to 1.

For. 2.4: Change the VOH minimum limit from 2.6 to VCC-0.4.

Justification:

2.3.1.1, 1 and 2.3.1.2, 1 - Include the listing of all applicable Variants for the required electrical measurements.
2.3.1.1, 2 and 2.3.1.2, 2 - Correction of typographic errors, i.e. removal of erroneous -ve symbols.
2.4 - There are actually two possible minimum absolute limits for VOH during Parameter Drift Value testing, it depends on the Specified Supply Voltage of the Variant being tested.

Attachments:
N/A
Modifications:
N/A
Approval signature:
Date signed:
2016-02-05