



DOCUMENT CHANGE REQUEST

DCR number	998	Changes required for:	General	Originator:	André Uguen
Date:	2016/06/16	Date sent:	2016/05/25	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: Integrated Circuits, Silicon Monolithic, Pulse Width Modulator based on Type ST1843

Number:	9108/020	Issue:	1
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Other documents affected:

Page:

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Paragraph:

2.3.2

Original wording:

minimum limit for Idischg parameter is 7,8mA

Proposed wording:

minimum limit for Idischg parameter shall be 7,2mA

Justification:

typo when releasing the first issue; value in line with similar device in specification 9108/021

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2016-06-16