



DOCUMENT CHANGE REQUEST

DCR number 1251 Changes required for: General

Date: 2019/05/14

Date sent: 2019/03/19

Originator: Steve Thacker

Organisation: ESCC Executive
Secretariat

Status: IMPLEMENTED

Title: CMOS Dual 3-Input NOR Gate plus Inverter, based on type 4000B

Number: 9201/040

Issue: 4

Other documents affected:

9201/054-4, 9201/068-4, 9201/122-4, 9201/132-4, 9201/133-4, 9202/038-4, 9202/060-4, 9202/068-4, 9202/069-4, 9203/061-4, 9204/013-4, 9204/067-4, 9204/068-4, 9204/072-4, 9204/077-4, 9204/078-4, 9204/079-4, 9204/080-4, 9205/016-5, 9205/020-4, 9205/022-4, 9207/008-4, 9208/002-4, 9301/009-4, 9306/013-4, 9306/032-4, 9306/034-4, 9306/040-4, 9306/049-4, 9306/055-5, 9401/054-4, 9405/016-5, 9410/016-4

Page:

Paragraph:

Original wording:

Proposed wording:

Justification:

Manufacturer ST confirms these specs are no longer supported in email RE: DCR1200 on ST 4000B Series dated 16/03/2019 from ST/A.Uguen



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Title: CMOS Dual 4-Input NAND Gate, based on type 4012B

Number: 9201/044

Issue: 4

Other documents affected:

9201/045-4, 9201/046-4, 9201/062-4, 9201/066-4, 9202/041-4

Page:

Paragraph:

Original wording:

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Status: IMPLEMENTED

Originator: Steve Thacker

Organisation: ESCC Executive
Secretariat

Title: CMOS 8-Stage Static Bidirectional Parallel/Serial Input/Output Bus Register with 3-State Outputs,

Number: 9306/025

Issue: 4

Other documents affected:

Page:

Paragraph:

Original wording:

Proposed wording:

Justification:

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2019-05-14