	ESC	C	I	DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Q	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Dual 4-Bit	Negative-Edg	ge-Triggered	Binary Counters, ba	used on type 54HC393	
Number:	9204/074		Issue:	6		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Ge	eneral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent:	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	EMENTED				Secletanat	
Title:	CMOS Presettable	Divide-by-N (Counter, based	on type 4018B		
Number:	9204/021		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C	C	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	MENTED				Secretariat	
Title:	Integrated Circuits,	Silicon Monol	lithic, HCMOS	3 to 8 Line Decode	er/Demultiplexer with Fully	
Number:	9408/046		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C	C	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes rec	quired for: G	eneral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	EMENTED				Secretariat	
Title:	Integrated Circuits,	Silicon Monol	thic, HCMOS	Quad 2-Input NAN	ID Gate with Schmitt-Trigger	
Number:	9201/120		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	ecs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Ge	eneral	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Quad Bus	Buffers with 3	3 State Output	s, based on type 54	4HC125	
Number:	9401/039		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C	I	DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Q	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	EMENTED				Ocorolanal	
Title:	CMOS Triple 3-Inp	ut OR Gates, I	based on typ	e 4075B		
Number:	9201/065		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. Justification:						

	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: (General	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Synchron	ous Presettabl	e 4-Bit Deca	de Counter with Dire	ect Clear, based on type 54HC160	
Number:	9204/062		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	Iblished & still supp	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
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Justification:						

	ESC	C	C	DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	EMENTED				Secretariat
Title:	HCMOS Octal Bus	Buffers with Ir	nverted 3-Sta	te Outputs, based o	on type 54HC240
Number:	9401/034		Issue:	5	
Other documen	ts affected:			·	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	g:				
Per currently pu	ublished & still supp	orted ESCC sp	Decs		
Proposed wordi	ng:				
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 					
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.					
Justification:					

	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: (General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Qud 2-inp	out OR Gates,	based on typ	e 54HC32		
Number:	9201/111		Issue:	5		
Other documen	ts affected:			·		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still supp	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 						
Justification:						

	ESC	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Ge	neral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS Hex Non-Inv	verting Buffers	s with 3-State C	Outputs, based on	type 4503B	
Number:	9401/030		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

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	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				occicianat	
Title:	CMOS Quad 2-Inp	ut NAND Gate	, based on t	ype 4011B		
Number:	9201/043		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	becs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 						
Justification:						

	<u>ESC</u>	C		DOCUMEN	T CHANGE REQUEST	
DCR number	1258	Changes ree	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Octal D-T	ype Edge-Trig	gered Flip-F	lops with Clear, ba	sed on type 54HC273	
Number:	9203/053		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS Dual 1-of-4	Decoder/Dem	ultiplexers (Outputs Low on Sele	ct), based on type 4556B	
Number:	9408/025		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requirement	s in each spe nt Type Varia	c as applica nts, MAXIM	ble e.g.: UM RATINGS, PHYS	ESCC Detail Specs should be removed	
Justification:						

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: (General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Asynchron	nous Negative	-Edge-Trigg	ered 14 Bit Binary Co	ounters,based on type 54HC4020	
Number:	9204/070		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL 						
IDENTIFICATIC	ON, FUNCTIONAL E	DIAGRAM, PIN	N ASSIGNME	ENT.		

	ESC	C	D	OCUMENT	CHANGE REQUEST		
DCR number	1258	Changes requ	iired for: Ge	eneral	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 20	19/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Dual 4-Inpu	t OR Gates, bas	sed on type 4	072B			
Number:	9201/082	ls	sue:	5			
Other documen	ts affected:	·		-			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC spe	cs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: 							
Remove referer		ent Type Variant	s, MAXIMUN	I RATINGS, PHYS	SICAL DIMENSIONS AND TERMINAL		
Justification:							

	ESC	C		DO	CUMENT	CHANGE REQUEST
DCR number	1258	Changes ree	quired for:	Gene	al	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19)		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secletanat
Title:	CMOS BCD-to-Dec	imal OR Bina	ry-to-Octal	Decod	er, based on ty	pe 4028B
Number:	9205/010		Issue:	ł	5	
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	rted ESCC sp	becs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requirement	s in each spe nt Type Varia	c as applica nts, MAXIN	able e. //UM R	g.:	ESCC Detail Specs should be removed
Justification:						
				_		

	ESC	C		DOCUMEN	CHANGE REQUEST		
DCR number	1258	Changes ree	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	Integrated Circuits,	Silicon Monol	ithic, HCMC	S Asynchronous Ne	egative-Edge-Triggered 14-Bit		
Number:	9204/076		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
Remove referer	sociated requirement nces from: Compone DN, FUNCTIONAL D	ent Type Varia	nts, MAXIM	UM RATINGS, PHY	SICAL DIMENSIONS AND TERMINAL		
Justification:							

	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes red	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS Analogue M	Iultiplexer/Den	nultiplexer, t	based on type 4067E		
Number:	9408/009		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referen	sociated requiremen	its in each spe ent Type Varia	c as applica nts, MAXIM	ble e.g.: UM RATINGS, PHY:	ESCC Detail Specs should be removed	
Justification:						

	ESC	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Ge	neral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent:	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS 8-Bit Shift	Registers wit	h 3 State Outpu	t Registers, based	d on type 54HC595	
Number:	9306/051		Issue:	6		
Other documen	ts affected:		•			
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

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	ESC	C		DOC	CUMENT	CHANGE REQUEST
DCR number	1258	Changes red	quired for:	Genera	al -	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19)		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secretariat
Title:	HCMOS Synchrono	ous 4-Bit Binar	y Counter,	with dir	ect Clear base	d on type 54HC161
Number:	9204/059		Issue:	5		
Other document	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j:					
Per currently pu	iblished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requirement	ts in each spe ent Type Varia	c as applica nts, MAXIN	able e.g //UM RA	.:	ESCC Detail Specs should be removed
Justification:						

	ESC	C	l	DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: (General	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secletanat	
Title:	HCMOS Quad Bila	iteral Switch, b	ased on type	e 54HC4066		
Number:	9408/052		Issue:	5		
Other document	ts affected:			·		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. Justification:						

	<u>ESC</u>	C		DOCUN	/ENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for:	General		Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secretariat
Title:	HCMOS Triple 3-In	put AND Gate	s, based or	n type 54HC1	1	
Number:	9201/117		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requirement	ts in each spe ent Type Varia	c as applica nts, MAXIM	able e.g.: IUM RATING		ESCC Detail Specs should be removed
Justification:						

	<u>ESC</u>	C		DO	CUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	Genera	al	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19)		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED					Secletanat	
Title:	CMOS Low Power	Monostable/A	stable Mult	tivibrato	r, based on typ	e 4047B	
Number:	9207/003		Issue:	5			
Other documen	ts affected:			•			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
including all ass Remove referen	sociated requirement	s in each spe nt Type Varia	c as applicants, MAXIM	able e.g MUM RA	.:	SICAL DIMENSIONS AND TERMINAL	
Justification:							

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes rec	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS 8-Bit Piso	Shift Register,	with fully b	uffered outputs, base	d on type 54HC166	
Number:	9306/043		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C		DOC	CUMENT	CHANGE REQUEST
DCR number	1258	Changes rec	quired for:	Genera	al	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19)		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secletanat
Title:	CMOS Quad 2-Inpu	ut Exclusive O	R Gates, b	based or	type 4030B	
Number:	9201/047		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 						
including all ass Remove referer	sociated requirement	ts in each specent Type Varia	c as applicants, MAXIN	able e.g MUM RA	.:	SICAL DIMENSIONS AND TERMINAL
Justification:						

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes rec	quired for:	General	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED				Secretariat
Title:	CMOS Hex Buffer/C	Converter Inve	rting, based	d on type 4049UB	
Number:	9202/045		lssue:	6	
Other documen	ts affected:			·	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j :				
Per currently pu	ublished & still suppo	rted ESCC sp	ecs		
Proposed wordi	ng:				
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 					
Justification:					

	ESC	C	Γ	OCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	quired for: G	eneral	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Presettable	Binary Up/Do	own Counter	With Fully Buffered	Outputs, based on type 4516B		
Number:	9204/045		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
including all ass Remove referer	Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Dual 2-Lir	ne to 4-Line De	ecoders/Den	nultiplexers with Inve	rted Outputs, based on type	
Number:	9205/017		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requiremen	ts in each spe ent Type Varia	c as applica ints, MAXIM	ble e.g.: UM RATINGS, PHYS	ESCC Detail Specs should be removed	
Justification:						

	ES(<u>, C</u>	D	OCUMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for: Ge	eneral	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent:	2019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	EMENTED				Occitication
Title:	CMOS Hex Inver	ter, based on ty	pe 4069UB		
Number:	9401/010		Issue:	5	
Other documen	ts affected:			-	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	g:				
Per currently pu	ublished & still sup	ported ESCC sp	Decs		
Proposed wordi	ing:				
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 					
including all ass Remove refere	sociated requireme	ents in each spe nent Type Varia	c as applicable ints, MAXIMUM	e.g.: I RATINGS, PHYS	SICAL DIMENSIONS AND TERMINAL
Justification:					

	ESC	C		DO	CUMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for:	Gener	al	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19)		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secletanat
Title:	CMOS Ripple-Carr	y Binary Coun	ter/Divider,	, based	on type 4020B	
Number:	9204/022		Issue:	5	;	
Other documen	ts affected:			•		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL 						
IDENTIFICATIO	DN, FUNCTIONAL D					
Justification:						

	ESC	C		DC	CUMENT	CHANGE REQUEST	
DCR number	1258	Changes req	uired for:	Gene	eral	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	019/03/19			Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED					Secletanat	
Title:	Integrated Circuits,	Silicon Monoli	thic, HCMO	S Di	al J-K Positive-E	Edge-Triggered Flip-Flop with	
Number:	9306/048		lssue:		5		
Other document	ts affected:			-			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j:						
Per currently pu	ublished & still suppo	orted ESCC spo	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
including all ass Remove referer	sociated requirement	s in each spec nt Type Variar	as applications, MAXIM	ble e UM F	.g.: RATINGS, PHYS	ESCC Detail Specs should be removed	
Justification:							

	ESC	C		DOCUM	IENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for:	General		Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secretariat
Title:	HCMOS Octal D-Ty	pe Transpare	ent Latches	with 3-State C	outputs,	based on type 54HC373
Number:	9203/059		Issue:	5		
Other document	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	rted ESCC sp	becs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requirement	s in each spe nt Type Varia	c as applica nts, MAXIM	ble e.g.: IUM RATINGS		ESCC Detail Specs should be removed
Justification:						

	ESC	C		DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for: (General	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive
Status: IMPLE	MENTED				Secretariat
Title:	HCMOS Quad 2-L	ine to 1-Line D	ata Selector	s/Multiplexers with 3	-State Outputs, based on type
Number:	9408/047		Issue:	5	
Other documen	ts affected:				
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j:				
Per currently pu	iblished & still supp	orted ESCC sp	Decs		
Proposed wordi	ng:				
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 					
Justification:					

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes red	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS Dual 4-Stag	je Static Shift I	Register witl	h Serial Input/Paralle	I Input, based on type 4015B	
Number:	9306/015		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	becs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referen	sociated requiremen	ts in each spe ent Type Varia	c as applica nts, MAXIM	ble e.g.: UM RATINGS, PHYS	ESCC Detail Specs should be removed	
Justification:						

	ESC	C	I	DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: C	General	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secletanat	
Title:	CMOS 8-Input NAM	ND/AND Gate,	based on ty	pe 4068B		
Number:	9201/061		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 						
including all ass Remove referen	sociated requiremen	ts in each spe ent Type Varia	c as applicat ints, MAXIMU	ble e.g.: JM RATINGS, PHYS	SICAL DIMENSIONS AND TERMINAL	
Justification:						

	<u>ESC</u>	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes requ	ired for: Ge	eneral	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 20	19/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS Octal Count	er/Divider, base	d on type 40)22B		
Number:	9204/023	ls	sue:	5		
Other documen	ts affected:	·		·		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	orted ESCC spec	cs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL 						
Justification:	ON, FUNCTIONAL D		400IGINIVIEI	NI.		

	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS Quad 2-Inp	out AND Gate,	based on typ	be 4081B		
Number:	9201/052		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still supp	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 						
including all ass Remove referen	sociated requiremer	nts in each spe ent Type Varia	c as applica ints, MAXIM	ble e.g.: UM RATINGS, PHYS	SICAL DIMENSIONS AND TERMINAL	
Justification:						

	ESC	C		DOCUMI	ENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for:	General		Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive
Status: IMPLE	MENTED					Secretariat
Title:	CMOS Presettable	Up/Down Cou	inter Binary	OR BCD Deca	ade, ba	used on type 4029B
Number:	9204/025		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	rted ESCC sp	becs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referen	sociated requirement	s in each spe nt Type Varia	c as applica nts, MAXIM	ble e.g.: UM RATINGS,		ESCC Detail Specs should be removed
Justification:						

	ESC	C	D	OCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for: Ge	eneral	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Hex 'D'-Typ	e Flip-Flop, ba	ased on type 4	0174B			
Number:	9203/038		Issue:	5			
Other documen	ts affected:			·			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still supp	orted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
including all ass Remove referer	sociated requiremer	its in each spe ent Type Varia	c as applicable ints, MAXIMUN	e e.g.: M RATINGS, PHYS	SICAL DIMENSIONS AND TERMINAL		
Justification:							

	ESC	C	Γ	OCUMENT	CHANGE REQUEST				
DCR number	1258	Changes red	quired for: G	eneral	Originator: Steve Thacker				
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive				
Status: IMPLE	EMENTED				Secretariat				
Title:	Integrated Circuits, Silicon MonIolithic, HCMOS Octal Bus Buffer with 3-State Outputs, based on								
Number:	9401/048		lssue:	6					
Other documen	ts affected:			·					
Page:									
Various									
Paragraph:									
1.4.2, 1.7 & oth	ers as applicable								
Original wording	g:								
Per currently pu	ublished & still suppo	rted ESCC sp	ecs						
Proposed wordi	ng:								
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads									
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.									
Justification:									

	ESC	C	I	DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes red	quired for: (General	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Quad 2-In	put NAND Ga	tes, based o	n type 54HC00		
Number:	9201/105		Issue:	6		
Other documen	ts affected:			·		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	iblished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 						
Justification:						

	ESC	C		DOCUMEN	T CHANGE REQUEST		
DCR number	1258	Changes ree	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS 8-Bit Add	ressable Latch	es, based o	n type 54HC259			
Number:	9203/073		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still supp	orted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: 							
	nces from: Compon DN, FUNCTIONAL I				YSICAL DIMENSIONS AND TERMINAL		
Justification:							

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes red	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Quad D-T	ype Edge-Trig	gered Flip F	lops WITH Clear, ba	ased on type 54HC175	
Number:	9203/052		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C	I	DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes ree	quired for: 0	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS Analogue N	/ultiplexer/Den	nultiplexer, b	ased on type 4052B		
Number:	9202/048		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still supp	orted ESCC sp	Decs			
Proposed wordi	ng:					
 package types Var 01 F Var 02 F Var 03 D 	T has advised for th (ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads			•	that they now only support the following	
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	<u>ESC</u>	C	Γ	DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Dual 'D'-Ty	/pe Flip-flop, ba	ised on type 4	4013B			
Number:	9203/023		Issue:	6			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still supp	ported ESCC sp	ecs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	<u>ESC</u>	C	C	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	EMENTED				Secretariat	
Title:	CMOS 8-Stage Syn	chronous Sta	tic Shift Regis	ster, based on type	4014B	
Number:	9306/014		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL 						
	DN, FUNCTIONAL D					
Justification:						

	ESC	C		DO	CUMENT	CHANGE REQUEST	
DCR number	1258	Changes rec	quired for:	Genera	al	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19	1		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED					Secretariat	
Title:	HCMOS Dual D-Ty	pe Flip-Flop w	ith Preset a	and Cle	ar, based on ty	/pe 54HCT74	
Number:	9203/070		Issue:	5			
Other document	ts affected:			•			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	<u>ESC</u>	C	C	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS 9-Bit Odd	Even Parity G	enerators/Ch	eckers based on ty	pe 54HC280	
Number:	9208/003		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	<u>ESC</u>	C	C	OCUMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker
Date: 2024/02	/29	Date sent:	2019/03/19		Organisation: ESCC Executive
Status: IMPLE	MENTED				Secretariat
Title:	CMOS Analogue M	ultiplexer/Der	nultiplexer (Tri	ple 2-Channel), ba	sed on type 4053B
Number:	9202/049		Issue:	5	
Other documen	ts affected:			·	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	g:				
Per currently pu	ublished & still suppo	rted ESCC sp	Decs		
Proposed wordi	ng:				
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads					
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.					
Justification:					

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	<u>ESC</u>	C	l	DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes rec	quired for: (General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Dual Posit	ive or Negativ	e Edge Schr	mitt-Rretriggerable M	lonostable Multivibrators with	
Number:	9207/006		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requirement	ts in each specent Type Varia	c as applicat nts, MAXIMU	ble e.g.: JM RATINGS, PHYS	ESCC Detail Specs should be removed	
Justification:						

	ESC	C		DOC	UMENT	CHANGE REQUEST
DCR number	1258	Changes rec	quired for:	General		Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secletanat
Title:	CMOS Dual 1-of-4	Decoder/Dem	ultiplexer, b	based on	type 4555B	
Number:	9408/011		Issue:	6		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 						
Justification:						

	ESC	C		DOCI	JMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for:	General		Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19)		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secletanat
Title:	CMOS Dual J-K Ma	ster Slave Fli	p-Flop, bas	sed on type	e 4027B	
Number:	9203/022		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 						
Justification:						

	ESC	C		DOCUMEN	T CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Octal D-Ty	ype Transpare	ent Latches v	vith 3-State Output	s, based on type 54HC573	
Number:	9202/072		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requiremen	ts in each spe ent Type Varia	c as applica nts, MAXIM	ble e.g.: UM RATINGS, PH	ed ESCC Detail Specs should be removed	
Justification:						

	ESC	C		DOC	UMENT	CHANGE REQUEST
DCR number	1258	Changes rec	quired for:	General		Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secretariat
Title:	HCMOS Quad 2-In	put NOR Gate	s,based on	n type 54H	HC02	
Number:	9201/113		Issue:	7		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 						
Justification:						

	ESC	C	Γ	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes ree	quired for: G	eneral	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Octal Bus	Buffers with 3	-State Output	s, based on type 54	4HC541	
Number:	9401/047		Issue:	5		
Other document	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 						
Remove referer	sociated requirement nces from: Compone DN, FUNCTIONAL D	ent Type Varia	nts, MAXIMU	M RATINGS, PHYS	SICAL DIMENSIONS AND TERMINAL	
Justification:						

	ESC	C		DOC	CUMENT	CHANGE REQUEST
DCR number	1258	Changes red	quired for:	Genera	al -	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19)		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secretariat
Title:	HCMOS Analogue	Multiplexer/De	emultiplexe	er (Triple	2-Channel),ba	ased on type 54HC4053
Number:	9408/065		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	rted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	sociated requirement	s in each spe nt Type Varia	c as applica nts, MAXIN	able e.g MUM RA	.:	ESCC Detail Specs should be removed
Justification:						

	<u>ESC</u>	C		DO	CUMENT	CHANGE REQUEST
DCR number	1258	Changes red	quired for:	Gener	al	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19	I		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secretariat
Title:	HCMOS Hex D-Typ	be Edge-Trigg	ered Flip-Fl	lops wi	th Clear, based	l on type 54HC174
Number:	9306/052		Issue:	Ę	5	
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C	[DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Hex Buffer	s/Converters	with Inverted	Inputs, based on ty	vpe 54HC4049	
Number:	9401/037		Issue:	6		
Other documen	ts affected:			·		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Ge	eneral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS Quad Low to	o High 3-State	e Voltage Leve	I Shifter, based on	type 40109B	
Number:	9407/003		Issue:	6		
Other documen	ts affected:			•		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: (General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS 8-Stage Shi	ft and Store B	us Register	with Synchronous Se	erial Outputs and 3-State Parallel	
Number:	9306/026		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: 						
	nces from: Compone DN, FUNCTIONAL D	•••			SICAL DIMENSIONS AND TERMINAL	
Justification:						

	ESC	C		DOCUME	ENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	General		Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive	
Status: IMPLE	MENTED					Secretariat	
Title:	CMOS 14-Stage Rip	ople-Carry Bi	nary Counte	r/Divider and Os	scillate	or, based on type 4060B	
Number:	9204/052		Issue:	6			
Other documen	ts affected:			·			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	ESC	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes red	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS Octal D-Ty	vpe Edge-Trig	gered Flip-F	lops with 3 State Ou	tputs, based on type 54HC374		
Number:	9203/060		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	ESC	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes red	quired for: (General	Originator: Steve Thacker		
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secletanat		
Title:	CMOS Triple 3-Inpu	ut AND Gate,	based on typ	be 4073B			
Number:	9201/064		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	Iblished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
Remove referen	including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	ESC	C	I	DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes red	quired for: 0	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS 3-Line to 8	3-Line Decode	rs/Demultiple	exers with Address L	atches, based on type 54HC237		
Number:	9205/021		lssue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	<u>ESC</u>	C		DO	CUMENT	CHANGE REQUEST		
DCR number	1258	1258 Changes required for: General Originator: Steve Thacker						
Date: 2024/02	2/29	Date sent: 2	2019/03/19	I		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED					Secletanat		
Title:	CMOS Quad 2-Inpu	t NAND-Gate	with Schm	nitt Trig	ger Inputs, base	ed on type 4093B		
Number:	9409/002		Issue:	:	5			
Other documen	ts affected:		•					
Page:								
Various								
Paragraph:								
1.4.2, 1.7 & oth	ers as applicable							
Original wording	j :							
Per currently pu	ublished & still suppo	rted ESCC sp	Decs					
Proposed wordi	ng:							
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads								
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.								
Justification:								

	ESC	C	I	DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for: C	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Dual 4-Inpu	ut NOR Gates,	based on typ	be 4002B			
Number:	9201/042		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still supp	orted ESCC sp	Decs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	ESC	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Quad 2-Inpu	ut OR Gates, I	based on typ	be 4071B			
Number:	9201/063		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 							
Justification:							

	ESCC DOCUMENT CHANGE REQUEST							
DCR number	1258	Changes re	quired for: Gen	eral	Originator: Steve Thacker			
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat			
Status: IMPLE	MENTED							
Title:	CMOS Quad 2-Inpu	t NOR Gates	, based on type	4001B				
Number:	9201/041		Issue:	5				
Other document	ts affected:							
9201/042-5, 9201/043-5, 9201/047-5, 9201/048-5, 9201/052-5, 9201/055-5, 9201/061-5, 9201/063-5, 9201/064-5, 9201/065-5, 9201/082-5, 9201/105-6, 9201/106-5, 9201/107-5, 9201/108-5, 9201/109-5, 9201/110-5, 9201/111-5, 9201/113-7, 9201/114-6, 9201/117-5, 9201/118-5, 9201/120-5, 9201/120-5, 9202/043-5, 9202/040-5, 9202/042-5, 9202/043-5, 9202/044-5, 9202/045-6, 9202/046-5, 9202/048-5, 9202/048-5, 9202/045-5, 9202/045-5, 9202/045-6, 9202/045-6, 9203/052-5, 9203/052-5, 9203/053-5, 9203/054-6, 9203/059-5, 9203/060-5, 9203/064-5, 9203/070-5, 9203/073-5, 9204/020-6, 9204/021-5, 9204/022-5, 9204/023-5, 9204/024-5, 9204/025-5, 9204/026-5, 9204/028-5, 9204/036-5, 9204/041-5, 9204/045-5, 9204/025-6, 9204/054-5, 9204/052-5, 9204/065-5, 9204/065-5, 9204/069-5, 9204/070-5, 9204/071-5, 9204/074-6, 9204/076-5, 9205/011-5, 9205/013-5, 9205/017-5, 9205/011-5, 9205/013-5, 9205/017-5, 9205/012-5, 9204/074-6, 9207/003-5, 9207/006-5, 9207/007-5, 9208/003-5, 9209/001-5, 9209/004-5, 9209/005-5, 9306/014-5, 9306/015-5, 9306/016-5, 9306/022-5, 9306/041-5, 9306/042-5, 9306/043-5, 9306/047-5, 9306/015-5, 9306/015-5, 9306/051-6, 9306/022-5, 9306/041-5, 9306/042-5, 9306/043-5, 9306/047-5, 9306/050-5, 9306/051-6, 9306/052-5, 9306/041-5, 9401/013-5, 9401/030-5, 9401/033-5, 9401/034-5, 9401/037-6, 9401/038-6, 9401/039-5, 9408/012-5, 9408/012-5, 9408/025-5, 9408/038-5, 9408/014-5, 9408/012-5, 9408/025-5, 9408/038-5, 9408/014-5, 9408/012-5, 9408/025-5, 9408/038-5, 9408/046-5, 9408/047-5, 9408/012-5, 9408/012-5, 9408/025-5, 9408/038-5, 9408/046-5, 9408/047-5, 9408/045-5, 9408/055-5, 9408/065-5, 9408/045-5, 9408/055-5, 9408/055-5, 9408/055-5, 9408/055-5, 9408/065-5, 9408/0								
Various								
Paragraph:								
1.4.2, 1.7 & oth	ers as applicable							
Original wording	g:							
Per currently pu	ublished & still suppo	rted ESCC sp	Decs					
Proposed wordi	ng:							
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 								
Justification:								

	ESC	C	C	OCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker		
Date: 2024/02 Status: IMPLE		Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Title:	HCMOS Synchrone	ous 4-Bit Up/D	own Binary C	ounters (Dual Cloc	k with Clear), based on type		
Number:	9204/065		Issue:	5			
Other document	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j:						
Per currently pu	iblished & still suppo	orted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
including all ass Remove referer	Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	<u>ESC</u>	C		DOCUMEN	T CHANGE REQUEST		
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secletanat		
Title:	HCMOS Analogue I	Multiplexer/De	emultiplexe	r, based on type 54	HC4051		
Number:	9408/064		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	ESC	C	ļ	DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes red	quired for: (General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS Octal D-Ty	/pe Edge-Trig	gered Flip-Fl	ops with 3-State Ou	tputs, based on type 54HC574		
Number:	9203/054		lssue:	6			
Other documen	ts affected:			·			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	<u>ESC</u>	C		DOCUMEN	F CHANGE REQUEST	
DCR number	1258	Changes req	uired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	EMENTED				Secretariat	
Title:	HCMOS 8-Bit Sipe	o Shift Register,	based on	type 54HC164		
Number:	9306/041		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still supp	ported ESCC sp	ecs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. Justification:						

	ESC	C		DOCUMEN	T CHANGE REQUEST		
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Dual Binary	Up Counter, I	based on ty	pe 4520B			
Number:	9204/028		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
including all ass Remove referer	sociated requirement	ts in each spe ent Type Varia	ec as applica ints, MAXIN	able e.g.: IUM RATINGS, PH	SICAL DIMENSIONS AND TERMINAL		
Justification:							

	ESC	C		DOC	CUMENT	CHANGE REQUEST	
DCR number	1258	Changes rec	quired for:	Genera	I	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	019/03/19			Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED					Secretariat	
Title:	CMOS Decade Co	ounter/Divider, b	based on ty	/pe 4017	7B		
Number:	9204/020		Issue:	6			
Other document	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j:						
Per currently pu	blished & still supp	oorted ESCC sp	ecs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. Justification:							

	ESC	C	D	OCUMENT	CHANGE REQUEST		
DCR number	1258	Changes red	quired for: Ge	eneral	Originator: Steve Thacker		
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	Integrated Circuits,	Silicon Monol	ithic, HCMOS	8-Bit Identity Com	parator with Fully Buffered		
Number:	9209/005		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	rted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	ESC	C		DOCUMEN	T CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Hex Schmi	tt Trigger Inve	erters, base	d on type 54HC14		
Number:	9409/007		Issue:	5		
Other document	ts affected:			·		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	Iblished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads • Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. Justification:						

	ESC	C	I	DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	quired for: Q	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive		
Status: IMPLE	MENTED				Secretariat		
Title:	Integrated Circuits,	Silicon Monol	ithic, HCMOS	8 8-Bit Piso Shift R	egister with Fully Buffered		
Number:	9306/042		lssue:	5			
Other documen	ts affected:			·			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

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	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	uired for: (General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	Integrated Circuits,	Silicon Monoli	thic, HCMO	S Octal Bus Buffer w	vith Inverted 3-State Outputs,		
Number:	9401/049		lssue:	5			
Other documen	ts affected:			·			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	ESC	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Ge	neral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive	
Status: IMPLE	EMENTED				Secretariat	
Title:	HCMOS Quad 2-In	out Exclusive	OR Gates, bas	ed on type 54HC8	36	
Number:	9201/119		Issue:	5		
Other documen	ts affected:		•			
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	<u>ESC</u>	C		DOCUMEN	IT CHANGE REQUEST		
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Dual Monos	table Multivib	rator with Re	eset, based on typ	e 4538B		
Number:	9207/007		Issue:	5			
Other documen	ts affected:			·			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
Remove referer	including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	ESC	C	I	DO	CUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: 0	Genei	ral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent:	2019/03/19			Organisation: ESCC Executive	
Status: IMPLE	MENTED					Secretariat	
Title:	CMOS 8-Channel M	Iultiplexer wit	h 3-State Ou	Itputs	, based on type	4512B	
Number:	9408/006		Issue:	ť	5		
Other documen	ts affected:			-			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	rted ESCC sp	Decs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads							
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Justification:							

	ESC	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS Dual 4-Lir	ne to 1-Line Da	ta Selector	s/Multiplexers, based	on type 54HC153		
Number:	9408/038		Issue:	5			
Other documen	ts affected:			·			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads							
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Justification:							

	ESC	C	D	OCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for: Ge	eneral	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secletanat		
Title:	Integrated Circuits,	Silicon Monol	ithic, HCMOS	8-Bit Latch/Shift R	egister with Fully Buffered		
Number:	9306/054		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	orted ESCC sp	Decs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads							
including all ass Remove referer	Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Micropower	Phase-Locked	d Loop, bas	ed on type 4046B			
Number:	9202/044		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	uired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	019/03/19		Organisation: ESCC Executive		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS 4-Bit Mag	nitude Compar	ators, base	d on types 54HC85 a	nd 54HC85A		
Number:	9209/004		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
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Justification:							

	ESC	C	C	DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS 8-Line to 3	-Line Priority	Encoders, ba	sed on type 54HC1	48		
Number:	9410/017		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: 							
	Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	ESC	C	C	OCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	quired for: Ge	eneral	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS Octal Bus	Buffers with 3	-State Output	s, based on type 54	4HCT244		
Number:	9402/009		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	<u>ESC</u>	C		DOC	UMENT	CHANGE REQUEST	
DCR number	1258	Changes ree	quired for:	Genera	I	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED					Secretariat	
Title:	CMOS Analogue M	ultiplexer/Den	nultiplexer, l	based o	n type 4051B		
Number:	9202/047		Issue:	6			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	rted ESCC sp	becs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	ESC	C	D	OCUMENT	CHANGE REQUEST		
DCR number	1258 Changes required for: General Originator: Steve Thacker						
Date: 2024/02	2/29	Date sent:	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	EMENTED				Secretariat		
Title:	HCMOS 8-to-1-Lin	e Data Selecto	ors/Multiplexers	with fully buffered	d outputs, based on type 54HC151		
Number:	9408/054		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	Decs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	quired for: (General	Originator: Steve Thacker		
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive		
Status: IMPLE	MENTED				Secretariat		
Title:	Integrated Circuits,	SIlicon Monol	ithic, HCMO	S Asynchronous Ne	gative Edge Triggered 12-Bit		
Number:	9204/069		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
Remove referer	including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	ESC	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes red	quired for: (General	Originator: Steve Thacker		
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Quad Bilate	ral Switch, bas	sed on type	4066B			
Number:	9408/005		lssue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	Iblished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: 							
Remove referer	Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	ESC	C	l	DOCUMEN	F CHANGE REQUEST	
DCR number	1258	Changes rec	quired for: (General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPL	EMENTED				Georetanat	
Title:	CMOS Quad Exclu	usive NOR Gate	e, based on	type 4077B		
Number:	9201/055		lssue:	5		
Other documer	its affected:	•				
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ners as applicable					
Original wordin	g:					
Per currently p	ublished & still suppo	orted ESCC sp	ecs			
Proposed word	ing:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						

	ESC	C		DO	CUMENT	CHANGE REQUEST
DCR number	1258	Changes red	quired for:	Gener	al	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19)		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secletanat
Title:	CMOS Quad AND/0	OR Select Ga	te, based o	on type 4	4019B	
Number:	9202/051		Issue:	5		
Other documen	ts affected:			·		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	rted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 						
Justification:						

	ESC	C	D	OCUMENT	CHANGE REQUEST		
DCR number	1258 Changes required for: General Originator: Steve Thacker						
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive		
Status: IMPLE	MENTED				Secretariat		
Title:	Integrated Circuits,	Silicon Monol	lithic, HCMOS I	Hex Inverter with F	ully Buffered Outputs, based on		
Number:	9401/033		Issue:	5			
Other documen	ts affected:		-				
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still suppo	orted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	<u>ESC</u>	C	[DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker		
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Quad True/0	Complement E	Buffer with Ur	buffered Outputs,	based on type 4041UB		
Number:	9202/040		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	rted ESCC sp	Decs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads							
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.							
Justification:							

	ESC	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive		
Status: IMPLE	MENTED				Secretariat		
Title:	HCMOS Triple 3-Ir	put NOR Gate	es with fully	buffered Outputs, ba	used on type 54HC27		
Number:	9201/109		Issue:	5			
Other documen	ts affected:						
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	ublished & still supp	orted ESCC sp	Decs				
Proposed wordi	ng:						
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 							
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Justification:							

	ESC	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Ge	eneral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Octal Bus	Transcievers	with 3-State O	utputs, based on t	ype 54HCT245	
Number:	9405/014		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
including all ass Remove referer	Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.					
Justification:						

	ESC	C		DOCUM	IENT	CHANGE REQUEST		
DCR number	1258	Changes ree	quired for:	General		Originator: Steve Thacker		
Date: 2024/02	/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED					Secletanat		
Title:	CMOS Dual Monos	stable Multivib	rator, based	d on type 4098	B			
Number:	9206/003		Issue:	5				
Other documen	ts affected:							
Page:								
Various								
Paragraph:								
1.4.2, 1.7 & oth	ers as applicable							
Original wording	j :							
Per currently pu	ublished & still suppo	orted ESCC sp	becs					
Proposed wordi	ng:							
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 								

	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes ree	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	CMOS 4-Bit Magnit	tude Compara	tor, based o	n type 4063B		
Number:	9209/001		Issue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	Iblished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 						
Remove referer	including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.					
Justification:						

	ESC	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes rec	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS 8-Stage Sta	tic Shift Regis	ter, based o	on type 4021B			
Number:	9306/016		lssue:	5			
Other documen	ts affected:			·			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
Remove referer	including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	ESC	C	C	OCUMENT	CHANGE REQUEST
DCR number	1258	Changes requ	ired for: Ge	eneral	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 20	19/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	EMENTED				Secretariat
Title:	CMOS Hex Buffer/	Converter Non-I	nverting, ba	sed on type 4050B	
Number:	9202/046	ls	ssue:	5	
Other documen	ts affected:	·		·	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	g:				
Per currently pu	ublished & still supp	orted ESCC spe	cs		
Proposed wordi	ng:				
 package types Var 01 Fl Var 02 Fl Var 03 D 	T has advised for th (ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads			SCC Detail specs	that they now only support the following
including all ass Remove referer	sociated requiremer	nts in each spec ent Type Variant	as applicabl s, MAXIMUI	e e.g.: M RATINGS, PHYS	ESCC Detail Specs should be removed
Justification:					

	<u>ESC</u>	C		DOCUM	ENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for:	General		Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secretariat
Title:	CMOS Presettable	8-Bit Synchro	nous Down	Counters, bas	ed on t	ype 40103B
Number:	9204/036		Issue:	5		
Other documen	ts affected:			•		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still suppo	rted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						
					_	

	ESC	C	D	OCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for: Ge	neral	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secletanat	
Title:	HCMOS Octal Bus	Transceivers	with 3-State O	utputs, based on t	ype 54HC245	
Number:	9405/013		Issue:	6		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads						
including all ass Remove referer	Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.					
Justification:						

	ESC	C		DOCUMENT	CHANGE REQUEST		
DCR number	1258	Changes red	quired for:	General	Originator: Steve Thacker		
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secretariat		
Title:	CMOS Quad Exclus	sive OR Gate,	based on t	type 4070B			
Number:	9201/048		Issue:	5			
Other documen	ts affected:			•			
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	j :						
Per currently pu	Iblished & still suppo	rted ESCC sp	ecs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL 							
Justification:	DN, FUNCTIONAL D			ILINI.			

	ESC	C	D	OCUMENT	CHANGE REQUEST		
DCR number	1258	Changes re	quired for: Ge	neral	Originator: Steve Thacker		
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat		
Status: IMPLE	MENTED				Secletanat		
Title:	Integrated Circuits,	Silicon Monol	ithic, HCMOS	Quad 2-Input and	Gate with Fully Buffered Outputs,		
Number:	9201/106		Issue:	5			
Other document	ts affected:		-				
Page:							
Various							
Paragraph:							
1.4.2, 1.7 & oth	ers as applicable						
Original wording	g:						
Per currently pu	ublished & still suppo	orted ESCC sp	Decs				
Proposed wordi	ng:						
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed 							
Remove referer	including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:							

	ESC	C		DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Synchron	ous 4-Bit Up/D	own Binary	Counter, based on ty	/pe 54HC191	
Number:	9204/066		Issue:	6		
Other documen	ts affected:			·		
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still supp	orted ESCC sp	Decs			
Proposed wordi	ng:					
Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): • Var 01 FP with G2 leads • Var 02 FP with G4 leads • Var 03 DIP with G2 leads • Var 04 DIP with G4 leads						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	<u>ESC</u>	C	Γ	DOCUMENT	CHANGE REQUEST	
DCR number	1258	Changes rec	quired for: G	General	Originator: Steve Thacker	
Date: 2024/02	2/29	Date sent: 2	019/03/19		Organisation: ESCC Executive Secretariat	
Status: IMPLE	MENTED				Secretariat	
Title:	HCMOS Quad 2-Li	ne to 1-Line Da	ata Selectors	/Multiplexers, based	d on type 54HC157	
Number:	9408/057		lssue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still suppo	orted ESCC sp	ecs			
Proposed wordi	ng:					
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads 						
Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT.						
Justification:						

	ESC	C		DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive
Status: IMPLE	MENTED				Secretariat
Title:	CMOS Ripple Carry	Binary Coun	ter/Divider, I	based on type 4040	3
Number:	9204/026		Issue:	5	
Other documen	ts affected:			·	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j :				
Per currently pu	ublished & still suppo	rted ESCC sp	Decs		
Proposed wordi	ng:				
 Manufacturer ST has advised for the 4000B & 54HC series of ESCC Detail specs that they now only support the following package types (ref email from ST/A.Uguen dated 16/03/2019): Var 01 FP with G2 leads Var 02 FP with G4 leads Var 03 DIP with G2 leads Var 04 DIP with G4 leads Var 04 DIP with G4 leads Accordingly, all other listed variants/packages (e.g. CCP, SO) in each of the listed ESCC Detail Specs should be removed including all associated requirements in each spec as applicable e.g.: Remove references from: Component Type Variants, MAXIMUM RATINGS, PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION, FUNCTIONAL DIAGRAM, PIN ASSIGNMENT. 					
Justification:		,			

	ESC	C	[DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for: G	eneral	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED				Secretariat
Title:	HCMOS 8 Bit Sipo	Shift Latch Re	egisters with	3-State Outputs, ba	sed on type 54HC4094
Number:	9306/050		Issue:	5	
Other documen	ts affected:			·	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j :				
Per currently pu	Iblished & still suppo	rted ESCC sp	Decs		
Proposed wordi	ng:				
 package types Var 01 Fl Var 02 Fl Var 03 D 	T has advised for the (ref email from ST/A. P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads			ESCC Detail specs	that they now only support the following
including all ass Remove referer	sociated requirement	s in each spe nt Type Varia	c as applicab ints, MAXIMU	le e.g.: IM RATINGS, PHY	ESCC Detail Specs should be removed
Justification:					

	ESC	C		DOC	CUMENT	CHANGE REQUEST
DCR number	1258	Changes req	uired for:	Genera	al	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	019/03/19			Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED					Secretariat
Title:	HCMOS 4-to-6 Lin	e Decoders/De	multiplexer	rs with I	nverted Outpu	ts, based on type 54HC154
Number:	9205/023		lssue:	5		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	g:					
Per currently pu	ublished & still supp	orted ESCC sp	ecs			
Proposed wordi	ng:					
 package types Var 01 Fl Var 02 Fl Var 03 D 	T has advised for th (ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads				Detail specs t	that they now only support the following
including all ass Remove referer	sociated requiremer	nts in each spec ent Type Variar	c as applicants, MAXIM	able e.g /IUM RA	.:	ESCC Detail Specs should be removed
Justification:						

	ESC	C		DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes red	quired for:	General	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED				Secretariat
Title:	CMOS Quad 3-Sta	te NOR R/S L	atches, base	ed on type 4043B	
Number:	9202/042		Issue:	5	
Other documen	ts affected:				
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j :				
Per currently pu	ublished & still suppo	orted ESCC sp	Decs		
Proposed wordi	ng:				
 package types Var 01 Fl Var 02 Fl Var 03 D Var 04 D Accordingly, all including all ass Remove referent IDENTIFICATION 	(ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads other listed variants sociated requiremen	.Uguen dated /packages (e. ts in each spe ent Type Varia	16/03/2019 g. CCP, SO c as applica nts, MAXIM):) in each of the listed ible e.g.: UM RATINGS, PHYS	that they now only support the following ESCC Detail Specs should be removed SICAL DIMENSIONS AND TERMINAL
Justification:					

	ESC	C		DOCUI	MENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for:	General		Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19			Organisation: ESCC Executive
Status: IMPLE	MENTED					Secretariat
Title:	HCMOS Quad 2-Ir	put NAND Ga	tes with Op	en Drain Ou	tput, base	ed on type 54HC03
Number:	9201/114		Issue:	6		
Other documen	ts affected:					
Page:						
Various						
Paragraph:						
1.4.2, 1.7 & oth	ers as applicable					
Original wording	j :					
Per currently pu	ublished & still supp	orted ESCC sp	ecs			
Proposed wordi	ng:					
 package types Var 01 Fl Var 02 Fl Var 03 D 	T has advised for th (ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads				ail specs	that they now only support the following
including all ass Remove referer	sociated requiremen	its in each spe ent Type Varia	c as applica nts, MAXIM	able e.g.: 1UM RATINO		ESCC Detail Specs should be removed
Justification:						

	ESC	C	D	OCUMENT	CHANGE REQUEST
DCR number	1258	Changes re	quired for: Ge	neral	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive
Status: IMPLE	EMENTED				Secretariat
Title:	HCMOS 8 to 1 Line	Data Selecto	ors/Multiplexers	with 3-State Outp	uts, based on type 54HC251
Number:	9408/048		Issue:	5	
Other documen	ts affected:		•		
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	g:				
Per currently pu	ublished & still suppo	rted ESCC sp	Decs		
Proposed wordi	ng:				
 package types Var 01 F Var 02 F Var 03 D 	T has advised for the (ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads			SCC Detail specs	that they now only support the following
including all ass Remove refere	sociated requirement	s in each spe nt Type Varia	ec as applicable ants, MAXIMUM	e.g.: I RATINGS, PHYS	ESCC Detail Specs should be removed
Justification:					

	ESC	C		DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes rec	uired for:	General	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED				Secretariat
Title:	HCMOS Triple 3-In	put NAND Gat	es, based c	on type 54HC10	
Number:	9201/107		Issue:	5	
Other documen	ts affected:				
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j :				
Per currently pu	Iblished & still suppo	rted ESCC sp	ecs		
Proposed wordi	ng:				
 Package types Var 01 Fl Var 02 Fl Var 03 D Var 04 D Accordingly, all including all ass Remove referent IDENTIFICATION 	(ref email from ST/A. P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads other listed variants/ sociated requirement	Uguen dated /packages (e.g s in each spec nt Type Variar	16/03/2019) g. CCP, SO) c as applica hts, MAXIM):) in each of the listed ble e.g.: UM RATINGS, PHYS	that they now only support the following ESCC Detail Specs should be removed SICAL DIMENSIONS AND TERMINAL
Justification:					

	ESC	C	I	DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes ree	quired for: 0	General	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED				Secretariat
Title:	CMOS 4-Bit Latch/	4-to-16 Decod	er, based on	type 4514B	
Number:	9408/012		Issue:	5	
Other documen	ts affected:			·	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	g:				
Per currently pu	ublished & still suppo	orted ESCC sp	becs		
Proposed wordi	ng:				
package types Var 01 Fl Var 02 Fl Var 03 D Var 04 D Accordingly, all including all ass Remove referent IDENTIFICATION	(ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads other listed variants sociated requirement	.Uguen dated s/packages (e.g its in each spe ent Type Varia	16/03/2019) g. CCP, SO) c as applicat nts, MAXIMU	in each of the listed ble e.g.: JM RATINGS, PHYS	that they now only support the following ESCC Detail Specs should be removed SICAL DIMENSIONS AND TERMINAL
Justification:					

	<u>ESC</u>	C		DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes require	ed for:	General	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2019	9/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED				Secletanat
Title:	CMOS HEX Schmi	itt Triggers, based	on type	40106B	
Number:	9409/005	Issu	re:	4	
Other documen	ts affected:				
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j :				
Per currently pu	Iblished & still supp	orted ESCC specs			
Proposed wordi	ng:				
 package types Var 01 Fl Var 02 Fl Var 03 D Var 04 D Accordingly, all including all ass Remove referent 	(ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads other listed variants sociated requiremen	NUguen dated 16/0 S/packages (e.g. Conts in each spec as ent Type Variants,	CP, SO) applical MAXIM): in each of the listed ble e.g.: UM RATINGS, PHYS	that they now only support the following ESCC Detail Specs should be removed SICAL DIMENSIONS AND TERMINAL

	ESC	C		DOCUMENT	CHANGE REQUEST
DCR number	1258	Changes ree	quired for:	General	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED				Secretariat
Title:	HCMOS Hex Buffe	rs/Converters,	based on t	ype 54HC4050	
Number:	9401/038		Issue:	6	
Other document	ts affected:				
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j :				
Per currently pu	ublished & still suppo	orted ESCC sp	ecs		
Proposed wordi	ng:				
package types Var 01 Fl Var 02 Fl Var 03 D Var 04 D Accordingly, all including all ass Remove referent IDENTIFICATIO	(ref email from ST/A P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads other listed variants sociated requiremen	.Uguen dated /packages (e. ts in each spe ent Type Varia	16/03/2019 g. CCP, SO c as applica nts, MAXIM):) in each of the listed able e.g.: IUM RATINGS, PHYS	that they now only support the following ESCC Detail Specs should be removed SICAL DIMENSIONS AND TERMINAL
Justification:					

	ESC	C		DOCUMEN	T CHANGE REQUEST
DCR number	1258	Changes re	quired for:	General	Originator: Steve Thacker
Date: 2024/02	/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive Secretariat
Status: IMPLE	MENTED				Secretariat
Title:	HCMOS 4 to 16 Lin	e Decoder/La	tch, based	on type 54HC4514	
Number:	9205/019		Issue:	5	
Other document	ts affected:				
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	j :				
Per currently pu	iblished & still suppo	rted ESCC sp	Decs		
Proposed wordi	ng:				
package types Var 01 Fl Var 02 Fl Var 03 D Var 04 D Accordingly, all including all ass Remove referent IDENTIFICATIO	(ref email from ST/A. P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads other listed variants/ sociated requirement	Uguen dated packages (e. s in each spe nt Type Varia	16/03/2019 g. CCP, SC c as applica nts, MAXIM)):)) in each of the liste able e.g.: 1UM RATINGS, PHY	s that they now only support the following d ESCC Detail Specs should be removed SICAL DIMENSIONS AND TERMINAL
Justification:					

	ESC	C	Γ	OCUMENT	CHANGE REQUEST
DCR number	1258	Changes red	quired for: G	eneral	Originator: Steve Thacker
Date: 2024/02	2/29	Date sent: 2	2019/03/19		Organisation: ESCC Executive
Status: IMPLE	MENTED				Secretariat
Title:	Integrated Circuits,	Silicon Monol	thic, HCMOS	Dual D-Type Flip-l	Flop with Preset and Clear and
Number:	9203/050		Issue:	5	
Other documen	ts affected:			·	
Page:					
Various					
Paragraph:					
1.4.2, 1.7 & oth	ers as applicable				
Original wording	g:				
Per currently pu	ublished & still suppo	rted ESCC sp	ecs		
Proposed wordi	ng:				
 package types Var 01 Fl Var 02 Fl Var 03 D Var 04 D 	(ref email from ST/A. P with G2 leads P with G4 leads IP with G2 leads IP with G4 leads	Uguen dated	16/03/2019):		that they now only support the following
including all ass Remove referer	sociated requirement	s in each spe nt Type Varia	c as applicab nts, MAXIMU	e e.g.: M RATINGS, PHYS	ESCC Detail Specs should be removed
Justification:					

Attachments:
N/A
Modifications:
N/A
Approval signature:
Surray Real
Date signed:
2024-02-29