	ESC		OCUMENT	CHANGE REQUEST	
DCR number	1454	Changes required for: Ge	eneral	Originator: Stephan Hernandez	
Date: 2021/08/19 Date sent: 2021/08/19		Date sent: 2021/08/19		Organisation: ESTEC	
Status: IMPLEMENTED					
Title:	ESCC Qualified Manufacturers List REP006				
Number:	REP006	Issue:	22		
Other documents affected:					
Page:					
-					
Paragraph:					
-					
Original wording:					
-					
Proposed wording:					
Extension with re-scope:					
 357A, Microchip (France) Introduction of the AT7991 (ESCC 9512/005), radiation tolerant GNSS baseband SoC. In addition to the GNSS core, the AT7991 embarks a LEON2FT processor, multiple interfaces like SPI, UART, Spacewire and 1553. It is packaged in a hermetic CQFP352. 359A, Microchip (France) Update of the domain: Introduction of 5 prequalified IP's. Obsolescence of multidecks packages, only flatsubstrate packages are proposed. 					
Justification:					
Publication QM	L August 2021				

Attachments:			
I/A			
Modifications:			
N/A			
Approval signature:			
Attack			
Date signed:			
2021-08-19			