

In LAT, 6 switches are sufficient for Resistive Endurance.

DOCUMENT CHANGE REQUEST

163 DCR number Changes required for: Qualification Originator: Philippe Baviere Organisation: CNES Date: 2005/11/02 Date sent: 2005/11/02 Status: IMPLEMENTED Title: Microswitches Sensitive 1PDT, based on series T3 Number: 1 3701/003 Issue: Other documents affected: Page: Page 12 Para 4.2.5 Paragraph: Page 12 Para 4.2.5 Original wording: Proposed wording: Rewrite (c) as following "Para 9.14.2 Inductive Endurance : Not Applicable" Delete in (d), the mention "The corresponding switches will be tested according to resistive endurance" Justification: The swtch T3 is a mechanical limit switch. It is always used at low level, and never used at high level with an inductive or capacitive load.

Attachments:
N/A
Modifications:
N/A
Approval signature:
12. Cc f(acri-q
Date signed:
2005-11-02