




DOCUMENT CHANGE REQUEST

DCR number		1563		Changes required for: General		Originator: Steve Jeffery	
Date: 2023/09/12				Date sent: 2023/03/29		Organisation: ESCC Executive	
Status: IMPLEMENTED							
Title:	INTEGRATED CIRCUITS, SILICON MONOLITHIC, CMOS DIGITAL, MEMORY, RADIATION						
Number:	9304/010		Issue:	1			
Other documents affected:							
Page:							
1, 5							
Paragraph:							
Title and Para. 1.4.2, Component Type Variants table							
Original wording:							
BASED ON TYPE NX1H35AS							
Proposed wording:							
BASED ON TYPE NX1H35AS (NG-MEDIUM)							
Justification:							
Expanding the FPGA's based on type to include a general part name, i.e. "NG-Medium", for clarification purposes (as there are also planned/possible ESCC Detail Specs for "NG-Large" and "NG-Ultra" FPGAs).							
Attachments:							
N/A							
Modifications:							
N/A							
Approval signature:							
							
Date signed:							
2023-09-12							