




DOCUMENT CHANGE REQUEST

DCR number		1734		Changes required for: MRB decision		Originator: Anita Weinschrott	
Date: 2025/06/02				Date sent: 2025/04/09		Organisation: TESAT	
Status: IMPLEMENTED							
Title:		INTEGRATED CIRCUITS, MONOLITHIC, CMOS AND HBT ON SiGe:C, 1.6 TO 12 GHz FREQUENCY					
Number:		9202/085		Issue:		1	
Other documents affected:							
Page:							
22							
Paragraph:							
APPENDIX A							
Original wording:							
Proposed wording:							
<p>Add a new line and in the column "Description of Deviations":</p> <p>Mounting Capability Test Conditions and Results are given in the Document No. 63.1504.017.66NOT, issue C, dated 12.03.2025, Mounting Capability Test of Novelo Package.</p> <p>Summary:</p> <p>PCB: 4 layers, Thickness: 788µm; CTE RT Duorid 6002: 16ppm/K; CTE Component: 7.7 ppm/K</p> <p>Thermal Cycling performed from -40°C to 100°C; 400 cycles successfully passed</p>							
Justification:							
MRB result, as agreed by IMST, DLR and ESA							

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2025-06-02