



# DOCUMENT CHANGE REQUEST

DCR number 245 Changes required for: General

Originator: S Thacker

Date: 2006/03/17

Date sent: 2006/03/17

Organisation: ESA/ESTEC

Status: IMPLEMENTED

Title: HCMOS Quad 2-Input NAND Gates, based on type 54HC00

Number: 9201/105

Issue: 2

Other documents affected:

9201/106-2, 9201/107-2, 9201/108-2, 9201/109-2, 9201/110-2, 9201/111-2, 9201/113-2, 9201/114-2, 9201/117-2, 9201/118-2, 9201/119-2, 9201/120-2, 9201/121-2, 9201/123-2, 9201/124-2, 9201/129-2, 9201/130-2, 9202/072-2, 9202/075-2, 9203/050-2, 9203/051-2, 9203/052-1, 9203/053-2, 9203/054-2, 9203/059-2, 9203/060-2, 9203/064-2, 9203/065-2, 9203/066-2, 9203/070-2, 9203/071-2, 9203/072-2, 9203/073-2, 9204/059-2, 9204/062-2, 9204/065-2, 9204/066-2, 9204/069-2, 9204/070-2, 9204/071-2, 9204/073-2, 9204/074-2, 9204/076-2, 9205/013-2, 9205/016-2, 9205/017-2, 9205/019-2, 9205/021-2, 9205/023-2, 9207/006-2, 9207/008-2, 9208/003-2, 9209/004-2, 9209/005-2, 9306/041-2, 9306/042-2, 9306/043-2, 9306/047-2, 9306/048-2, 9306/050-2, 9306/051-3, 9306/052-2, 9306/053-2, 9306/054-2, 9401/033-2, 9401/034-2, 9401/035-2, 9401/037-2, 9401/038-2, 9401/039-2, 9401/044-2, 9401/045-2, 9401/046-2, 9401/047-2, 9401/048-2, 9401/049-2, 9401/052-2, 9401/055-2, 9402/009-2, 9405/011-2, 9405/012-2, 9405/013-2, 9405/014-2, 9408/038-2, 9408/046-2, 9408/047-2, 9408/048-2, 9408/052-2, 9408/054-2, 9408/057-2, 9408/058-2, 9408/059-2, 9408/064-2, 9408/065-2, 9409/007-2, 9410/017-2

Page:

Various editorial changes throughout each spec

Paragraph:

Various editorial changes throughout each spec

Original wording:

Proposed wording:

Proposed Wording of Change

Changes include various editorial amendments in order to be consistent with other published ESCC Detail Specifications including the following points:

- 1) Component Type Variants: Delete terminal material/finish G8.
- 2) Capitalisation of terms (e.g. "Purchase Order", "Detail Specification", "Power Burn-in") , removal of obsolete terms (e.g. "applicable") where appropriate.
- 3) Replace various terms where appropriate.  
e.g.:  
"Typical" by "BSC"  
"CK" or "CL" by "CLK" to represent 'Clock'



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"f" by "fGEN"

4) Package Drawings:

Delete reference to note 6 for CCP.

Delete note 9 except for 14 & 16 pin packages

5) Para 1.7.5 Amend title to be "Notes to Physical Dimensions and Terminal Identification"

6) Apply the ESCC convention for units and values (e.g. "100 $\mu$ A" instead of "0.1mA", "6V" instead of "6.0V", "VSS" instead of "0V") and use the correct units in tables (e.g. "V") as appropriate.

7) Amend characteristic and pin names and symbols to be consistent throughout the spec. e.g. for input clamp voltages

8) Para 2.3.3 Note 1 Add to the end of the sentence " ....., inputs not under test shall be VIN=VDD or VSS and outputs not under test shall be open."

9) Correct the load capacitance tolerance to be "+/-" (was "+")

10) Amend text to reflect the correct Table reference terms "Room Temperature Electrical Measurements" & "High and Low Temperatures Electrical Measurements".

11) Addition of Appendix A for STM with content already included in issued 54HC (& 4000B series) ESCC specifications.

12) Add the output type to the spec title (e.g. "... With Fully Buffered Outputs")

13) Other minor editorial/format amendments:

spelling, grammar, use of punctuation, fonts, para & table layout.


Justification:

All changes are considered as editorial changes only for the purposes of clarification and consistency with other ESCC specifications, plus the correction of minor editorial errors.

Specifically:

1) "G8" is not supported by STM

2) to 13) For clarification and consistency with other ESCC detail specs and correction of minor editorial errors

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2006-03-17