DOCUMENT CHANGE REQUEST

DCR number 448	Changes required for: General	Originator: Steve Thacker - ESCC
Date: 2008/11/26	Date sent: 2008/11/26	Organisation: ESA/ESTEC
Status: IMPLEMENTED		

Title:	REP001		
Number:	REP001	Issue:	6

Other documents affected:

REP002-5

Page:

Class of Publication: 75 - Advanced CMOS 54ACseries
Pages 92 93 94

Paragraph:

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Pages 92 93 94

Original wording:

Proposed wording:

Remove from REP001 54 Integrated Circuit Detail Specifications (9***/*** under ESCC 9000), as listed below, for 54AC series components (to be transferred to REP002).

Specifications to be retired by this DCR:
Spec No / Spec Issue / Description

9201/134	1	Advanced CMOS Quad 2-Input NOR Gates, based on type 54ACT02
9201/137	1	Advanced CMOS Dual 4-Input NAND Gates, based on type 54AC20
9201/140	1	Advanced CMOS Quad 2-Input NOR Gates, based on type 54AC02
9203/067	1	Advanced CMOS Hex D-Type Edge Triggered Flip-Flops with Clear, based on type 54AC174
9203/068	1	Advanced CMOS Dual D-Type Positive Edge-Triggered Flip-Flops with Preset and Clear, based on type 54AC74
9203/069	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54AC374
9203/074	1	Advanced CMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54ACT373
9203/075	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54AC574
9203/076	1	Advanced CMOS Octal D-Type Flip-Flops with Clock Enable, based on type 54AC377
9203/077	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with Clear, based on type 54AC273
9203/078	1	Advanced CMOS Quad D-Type Edge-Triggered Flip-Flops with Clear, based on type 54AC175
9203/079	1	Advanced CMOS Dual D-Type Positive Edge-Triggered Flip-Flops with Preset and Clear, based on type

54ACT74
9203/080 1 Advanced CMOS Dual J-K Positive Edge-Tiggered Flip-Flops with Preset and Clear, based on type 54ACT109
9203/081 1 Advanced CMOS Hex D-Type Edge-Triggered Flip-Flops with Clear, based on type 54ACT174
9203/082 1 Advanced CMOS Quad D-Type Edge-Triggered Flip-Flops, based on type 54ACT175
9203/083 1 Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with Clear, based on type 54ACT273
9203/084 1 Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54ACT574
9203/085 1 Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54ACT374
9203/086 1 Advanced CMOS Dual J-K Positive Edge-Triggered Flip-Flops with Preset and Clear, based on type 54AC109
9203/087 1 Advanced CMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54AC573
9203/088 1 Advanced CMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54AC373
9203/091 1 Advanced CMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54ACT573
9204/075 1 Advanced CMOS Synchronous 4-Bit Binary Counter, based on type 54ACT163
9204/081 1 Advanced CMOS Synchronous 4-Bit Binary Counter with Direct Clear, based on type 54AC161
9204/082 1 Advanced CMOS Synchronous 4-Bit Binary Counter, based on type 54AC163
9204/083 1 Advanced CMOS Synchronous 4-Bit Up/Down Binary Counter, based on type 54AC169
9204/085 1 Advanced CMOS Synchronous 4-Bit Binary Counter with Direct CLear, based on type 54ACT161
9204/087 1 Advanced CMOS Synchronous Presettable 4-Bit Decade Counter with Direct Clear, based on type 54ACT160
9209/006 1 Advanced CMOS 8-Bit Identity Comparator, based on type 54AC521
9306/056 1 Advanced CMOS 8-Input Universal Shift/Storage Registers with Common Parallel I/O Pins Direct Clear and 3-State Outputs, based on type 54AC299
9306/057 1 Advanced CMOS 8-Input Universal Shift/Storage Registers with Common Parallel I/O Inputs Direct Clear and 3-State Outputs, based on type 54ACT299
9401/042 1 Advanced CMOS Octal Bus Buffer with 3-State Outputs, based on type 54AC240
9401/043 1 Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54AC244
9401/050 1 Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54ACT244
9401/051 1 Advanced CMOS Hex Inverter, based on type 54AC04
9401/053 1 Advanced CMOS Hex Inverter Schmitt Trigger Inverters, based on type 54ACT14
9401/056 1 Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54ACT541
9401/057 1 Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54ACT240
9401/058 1 Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54AC541
9401/059 1 Advanced CMOS Hex Inverters, based on type 54ACT04
9401/061 1 Advanced CMOS Quad Bus Buffers with 3-State Outputs, based on type 54AC125
9401/062 1 Advanced CMOS Octal Bus Buffers with Inverted 3-State Outputs, based on type 54AC540
9405/015 1 Advanced CMOS Octal Bus Transceivers with 3- State Outputs, based on type 54ACT245
9405/017 1 Advanced CMOS Octal Bus Transceivers with 3-State Outputs, based on type 54AC245
9408/061 1 Advanced CMOS Dual 2-Line to 4-Line Decoders/Demultiplexers with Inverted Outputs, based on type 54ACT139
9408/062 1 Advanced CMOS Quad 2-Line to 1-Line Data Selectors/ Multiplexers with 3-State Outputs, based on type

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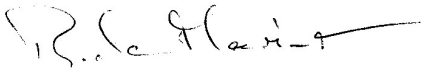
54ACT257
9408/066 1 Advanced CMOS Quad 2- Line to 1-Line Data Selectors/ Multiplexers with Inverted Outputs, based on type 54ACT158
9408/067 1 Advanced CMOS 8-Line to 1-Line Data Selectors/Multiplexers, based on type 54AC151
9408/068 1 Advanced CMOS Dual 4-Line to 1-Line Data Selectors/ Multiplexers, based on type 54AC153
9408/069 1 Advanced CMOS Quad 2-Line to 1-Line Data Selectors/ Multiplexers, based on type 54AC157
9408/070 1 Advanced CMOS 4-Line to 1-Line Data Selectors/Multiplexers with 3-State Outputs, based on type 54AC253
9408/071 1 Advanced CMOS Quad 2-Line to-1 Line Data Selectors/ Multiplexers with 3-State Outputs, based on type 54AC257
9408/074 1 Advanced CMOS Quad 2-Line to 1-Line Data Selectors/ Multiplexers with Inverted Ouputs, based on type 54AC158
9409/008 1 Advanced CMOS Hex Schmitt Trigger Inverters, based on type 54AC14

Justification:

The above listed specifications are no longer supported for new procurement against ESCC 9000 by any known manufacturer and as such are considered discontinued and shall be retired to REP002.

Note - STMicroelectrocis(F) does support a range of 14 ESCC specifications for 54AC series components as follows (which are not retired):

9201/125 Advanced CMOS Quad 2-Input NAND Gates, based on type 54AC00
9201/126 Advanced CMOS Quad 2-Input AND Gates, based on type 54AC08
9201/127 Advanced CMOS Quad 2-Input OR Gates, based on type 54AC32
9201/128 Advanced CMOS Quad 2-Input NAND Gates, based on type 54ACT00
9201/131 Advanced CMOS Quad 2-Input and Gates, based on type 54ACT08
9201/135 Advanced CMOS Quad 2-Input OR Gates, based on type 54ACT32
9201/136 Advanced CMOS Quad 2-Input Exclusive-OR Gates, based on type 54AC86
9201/138 Advanced CMOS Triple 3-Input AND Gates, based on type 54AC11
9201/139 Advanced CMOS Triple 3-Input NAND Gates, based on type 54AC10
9201/142 Advanced CMOS Triple 3-Input AND Gates, based on type 54ACT11
9201/143 Advanced CMOS Quad 2-Input Exclusive-OR Gates, based on type 54ACT86
9205/024 Advanced CMOS Dual 2-Line to 4-Line Decoders/Demultiplexers with Inverted Outputs, based on type 54AC139
9408/060 Advanced CMOS 3 to 8 Line Decoders/Demultiplexers with Inverted Outputs, based on type 54ACT138
9408/063 Advanced CMOS 3 to 8 Line Decoders/Demultiplexers with Inverted Outputs, based on type 54AC138

Attachments:
N/A
Modifications:
After discussion with ST/F on the subject of the 54AC series the 14 existing ESCC Details specs are not actually supported by ST/F for procurement (see 'Justification' section in DCR448). Accordingly they should be added to the list of 54AC specs to be retired by DCR448 i.e. a total of 68 54AC detail specs are now included on this DCR.
Approval signature:

Date signed:
2008-11-26