



DOCUMENT CHANGE REQUEST

DCR number		633		Changes required for: N/A		Originator: Steve Thacker - ESCC	
Date: 2010/11/25				Date sent: 2010/11/25		Organisation: ESA/ESTEC	
Status: IMPLEMENTED							
Title:		Integrated Circuits Silicon Monolithic CMOS Gate Array/Embedded Array , based on Type MH1RT					
Number:		9202/076		Issue:		3	
Other documents affected:							
Page:							
New Paras 1.7.6 & 1.7.7							
Paragraph:							
New Paras 1.7.6 & 1.7.7							
Original wording:							
Proposed wording:							
The drawings for the new LGA-349 & LGA-472 packages introduced in DCR615 are not suitable for inserting into this ESCC Detail specification. Simplified versions of both drawings including the dimension tables and notes, as per the attachment below, shall be used (replacing the drawings in DCR615).							
Justification:							
Original drawings for the LGA-349 & LGA-472 packages, in DCR615, were unsuitable for use.							
Attachments:							
N/A							
Modifications:							
N/A							
Approval signature:							
Date signed:							
2010-11-25							