



DOCUMENT CHANGE REQUEST

DCR number 74 Changes required for: Qualification
Date: 2008/01/11 Date sent: 2008/01/11
Status: IMPLEMENTED

Originator: Marielle BELASIC
Organisation: CNES

Title: Transistors Low Power NPN, based on type 2N2484

Number: 5201/001 Issue: 1

Other documents affected:

Page:

See attached file

Paragraph:

See attached file

Original wording:

Proposed wording:

See attached file

Justification:

During transfer from 4"(STMicroelectronics Catania) to 5" (ST Tours) wafers, some diffusion lines have not been reconducted due to the low market needs. But in order to avoid any devices obsolescence for space market. ST Rennes has conducted a qualification on the S111 line in 5" wafer which replace 1250 line 4" wafer. All the tests parameters are in full compliance with the detail specification except two AC parameters (high frequency current gain1 ; input capacitance). A PCN has been raised by STMicroelectronics to advise their customers and they didn't receive any comments concerning these electrical performances changes.

Attachments:
N/A
Modifications:
N/A
Approval signature:

Date signed:
2008-01-11