



DOCUMENT CHANGE REQUEST

DCR number	872	Changes required for:	General	Originator:	Steve Jeffery
Date:	2014/10/06	Date sent:	2014/07/02	Organisation:	ESCC Executive
Status:	IMPLEMENTED				

Title:	Integrated Circuits Silicon Monolithic CMOS Gate Array/Embedded Array , based on Type MH1RT		
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Number:	9202/076	Issue:	5
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Other documents affected:

Page:

17, 18, 21

Paragraph:

1.7.4, 1.7.5, 1.7.7

Original wording:

For some reason, during the update of this Specification from Issue 4 to 5 (DCR 697 refers), the package outlines in the Paras. given above were all deleted and incorrect package outline drawings inserted in their place.

Proposed wording:

Re-instate the three relevant (i.e .correct) drawings which can be found in Paras. 1.7.4, 1.7.5 and 1.7.7 of the archived specification 9202/076 Issue 4.

Justification:

N/A

Attachments:

N/A

Modifications:

N/A

Approval signature:

Date signed:

2014-10-06