DOCUMENT CHANGE REQUEST

DCR number 127	Changes required for: General	Originator: S Thacker
Date: 2004/06/29	Date sent: 2004/06/29	Organisation: ESA/ESTEC
Status: IMPLEMENTED		

Title:	CMOS Quad 3-State NAND R/S Latch, based on type 4044B		
Number:	9202/043	Issue:	1

Other documents affected:

Page:

FIGURE 3(c)- CIRCUIT SCHEMATIC (EACH LATCH) (Page 14)

Paragraph:

FIGURE 3(c)- CIRCUIT SCHEMATIC (EACH LATCH) (Page 14)

Original wording:

Proposed wording:

Figure is replaced by corrected figure as per attachment.

Justification:

Original figure is not a NAND R/S Latch as drawn.

Attachments:

DCR_9202043_details.jpg, null

Modifications:

N/A

Approval signature:



Date signed:

2004-06-29