DOCUMENT CHANGE REQUEST

DCR number 1220	Changes required for: General	Originator: Steve Jeffery
Date: 2019/05/14	Date sent: 2019/01/09	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title:	Matched Dual Transistors NPN, based on types 2N2919/2N2920 and 2N2920A		
Number:	5207/002	Issue:	9

Other documents affected:

5207/005-6

Page:

See the attached MSWord docs 5207/002 Draft 9A & 5207/005 Draft 7A.

Paragraph:

See the attached MSWord docs 5207/002 Draft 9A & 5207/005 Draft 7A.

Original wording:

Per 5207/002 Issue 8 & 5207/005 Issue 6.

Proposed wording:

As highlighted in 5207/002 Draft 9A & 5207/005 Draft 7A (see attached.)

Justification:

To introduce a number of editorial corrections to both specs, as shown in the attached draft updated specs. Also, for 5207/002, to introduce the required Terminal Strength test conditions for Variants in the Flat Package, as these have been omitted from Para. 2.3 of the spec since Issue 5 (DCR 866 refers.)

Attachments:

escc5207002draft9a_for_dcr_review.docx, escc5207005draft7a_for_dcr_review.docx

Modifications:

N/A

Approval signature:



Date signed:

2019-05-14