



DOCUMENT CHANGE REQUEST

DCR number		1254		Changes required for: Proc		Originator: MAILLARD	
Date: 2020/04/21				Date sent: 2019/03/14		Organisation: RAKON FRANCE	
Status: IMPLEMENTED							
Title:	CRYSTAL OSCILLATORS, CLASS 2, 4MHz TO 100MHz, AHCMOS COMPATIBLE OUTPUT, RAD-						
Number:	3503/001		Issue:	1			
Other documents affected:							
Page:							
-							
Paragraph:							
-							
Original wording:							
-							
Proposed wording:							
-							
Justification:							
-							



## DOCUMENT CHANGE REQUEST

DCR number	1254	Changes required for:	Proc	Originator:	MAILLARD
Date:	2020/04/21	Date sent:	2019/03/14	Organisation:	RAKON FRANCE
Status:	IMPLEMENTED				

Title:	CRYSTAL OSCILLATORS, CLASS 2, 4MHz TO 100MHz, AHCMOS COMPATIBLE OUTPUT, RAD-		
--------	--	--	--

Number:	3503/001	Issue:	1
---------	----------	--------	---

Other documents affected:

Page:

7

Paragraph:

1.5

Original wording:

In the table:


- Operating temperature range (Top) : -55 to +125°C
- Storage temperature range (Tstg) : -55 to +125°C

Proposed wording:

Operating temperature range (Top) : -55 to +110°C  
Storage temperature range (Tstg) : -55 to +110°C

Justification:

The below justification is based on RK135 evaluation and then qualification test results.  
Due to design limitations especially the use of a certain type of glue, the hybrid cannot be exposed to a temperature higher than +110°C either during operation or storage. Considering that the user has to consider the derating requirements of ECSS-Q-ST-30-11C in the application, the maximum temperature will in any case remain lower than +110°C. This temperature limitation will be transparent with any consequence for the users.

Attachments:
escc3503001iss2_for_publishing.docx
Modifications:
<p>All other references in the specification to the operating temperature of +125C shall also be changed to be +110C i.e. in Para 2.3.2 High and Low Temperature Electrical Measurements.</p> <p>The Rise Time and Fall Time limits in the tables Paras 2.3.1, 2.3.2, 2.5, 2.9.2 (for fNom: 16MHz to 100MHz) are changed from 5ns to 7ns.</p> <p>Note: for these tests in the table in Para 2.3.2, the test at 125C is removed and the test at 105C is changed to be at 110C.</p> <p>Note: several additional minor editorial changes have been implemented in this issue including the addition of ESCC specification hyperlinks &amp; internal cross-references.</p>
Approval signature:

Date signed:
2020-04-21