



DOCUMENT CHANGE REQUEST

DCR number	1270	Changes required for:	General	Originator:	André Uguen
Date:	2020/08/17	Date sent:	2019/05/09	Organisation:	STMicroelectronics
Status:	IMPLEMENTED				

Title: CMOS Hex Buffer/Converter Inverting, based on type 4049UB

Number:	9202/045	Issue:	5
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Other documents affected:

9202/046-5

Page:

Page 6

Paragraph:

1.5 Maximum rating

Original wording:

Input voltage (Vin) from -0.5V to VDD+0.5V

Proposed wording:

Input Voltage (Vin) from -0.5V to 18.5V

Justification:

this rating was present in the ESA SCC spec and was wrongly aligned with all the other device of the family. These two devices (HCC4049 and HCC4050) are specific as no protection diodes to VDD are implemented at the inputs

Attachments:

9202045_1.pdf, 9202046_1.pdf

Modifications:

N/A

Approval signature:

Date signed:

2020-08-17