



DOCUMENT CHANGE REQUEST

DCR number 1419 Changes required for: General

Date: 2021/05/31

Date sent: 2021/04/09

Originator: Valeries Lepaludier

Organisation: Atmel Microchip

Status: IMPLEMENTED

Title: INTEGRATED CIRCUITS, SILICON MONOLITHIC, CMOS, CELL-BASED ARRAY BASED ON TYPE

Number: 9202/083

Issue: 3

Other documents affected:

Page:

5 and 11 + minor corrections

Paragraph:

1.4.2 and 1.7

Original wording:

list of variant starting at 1

Proposed wording:

list of variant starting at 75 and adding pre-qualified IPs list

Justification:

remove all multi-decks packages option, only flat-substrate package available.

Attachments:

esc9202083iss4_draft_c_for_review.docx, esc9202083iss4_draft.docx

Modifications:

The original Draft Spec attachment is modified and accepted as follows:

- Title is re-worded (for clarification).
- Para. 1.5 is modified with editorial changes for improved readability and homogeneity with other specs.
- The new Para. 1.7 is re-worded for clarity and conciseness.
- Para. 2.4: The IIL and IIH Drift and Absolute Values for LVDS Buffers are re-instated (deleted by mistake in the original Draft).

Approval signature:

Date signed:

2021-05-31