DOCUMENT CHANGE REQUEST

DCR number 1423	Changes required for: General	Originator: Steve Jeffery	
Date: 2021/07/02	Date sent: 2021/04/22	Organisation: ESCC Executive	
Status: IMPLEMENTED			

Title:	IC, Silicon Monolithic, Radiation-Hardened 32-BIT ARM® CORTEX®-M7 Microcontroller based on		
Number:	9512/006	Issue:	1

Other documents affected:

Page:

All.

Paragraph:

Various.

Original wording:

See Issue 1.

Proposed wording:

Some editorial-only changes are proposed, including:

- Page layout and formatting changes.
- Editorial changes to Para. 1.5 for improved clarity and conciseness; The Storage Temperature Range Operating Condition, plus associated Note 4, are removed.
- The CQFP-256 package outline (in Para. 1.7) is made larger and therefore easier to read.
- Most of the diagrams included in Para. 1.10 are made larger and therefore easier to read.
- The font/size for note ref. 20 in Para. 2.3.1 is corrected.

See attached Draft 2B of ESCC 9512/006.

Justification:

This Detail Spec requires various editorial and formatting changes to be made to it so that it is in-line with the standard format and layout of other, long-standing, ESCC-format Detail Specs.

Attachments:
escc9512006_draft_2b_for_dcr_review.docx
Modifications:
DCR discussed through mail and agreement reached on escc9512006_draft_2b_dcr_review.doc
Approval signature:
Date signed:
2021-07-02