

DOCUMENT CHANGE REQUEST

1530 DCR number Changes required for: General Originator: Steve Thacker Date: 2023/09/12 Date sent: 2022/10/04 Organisation: ESCC Executive Secretariat Status: IMPLEMENTED Capacitors Fixed Chips Ceramic Dielectric Type I, based on type 0805 Title: Number: 3009/003 Issue: Other documents affected: 3009/005-7 Page: 7 Paragraph: 1.4.2 Note 1 Original wording: as per 3009/003 issue 8 & 3009/005 issue 7 Proposed wording: Amend the range of components listed in Para. 1.4.2 Note 1 as follows: For 3009/003: Amend 1.4.2 Note 1 as follows, including for UR = 50V a change to the maximum capacitance value (*); see attached specification mark-up for details: For UR = 200V: amend the capacitance value range to be 1 to 680pF For UR = 100V: amend the capacitance value range to be 1 to 1500pF For UR = 50V: amend the capacitance value range to be 1 to 2200pF(*) (was 1500pF) For UR = 25V: amend the capacitance value range to be 1 to 2200pF For UR = 16V: amend the capacitance value range to be 1 to 2700pF Modify the text under Note 1 to be as follows: Available Capacitance values for each Rated Voltage are as defined by the E12 to E96 series; however, any capacitance value within each specified capacitance range may be available on request. Capacitance tolerances available are: for Cn < 10pf: 0.25pF, 0.5pF • for Cn 10pf: 1% 2% 5% 10% For 3009/005: Amend 1.4.2 Note 1 as follows, including for UR = 100V a change to the maximum capacitance value (*); see attached specification mark-up for details:

For UR = 200V: amend the capacitance value range to be 100 to 10000pF



Date signed:

2023-09-12

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