DOCUMENT CHANGE REQUEST

DCR number 1534	Changes required for: General	Originator: Steve Thacker
Date: 2023/09/14	Date sent: 2022/11/10	Organisation: ESCC Executive Secretariat
Status: IMPLEMENTED		

Title:	Capacitors Fixed Chips Ceramic Dielectric Type I, based on type 0805		
Number:	3009/003	Issue:	9

Other documents affected:

3009/004-8, 3009/005-8, 3009/006-8, 3009/008-8, 3009/009-8, 3009/010-7, 3009/011-7, 3009/022-8, 3009/023-8, 3009/037-5, 3009/038-6, 3009/039-5, 3009/040-5, 3009/042-5, 3009/043-4

Page:

See attached mark-up draft specs

Paragraph:

Para. 1.4.2 & the Exxelia Appendix

Original wording:

As per current spec issue as listed

Proposed wording:

All the listed ESCC 3009/xxx Detail specs, as supported by Exxelia, are to be amended as follows; see attached spec mark-ups for full details:

1) Para. 1.4.2 Component Type Variants and Range of Components
Amend the table in Note 1 to simply list the minimum and maximum Capacitance values for each Rated Voltage selection. i.e., remove the tolerance and value series columns/tables (as applicable).

Amend the text under the table in Note 1 to read as follows (as applicable to the tolerances currently specified):
either:
....
Available Capacitance values for each Rated Voltage are as defined by the E12 to E96 series; however, any capacitance value within each specified capacitance range may be available on request.
Capacitance tolerances available are:
• for Cn < 10pf: 0.25pF, 0.5pF
• for Cn 10pf: 1% 2% 5% 10%
....
or:
....
Available Capacitance values for each Rated Voltage are as defined by the E6 to E24 series; however, any capacitance value within each specified capacitance range may be available on request.
Capacitance tolerances available are: 5% 10% 20%.

.....
2) Add a new Appendix for Manufacturer Exxelia to add a deviation to the requirements for Chart F3 Burn-in (ref. Para. 2.1.1) as follows:
...
Burn-in:

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Due to the use of components of varying designs to cover the available range of components as listed in REP005 (ESCC QPL), for any particular component, the Applied Voltage for Burn-in shall be as specified in the PID as agreed with the ESCC Executive, with a minimum value of 2 times the Rated Voltage (DC).
...
.....
3) For the following specific ESCC Detail specs, additional amendments apply:
3009/039 Para. 1.4.2: remove note 4 from Variants 25 & 26 (correction of typographic error).

3009/040 Para. 1.4.2: amend minimum Capacitance values for 25V & 16V Variants 01, 02, 03, 07, 08, 09 to be 1pF (was 10pF) (to be consistent with the ESCC QPL)
.....

Justification:

This DCR is raised on behalf of Exxelia.

Item 1: allows flexibility to the Manufacturers to provide any available tolerance to any Capacitance value in the range.

Item 2: Exxelia have advised that for some components in the ESCC QPL specified ranges, the currently specified, marked and ordered Rated Voltage, UR, is not always the actual maximum voltage that particular component has been design to withstand.
Accordingly, this deviation is defined to let the ESCC Executive approved Manufacturer's PID define the actual voltage applied for Burn-in in Chart F3.

Item 3:
Corrects a typographic error.
Makes the specified range consistent with the available range in the ESCCQPL.

Note: this DCR replaces DCR1523 raised by Exxelia which has been withdrawn due to only being raised, in error, on a single Detail spec 3009/003.

Attachments:

escc3009010iss_draft_7c_in_review.docx, escc3009038iss_draft_6c_in_review.docx, escc3009005iss_draft_8c_in_review.docx, escc3009006iss_draft_8b_in_review.docx, escc3009043iss_draft_5c_in_review.docx, escc3009037iss_draft_5b_in_review.docx, escc3009003iss_draft_9d_in_review.docx, escc3009008iss_draft_8c_in_review.docx, escc3009022iss_draft_8b_in_review.docx, escc3009023iss_draft_8d_in_review.docx, escc3009009iss_draft_8c_in_review.docx, escc3009040iss_draft_5c_in_review.docx, escc3009039iss_draft_5d_in_review.docx, escc3009042iss_draft_5b_in_review.docx,

Modifications:

N/A

Approval signature:

Date signed:

2023-09-14