DOCUMENT CHANGE REQUEST

DCR number 161

Changes required for: General

Originator: S Jeffery

Date: 2005/03/08

Date sent: 2005/03/08

Organisation: ESA/ESTEC

Status: IMPLEMENTED

Title:	CMOS Strobed Hex Inverter/Buffer, based on type 4502B		
Number:	9401/006	Issue:	1

Other documents affected:

Page:

Figure 4(b)- Quiescent Current Test Table, page 35.

Paragraph:

Figure 4(b)- Quiescent Current Test Table, page 35.

Original wording:

Proposed wording:

Change Pin Number 12 for Pattern Number 7 from 1 to 0. NB: as part of the conversion of this specification to ESCC format, this Figure will be incorporated into a note as shown in the attachment.

Justification:

To correct error in the specification.

Attachments:

Attachment_to_DCR_for_9401006.pdf, null

Modifications:

N/A

Approval signature:

Date signed:

2005-03-08

3. Quiescent Current shall be tested using the following input conditions:
   - (a) All Inputs = V_{IL}
   - (b) Inputs OD = INH = V_{IL} ; all other inputs = V_{IH}
   - (c) Input INH = V_{IH} ; all other inputs = V_{IL}.
   - (d) Input OD = V_{IL} ; all other inputs = V_{IH}.
   - (e) Input OD = V_{IH} ; all other inputs = V_{IL}.
   - (f) Inputs OD = INH = V_{IH} ; all other inputs = V_{IL}.
   - (g) Input INH = V_{IL} ; all other inputs = V_{IH}.
   - (h) All inputs = V_{IH}.