



DOCUMENT CHANGE REQUEST

DCR number		1633		Changes required for: General		Originator: Steve Jeffery	
Date: 2024/03/04				Date sent: 2024/01/30		Organisation: ESCC Executive	
Status: IMPLEMENTED							
Title:		INTEGRATED CIRCUITS, SILICON MONOLITHIC, CMOS DIGITAL, MEMORY, RADIATION					
Number:		9304/010		Issue:		2	
Other documents affected:							
Page:							
1, 5, 33, 37							
Paragraph:							
Title Page & Paras. 1.4.1, 1.4.2, 2.3.1, 2.3.2, 2.3.3							
Original wording:							
See ESCC 9304/010 Issue 2.							
Proposed wording:							
See attached mark-up "ESCC9304010Iss3 Draft A for DCR review.docx". Changes are highlighted in yellow.							
Justification:							
Various corrections and updates are required for proper component definition, as defined and requested by STMicroelectronics and NanoXplore.							
Attachments:							
esc9304010iss3_draft_a_for_dcr_review.docx							
Modifications:							
N/A							
Approval signature:							
Date signed:							
2024-03-04							