



DOCUMENT CHANGE REQUEST

DCR number		1635		Changes required for: General		Originator: Steve Jeffery	
Date: 2024/04/11				Date sent: 2024/01/30		Organisation: ESCC Executive	
Status: IMPLEMENTED							
Title:	INTEGRATED CIRCUITS, SILICON MONOLITHIC, CMOS RADIATION HARDENED 65nm ASIC						
Number:	9202/086		Issue:	1			
Other documents affected:							
Page:							
5, 6, 11							
Paragraph:							
Paras. 1.4.1, 1.4.2, 1.5, 1.7							
Original wording:							
See ESCC 9202/086 Issue 1.							
Proposed wording:							
See attached mark-up ESCC9202086Iss2 Draft A for DCR review.docx. Changes are highlighted in yellow (changes as a result of the proposed update of ESCC 9304/010 to Issue 3) and cyan (changes covering the addition of Variant 03 and reflecting the new Detail Spec 9304/011 [C.A. No. 340] currently in progress for NG-LARGE)							
Justification:							
-							
Attachments:							
esc9202086iss2_draft_a_for_dcr_review.docx							
Modifications:							
This DCR is considered to be acceptable with the following changes:							
Page: 5							
Paragraph: 1.4.1, 1.4.2							
Proposed wording: See MSWord file "ESCC9202086Iss2 Draft B.docx". The changes, highlighted in yellow, harmonise this Detail Spec with the changes incorporated in ESCC 9304/010 Issue 3 via DCR1633 (implemented in March 2024).							
Approval signature:							
Date signed:							

