




DOCUMENT CHANGE REQUEST

DCR number		1661		Changes required for: General		Originator: Steve Jeffery	
Date: 2024/05/21				Date sent: 2024/04/19		Organisation: ESCC Executive	
Status: IMPLEMENTED							
Title:		Capacitor Filters C-Type Feedthrough Electromagnetic Interference Suppression Hermetically					
Number:		3008/020		Issue:		4	
Other documents affected:							
Page:							
7, 12, 15							
Paragraph:							
1.5, 2.6.2, Appendix A							
Original wording:							
<p>- Para. 1.5, Note 1: ... For Variants with UR = 100V at Tamb +85°C, derate linearly to 75V at Tamb = +125°C. For Variants with UR = 200V at Tamb +85°C, derate linearly to 150V at Tamb = +125°C.</p> <p>- Para. 2.6.2, Test Temperature tolerance is (+0 -5) / (+5 -0) °C.</p>							
Proposed wording:							
<p>The proposed changes to ESCC Detail Spec 3008/020 are as a result of recent discussion between the mfr Exxelia and the ESCC T.W.</p> <p>See the attached mark-up esc3008020iss5 DraftA for DCR review.docx (proposed changes are highlighted in yellow).</p> <p>(Note similar additional changes, detailed in the Comments to DCRs 1625 thru 1632, which were originally raised in order to convert the 8 other 3008/### specs to the ESCC format.)</p>							
Justification:							
<p>- The revision of the Voltage derating, as advised by Exxelia, is considered by the ESCC T.W. to be acceptable.</p> <p>- Harmonisation across all 3006/### Detail Specs and 3008/### Detail Specs of the tolerance margin for the high and low test temperatures defined in High and Low Temperatures Electrical Measurements, Burn-in and Operating Life.</p> <p>- For the added Deviation in Appendix A, the ESCC T.W. considers it acceptable for Exxelia to perform the Room Temperature Insertion Loss measurements during Screening (Chart F3) on a sample basis instead of 100%.</p>							

Attachments:
escc3008020iss5_drafta_for_dcr_review.docx
Modifications:
N/A
Approval signature:

Date signed:
2024-05-21