DOCUMENT CHANGE REQUEST

DCR number 1671	Changes required for: General	Originator: Steve Thacker
Date: 2024/07/23	Date sent: 2024/07/03	Organisation: ESCC Executive Secretariat
Status: IMPLEMENTED		

Title:	Capacitors Leadless Surface Mounted Tantalum Solid Electrolyte Enclosed Anode Connection,		
Number:	3012/001	Issue:	10

Other documents affected:

3012/004-7, 3012/006-5

Page:

Various; see attached for details

Paragraph:

Various; see below & attached for details

Original wording:

As per current spec issue above

Proposed wording:

Amend the specifications as follows:

A) ESCC 3012/006:
Expand the range of components covered by this specification to 15uF to 680uf (4V to 50V); see Paras 1.4.1.1, 1.4.2 & 1.5 in attached spec mark-up for details (i.e. yellow highlights)
...
In Para. 2.4 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS; see attached spec mark-up for details:
Due to the change in Capacitance value & Equivalent Series Resistance permitted by the mounting step of Chart F4, the room temperature measurements for High and Low Temperature Stability (Step 1, 3, 6) and Surge Voltage are amended to take the specified +/-5% permitted change of Capacitance & x1.25 factor change of ESR into account.
i.e. The absolute Capacitance maximum limit per Para 2.3.1 is removed and replaced by a capacitance change limit referred to each test's initial measurements (post-mounting) by up to an additional +/-5%; ESR limit is amended to reflect Note 2.

All initial measurements are amended to require values to be recorded if tested (editorial change only)

Note 3 is amended to allow the option for post-mounting measurements to be used as Step 1 measurements for High and Low Temperature Stability as well as initial measurements for all other tests
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B) ESCC 3012/001 & 3012/004:
Para. 2.4 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS; ; see attached spec mark-ups for details (i.e. yellow highlights):
Due to the change in Capacitance value & Equivalent Series Resistance (3012/004 only) permitted by the mounting step of

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Chart F4, the room temperature measurements for High and Low Temperature Stability (Step 1, 3, 6) and Surge Voltage are amended to take the specified +/-5% permitted change of Capacitance & x1.25 factor change of ESR (3012/004 only) into account.
i.e. The absolute Capacitance maximum limit per Para 2.3.1 is removed and replaced by a capacitance change limit referred to each test's initial measurements (post-mounting) by up to an additional +/-5%; ESR limit is amended to reflect Note 2 (3012/004 only).

Surge Voltage: An optional initial measurement is added.

All initial measurements are amended to require values to be recorded if tested (editorial change only)

Note 2 (3012/001) / 3 (3012/004) is amended to allow the option for post-mounting measurements to be used as Step 1 measurements for High and Low Temperature Stability as well as initial measurements for all other tests.

Justification:

This DCR is raised on behalf of KYOCERA AVX Components s.r.o.

AVX stated:
"The change results from requalification according to the ESCC 3012/006 standard, issue 5. Based on this, we would like to extend the qualified range to include new variants with lower and higher capacitance and rated voltage.

At the same time, we want to adjust the limit for High and Low Temperature Stability measurement at 22°C (Step 1, 3 and 6) ."

3012 /001 & /004 Para 2.4 are amended to be consistent with 3012/006 as all 3 specs are supported by AVX.

Attachments:

escc_3012001iss10_draft_a_in_review.docx, escc_3012004iss7_draft_a_in_review.docx, escc_3012006iss6_draft_a_in_review.docx

Modifications:

N/A

Approval signature:

Date signed:

2024-07-23