DOCUMENT CHANGE REQUEST

DCR number 1825	Changes required for: N/A	Originator: Khlifa Hadre
Date: 2026/09/10	Date sent: 2026/07/06	Organisation: ST Microelectronics
Status: IMPLEMENTED		

Title:	Integrated Circuits, Silicon Monolithic, HCMOS Dual D-Type Flip-Flop with Preset and Clear and		
Number:	9203/050	Issue:	5

Other documents affected:

Page:

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Paragraph:

1.9. PIN/PAD ASSIGNMENT

Original wording:

In ESSC Issue 5, the pin assignment:
• Pin 8 = 2Q Output
• Pin 9 = 2Q Output

Proposed wording:

The pin assignment:
• Pin 8 = 2Q Output
• Pin 9 = 2Q Output

Justification:

In Issue 2, the pin assignment is correct and consistent with the functional diagram:
• Pin 8 = 2Q Output
• Pin 9 = 2Q Output

In revision 5 of ESCC, the pin assignment is incorrect. It must be consistent the functional diagram in paragraph 1.8 (see attached screenshot). A typing error (swap between pins 8 and 9) was introduced into this revision

Attachments:
issuepinout_m54hc74_9203050.pdf
Modifications:
N/A
Approval signature:

Date signed:
2026-09-10