DOCUMENT CHANGE REQUEST

DCR number 691	Changes required for: General	Originator: Steve Jeffery
Date: 2012/09/14	Date sent: 2011/12/05	Organisation: ESCC Executive
Status: IMPLEMENTED		

Title:	Transistors Low Power PNP, based on type 2N3962		
Number:	5202/015	Issue:	2

Other documents affected:

Page:

6, 14.

Paragraph:

1.5 and 2.8

Original wording:

Maximum Ratings are revised, including the introduction of Thermal Resistance, Junction-to-Ambient and Thermal Resistance, Junction-to-Case and the removal of the Maximum Power Rating for CCP when mounted on a substrate.

Power Burn-in Test Conditions are revised.

Proposed wording:

For details of the changes see the attached mark-up (5202/015 Issue 3 Draft A).

Justification:

Improve the appearance, content and clarity of the spec.

Attachments:

5202015_issue_3_draft_a.pdf, null

Modifications:

N/A

Approval signature:



Date signed:

2012-09-14