DOCUMENT CHANGE REQUEST

DCR number 866	Changes required for: General	Originator: Aissa Nehdi
Date: 2014/11/19	Date sent: 2014/06/12	Organisation: STMicroelectronics
Status: IMPLEMENTED		

Title:	Matched Dual Transistors NPN, based on types 2N2919/2N2920 and 2N2920A		
Number:	5207/002	Issue:	4

Other documents affected:

Page:

Paragraph:

Original wording:

Proposed wording:

Justification:

Insert a new package (Flat Package 8 pin)

Attachments:

2n2920a.docx, 2n2920a_fp8.pdf

Modifications:

In addition to the changes detailed in the original DCR attachment, the following changes shall also be included in this DCR:

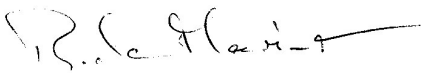
a) Para. 1.8 a), Materials and Finishes; Case
Amend 2nd sentence to read as follows:
"For the chip carrier package and the flat package, the case shall be hermetically sealed and have a ceramic body with a Kovar lid."

b) Para. 2.2 (a), Marking; Terminal Identification
Amend bullet to read as follows:

"(a) Terminal identification (CCP and FP-8 packages only)."

c) Paras 2.4.1, 2.4.2, 2.5 & 2.6, Electrical Measurements Tables
For all characteristics in all tables where the limit depends on the Variant number as listed in the Test Conditions column of each table (see below), add new variants 16 & 17 to the respective list (note - Variant 16 & 17 to be included in the same list as existing Variant 15).
i.e. applicable characteristics:
: Forward-Current Transfer Ratio
: Base-Emitter Voltage Differential

Approval signature:



Date signed:

2014-11-19