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RESISTOR, FIXED, CHIP, METAL FOIL

BASED ON TYPE SMR-PW, SMV-PW

ESCC Detail Specification No. 4001/028

Issue 2 May 2010





ISSUE 2

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DOCUMENTATION CHANGE NOTICE

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1. GENERAL

1.1 <u>SCOPE</u>

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. 4001.
- (b) A-QS-009, Isabellenhuette Test Procedure for Infrared Measurement.

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 <u>The ESCC Component Number</u>

The ESCC Component Number shall be constituted as follows:

Example: 4001028012R7D

- Detail Specification Reference: 4001028
- Component Type Variant Number: 01 (as required)
- Characteristic code: Resistance Value (2.7Ω): 2R7 (as required)
- Characteristic code: Resistance Tolerance (±0.5%): D (as required)

1.4.1.1 Characteristics and/or Ratings Codes

Characteristics and/or ratings to be codified as part of the ESCC Component Number shall be as follows:

(a) Resistance Value expressed by means of the following codes. The unit quantity shall be ohm (Ω) :

Resistance Value (Ω)	Code
0.00XX	R00XX
0.0XX	R0XX
0.XX	RXX
X.X	XRX

(b) Resistance Tolerance expressed by the following codes in accordance with ESCC Basic Specification No. 21700:



Tolerance (± %)	Code Letter
0.5	D
1	F

1.4.2 <u>Component Type Variants and Range of Components</u>

The component type variants and range of components applicable to this specification are as follows:

Variant Type	Туре	Style	Resistance Range R _n		Tolerance	Temperature	Value	Weight
Number		(Note 1)	Min (Ω)	Max (Ω)	(± %)	Coefficient TC (±10 ⁻⁶ /°C)	Series	max (g)
01	SMR- PW	4723 (2 Terminal)	0.010	4.7	0.5, 1	Note 2	E12	0.6
02	SMV- PW	4723 (4 Terminal)	0.0022	1.0	0.5, 1	Note 2	E12	0.6

NOTES:

1. See Physical Dimensions.

2.

Variant Number	Resistance (Ω)	Temperature Coefficient/ (10 ⁻⁶ /°C)	Remarks
01	$0.010 \le R_n \le 4.7$	-110, +0	over $T_{amb} = -55^{\circ}C$ to $+ 22^{\circ}C$
		±60	over $T_{amb} = +22^{\circ}C$ to $+140^{\circ}C$
		±50	over $T_{amb} = +22^{\circ}C$ to $+60^{\circ}C$
02	$0.0022 \le R_n \le 1.0$	-110, +0	over $T_{amb} = -55^{\circ}C$ to $+22^{\circ}C$
		±60	over $T_{amb} = +22^{\circ}C$ to $+140^{\circ}C$
	$0.0022 \le R_n < 0.010$	±50	over $T_{amb} = +22^{\circ}C$ to $+60^{\circ}C$
	$0.010 \le R_n \le 1.0$	±30	

1.5 <u>MAXIMUM RATINGS</u>

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Symbols	Limits	Units	Remarks
Rated Dissipation	P _n	3	W	Note 1
Rated Voltage	U _R	$\sqrt{(P_n \times R_n)}$	V	Note 2
Isolation Voltage	U _l	1000	V	-

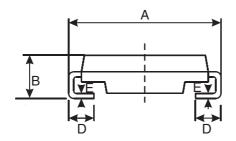


Characteristics	Symbols	Limits	Units	Remarks
Operating Temperature Range	T _{op}	-55 to +140	°C	T _{amb}
Storage Temperature Range	T _{stg}	-55 to +140	°C	-
Soldering Temperature	T _{sol}	+260	°C	Note 3

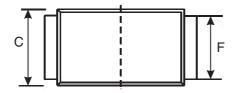
- At $T_{amb} \le +80^{\circ}$ C. For $T_{amb} > +80^{\circ}$ C derate linearly to 0W at $T_{amb} = +140^{\circ}$ C. $R_n =$ rated resistance. Duration 5 seconds maximum.

1.6 PHYSICAL DIMENSIONS

Variant 01 (2 Terminal Package) 1.6.1



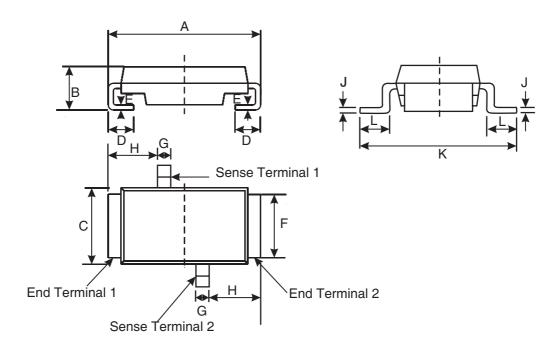




Symbols	Dimensions (mm)		Notes
	Min	Max	
Α	11.9	12.3	
В	3.2	3.6	
С	5.9	6.6	
D	1.5	2.5	both terminals
Е	0.3	0.5	both terminals
F	4.9	5.3	both terminals



1.6.2 <u>Variant 02 (4 Terminal Package)</u>



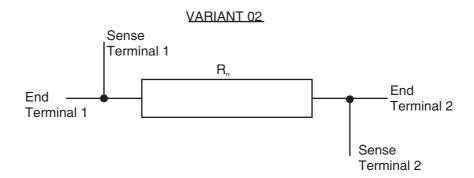
Symbols	Dimensions (mm)		Notes
	Min	Max	
Α	11.9	12.3	
В	3.2	3.6	
С	5.9	6.6	
D	1.5	2.5	both end terminals
E	0.3	0.5	both end terminals
F	4.9	5.3	both end terminals
G	0.8	1.3	both sense terminals
Н	4.3	4.9	
J	0.3	0.5	both sense terminals
K	8.8	9.6	
L	0.85	1.45	both sense terminals



1.7 FUNCTIONAL DIAGRAM







1.8 <u>MATERIALS AND FINISHES</u>

1.8.1 <u>Terminals</u>

The terminal finish shall be hot solder dip with Sn60 to a maximum thickness of 70µm.

2. REQUIREMENTS

2.1 GENERAL

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

2.1.1 Deviations from the Generic Specification

2.1.1.1 Deviations from Production Control (Chart F2)

(a) Prior to body moulding a 100% Infrared Measurement in accordance with A-QS-009 shall be performed on each resistor element: Applied Voltage: $\sqrt{(5P_n \times R_n)}$



Duration: 10ms

2.1.1.2 Deviations from Screening Tests (Chart F3)

- (a) Screening Sub-lot Samples Selection, Mounting and Testing
 A quantity of 20 components shall be selected at random from the production lot and mounted in
 accordance with ESCC Generic Specification No. 4001 Para. 8.7. These mounted Screening Sublot samples shall be serialised and subjected to tests and inspections of Chart F3 in parallel with the
 100% testing. For each test requiring electrical measurements the results shall be recorded against
 component serial number. These samples shall not form part of the delivery lot.
- (b) Para. 8.1 Overload Overload shall only be performed on the mounted Screening Sub-lot samples. Resistance shall be measured as specified in Room Temperature Electrical Measurements both before and after Overload. Change in Resistance shall be calculated, recorded against component serial number,
 - Change in Resistance during Overload: ±0.3% max
- (c) Para. 8.2, Non-Linearity: Not applicable.

and shall not exceed the limit as follows:

- (d) Para. 8.3.2, Room Temperature Electrical Measurements
 For the testing of the mounted Screening Sub-lot samples, Resistance shall be recorded against component serial number both before and after Burn-in. Change in Resistance shall be calculated, recorded against component serial number, and shall not exceed the limit as follows:
 Change in Resistance during Burn-in: ±0.2% max.
- (e) Para. 8.4 Burn-in:
 - For the 100% Testing during Chart F3, burn-in in accordance with No-Bias Burn-in of this specification shall be performed with a duration of 24 (+24, -0)h. For the testing of the mounted Screening Sub-lot samples, burn-in in accordance with Biased Burn-in of this specification shall be performed with a duration of 168 (+24 -0)h.
- (f) Para. 6.4 Check for Lot Failure
 0 failures are allowed during all Chart F3 testing of the mounted Screening Sub-lot samples. In the event of any failure the lot shall be considered as failed.

2.1.1.3 Deviations from Qualification and Periodic Tests (Chart F4)

(a) Para. 8.11, Robustness of Terminations: Not applicable.

2.1.1.4 Deviations from Data Documentation (Section 9)

(a) Para. 9.6, Screening Tests Data: For each test performed on the mounted Screening Sublot samples requiring electrical measurements, the results shall be recorded against component serial number. Component drift calculations shall be recorded for each specified test against component serial number.

2.2 MARKING

The marking of all components delivered to this specification shall be in accordance with the requirements of ESCC Basic Specification No. 21700. When the component is too small to accommodate all of the marking specified, as much as space permits shall be marked and the marking information, in full, shall accompany each component in its primary package.

The information to be marked and the order of precedence, shall be as follows:



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- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- (b) The ESCC Component Number.
- (c) Traceability information.

2.3 OVERLOAD

The test conditions for Overload, tested as specified in the ESCC Generic Specification, shall be as follows:

Voltage: $\sqrt{(5P_nxR_n)}$

Duration: 5s.

2.4 RESISTANCE TO SOLDERING HEAT

The test conditions for Resistance to Soldering Heat, tested as specified in the ESCC Generic Specification, shall be as follows:

Temperature: 260°C

Duration: 5(+0-1)s

2.5 <u>ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES</u>

2.5.1 Room Temperature Electrical Measurements

The measurements shall be performed at T_{amb} =+22 ±3°C.

Characteristics	Symbols	ESCC 4001 Test	Tolerance	Lin	Units	
		Method and Conditions	(± %)	Min	Max	
Resistance	R _A	Para. 8.3.1.1	0.5	0.995 R _n	1.005 R _n	Ω
			1	0.99 R _n	1.01 R _n	
Insulation Resistance	R _I	Para. 8.3.1.2 V=500V Note 1	All	1000	-	МΩ

NOTES:

1. Guaranteed but not tested during Screening Tests.

2.5.2 <u>High and Low Temperatures Electrical Measurements</u>

Characteristics	Symbols	ESCC 4001 Test Method and Conditions				Lin	nits	Units
				Min	Max			
Resistance Change between -55 (+3-0)°C and +22 ± 3°C	$\Delta R_A/R_A$	Para. 8.3.1.1 Note 1	$TC = -110 \times 10^{-6} / ^{\circ}C$	-0.88	+0	%		





Characteristics	Symbols	ESCC 4001 Test Method and Conditions		Limits		Units
				Min	Max	
Resistance Change between +140 (+0 -3)°C and +22 ± 3°C	$\Delta R_A/R_A$	Para. 8.3.1.1 Note 1	$TC = \pm 60 \times 10^{-6}/^{\circ}C$	-0.726	+0.726	%
Resistance Change between +60 ±3°C and +22 ± 3°C	$\Delta R_A/R_A$	Para. 8.3.1.1 Note 2	$TC = \pm 30 \times 10^{-6} / ^{\circ}C$ $TC = \pm 50 \times 10^{-6} / ^{\circ}C$	-0.132 -0.22	+0.132 +0.22	%

NOTES:

- 1. The measurements shall be performed on all the mounted Screening Sub-lot samples.
- 2. The measurements shall be performed on a sample of 5 components selected from the total production lot.

2.6 <u>INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS</u>

The components shall be mounted as specified in the ESCC Generic Specification.

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 $\pm 3^{\circ}$ C.

Unless otherwise specified the test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

Test Reference per	Characteristics	Symbols	Limits		Units
ESCC No. 4001			Min	Max	
Rapid Change of Temperature					
Initial Measurement	Resistance	R_A	Record Values		
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	±0.2		%
Vibration					
Initial Measurement	Resistance	R_A	Record	Values	
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	±0.2		%
Resistance to Soldering Heat					
Initial Measurement	Resistance	R_A	Record Values		
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	±0.3		%
Solderability					
Initial Measurement	Resistance	R_A	Record Values		
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	±0.1		%
Climatic Sequence					
Initial Measurements (Procedure 1)	Resistance (after drying)	R_{A}	Record Values		
Final Measurements	Change in Resistance	$\Delta R_A/R_A$	±0.3		%



Test Reference per	Characteristics	Symbols	Limits		Units	
ESCC No. 4001			Min	Max		
	Insulation Resistance	R _I	1000	-	MΩ	
Operating Life						
Initial Measurement (0 hour)	Resistance	R_A	Record Values			
Intermediate Measurements (1000 hours)	Change in Resistance	$\Delta R_A/R_A$	±0.2		%	
Final Measurements	Change in Resistance	$\Delta R_A/R_A$	±C).2	%	
(2000 hours)	Insulation Resistance	R_{l}	1000	-	$M\Omega$	

2.7 <u>BURN-IN CONDITIONS</u>

2.7.1 No-Bias Burn-in Conditions for 100% Testing

Characteristics	Symbols	Conditions (Note 1)	Units
Ambient Temperature	T _{amb}	+140(+0 -5)	°C
Test Voltage	V _T	No Bias	V

NOTES:

- 1. No-Bias Burn-in (High Temperature Bake) shall be performed on components that are not mounted.
- 2. No-Bias Burn-in duration shall be 24(+24 -0)h.
- 3. After Burn-in, the components shall be removed from the chamber and allowed to cool under normal atmospheric conditions for a minimum of 1 hour. They shall then be visually examined. There shall be no evidence of damage and marking shall still be legible.

2.7.2 <u>Biased Burn-in Conditions for Screening Sub-lot Samples</u>

Characteristics	Symbols	Conditions (Note 1)	Units
Terminal Temperature	T _{term}	+80(+0 -3)	°C
Test Voltage	V _T	$\sqrt{(P_n \times R_n)}$	V

NOTES:

- 1. Biased Burn-in shall be performed on the applicable sample components that are mounted in accordance with ESCC Generic Specification No. 4001 Para. 8.7.
- 2. Biased Burn-in duration shall be 168(+24 -0)h.
- 3. After Burn-in, the components shall be removed from the chamber and allowed to cool under normal atmospheric conditions for a minimum of 1 hour. They shall then be visually examined. There shall be no evidence of damage and marking shall still be legible.

2.8 OPERATING LIFE CONDITIONS

The conditions shall be as specified for Biased Burn-in.





APPENDIX A

AGREED DEVIATIONS FOR ISABELLENHUETTE (D)

Items Affected	Description of Deviations			
Deviations from Generic Specification:				
Production Control (Chart F2)	Para. 5.2.1, Dimension Check: a 100% inspection may be performed.			
Qualification and Periodic Tests (Chart F4)	Para. 8.8, Rapid Change of Temperature: a high temperature extreme of T_{amb} =+150(+0 -3) °C may be applied during testing. Para. 8.10, Climatic Sequence: Para. 8.10.5, Low Air Pressure: Not applicable Para. 8.10.7, DC Load: Not applicable Para. 8.15, Permanence of Marking: Not applicable			
Resistance Value Characteristic Code	Resistance Value shall be expressed by means of the following codes within the ESCC Component Number. The unit quantity shall be ohm (Ω)			
	Resistance Value (Ω)	Code		
	0.00XX R00XX 0.0XX R0XX 0.XXX RXXX			
	X.XX	XRXX		
Component Type Variants and Range of Components	For all Variants additional resistance values, R _n , from within the specified Resistance Range, but outside of the specified Value Series, are available upon request from Isabellenhuette.			
Marking of Tolerance on Resistance Value	The tolerance on resistance value may be marked using the actual numeric value (0.5% or 1%) instead of the specified code letter.			
Marking of Traceability Information	The manufacturing date code may be marked using a three-digit code where the first digit is a letter to indicate the year of manufacture in accordance with the 20-year cycle code per Table 10a of IEC Publication No. 60062, and the last two digits indicate the week of the year (i.e. 01 to 52) during which encapsulation or the final production process occurred. For example: W41 for week 41 of 2008			