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TRANSISTORS, MATCHED DUAL, PNP

BASED ON TYPE 2N3350

ESCC Detail Specification No. 5207/003

Issue 2 February 2008





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1. GENERAL

1.1 SCOPE

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. 5000
- (b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 <u>The ESCC Component Number</u>

The ESCC Component Number shall be constituted as follows:

Example: 520700302

Detail Specification Reference: 5207003

Component Type Variant Number: 02 (as required)

1.4.2 <u>Component Type Variants</u>

The component type variants applicable to this specification are as follows:

Variant No.	Based on Type	Case	Lead/Terminal Material and Finish	Weight max g
02	2N3350	TO-77	D3 or D4	0.95
03	2N3350	TO-77	D7	0.95
04	2N3350	CCP	2	0.2
05	2N3350	CCP	4	0.2

The lead/terminal material and finish shall be in accordance with the requirements of ESCC Basic Specification No. 23500.

1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and





when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Symbols	Maximum Ratings	Unit	Remarks
Collector-Base Voltage	V _{CBO}	-60	V	Over entire
Collector-Emitter Voltage	V _{CEO}	-45	V	operating temperature
Emitter-Base Voltage	V _{EBO}	-6	V	range
Collector Current	I _C	30	mA	Continuous
Power Dissipation (One Section)				At T _{amb} ≤ +25°C Note 1
For TO-77 and CCP	P _{totO1}	0.3	W	
For CCP	P _{totO2}	0.6 (Note 2)	W	
For TO-77	P _{totO3}	0.6	W	At T _{case} ≤ +25°C Note 1
Power Dissipation (Both Sections)				At T _{amb} ≤ +25°C Note 1
For TO-77 and CCP	P _{totB1}	0.6	W	
For CCP	P _{totB2}	1.2 (Note 2)	W	
For TO-77	P _{totB3}	1.2	W	At T _{case} ≤ +25°C Note 1
Operating Temperature Range	T _{op}	-55 to +200	°C	Note 3
Storage Temperature Range	T _{stg}	-65 to +200	°C	Note 3
Soldering Temperature For TO-77 For CCP	T _{sol}	+260 +245	°C	Note 4 Note 5

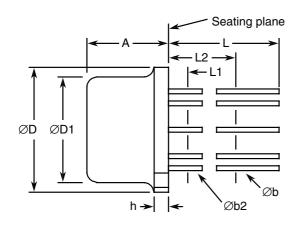
NOTES:

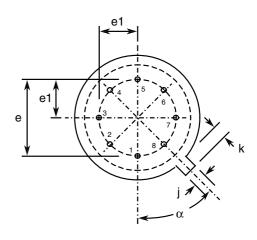
- 1. For T_{amb} or $T_{case} > +25$ °C, derate linearly to 0W at +200°C.
- 2. When mounted on a 15 x 15 x 0.6mm ceramic substrate.
- 3. For Variants with tin-lead plating or hot solder dip lead finish all testing performed at $T_{amb} > +125$ °C shall be carried out in a 100% inert atmosphere.
- 4. Duration 10 seconds maximum at a distance of not less than 1.5mm from the device body and the same lead shall not be resoldered until 3 minutes have elapsed.
- 5. Duration 5 seconds maximum and the same terminal shall not be resoldered until 3 minutes have elapsed.



1.6 PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION

1.6.1 <u>Metal Can Package (TO-77) - 6 lead</u>





Symbols	Dimension	Notes	
Symbols	Min	Max	Notes
A	6.1	6.6	
Øb	0.406	0.533	2, 3
Øb2	0.406	0.483	2, 3
ØD	8.51	9.4	
ØD1	7.75	8.51	
е	5.08	4	
e1	2.54	BSC	4
h	-	1.02	
j	0.711	0.864	
k	0.737	1.14	5
L	12.7	-	2
L1	-	1.27	3
L2	6.35	-	3
α	45° E	BSC	1, 4, 6

NOTES:

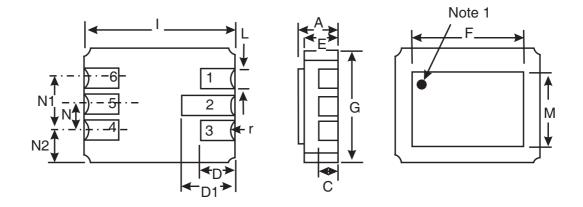
- 1. Terminal identification is specified by reference to the tab position where lead 1 = collector 1, lead 2 = base 1, lead 3 = emitter 1, lead 5 = emitter 2, lead 6 = base 2 and lead 7 = collector 2. Lead numbers 4 and 8 are not present on the actual package; they are shown in the drawing for information only.
- 2. Applies to all leads.
- 3. Øb2 applies between L1 and L2. Øb applies between L2 and 12.7mm from the seating plane. Diameter is uncontrolled within L1 and beyond 12.7mm from the seating plane.
- 4. Leads having maximum diameter 0.483mm measured in the gauging plane 1.37(+0.025, -0)mm below the seating plane of the device shall be within 0.178mm of their true position relative to a



maximum-width-tab.

- 5. Measured from the maximum diameter of the actual device.
- 6. Tab centreline.

1.6.2 Chip Carrier Package (CCP) - 6 terminal



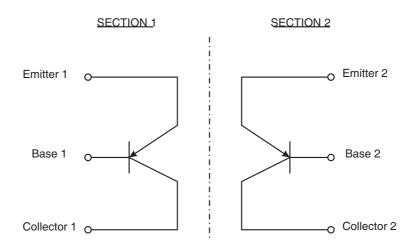
Symbols	Dimensio	Notes	
Symbols	Min	Max	Notes
Α	1.53	1.96	
С	0.89 TY	PICAL	2
D	1.52	1.78	
D1	2.08	2.49	
E	1.24	1.55	
F	5.76	5.92	
G	4.19	4.45	
I	6.1	6.35	
L	0.55	0.71	2
M	3.86	4.01	
N	1.14	1.4	
N1	2.41	2.67	
N2	0.89 TYPICAL		
r	0.23 TY	PICAL	2

NOTES:

- 1. Terminal identification is specified, when viewing the top side of the package, by reference to a black ink dot adjacent to terminal 1 = base 2. Terminal 2 = collector 2, terminal 3 = emitter 2, terminal 4 = emitter 1, terminal 5 = collector 1 and terminal 6 = base 1.
- 2. Applies to all terminals.



1.7 FUNCTIONAL DIAGRAM



NOTES:

- 1. For TO-77, the case is not connected to any lead.
- 2. For CCP, the lid is not connected to any terminal.

1.8 <u>MATERIALS AND FINISHES</u>

Materials and finishes shall be as follows:

a) Case

For the metal can package the case shall be hermetically sealed and have a metal body with hard glass seals.

For the chip carrier package the case shall be hermetically sealed and have a ceramic body with a Kovar lid.

b) Leads/Terminals

As specified in Component Type Variants.

2. REQUIREMENTS

2.1 GENERAL

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

2.1.1 Deviations from the Generic Specification

None.

2.2 MARKING

The marking shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and as follows.



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The information to be marked on the component shall be:

- (a) Terminal identification (CCP package only).
- (b) The ESCC qualified components symbol (for ESCC qualified components only).
- (c) The ESCC Component Number.
- (d) Traceability information.

2.3 <u>TERMINAL STRENGTH</u>

The test conditions for terminal strength, tested as specified in the ESCC Generic Specification, shall be as follows:

For TO-77, Test Condition: E, lead fatigue.

2.4 <u>ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES</u>

Electrical measurements shall be performed at room, high and low temperatures.

2.4.1 Room Temperature Electrical Measurements

The measurements shall be performed at T_{amb} =+22 ±3°C.



Characteristics	Symbols	MIL-STD-750	Test Conditions	Limits		Units
		Test Method		Min	Max	-
Collector-Base Breakdown Voltage	V _{(BR)CBO}	3001	I _C = -10μA, Bias Condition D	-60	-	V
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	3011	I _C = -10mA, Bias Condition D Note 1	-45	-	V
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	3026	I _E = -10μA, Bias Condition D	-6	-	V
Collector-Base Cut-off Current	І _{СВО}	3036	V _{CB} = -45V, Bias Condition D	-	-10	nA
Emitter-Base Cut- off Current	I _{EBO}	3061	V _{EB} = -5V, Bias Condition D	-	-2	nA
Collector-Emitter Saturation Voltage	V _{CE(sat)}	3071	I _C =-10mA I _B =-500μA Note 1	-	-500	mV
Base-Emitter Saturation Voltage	V _{BE(sat)}	3066	I _C =-10mA I _B =-1mA Test Condition A Note 1	-	900	mV
Forward-Current	h _{FE1}	3076	I_{C} =-10 μ A ; V_{CE} =-5 V	100	300	-
Transfer Ratio	h _{FE2}	3076	I _C =-1mA ; V _{CE} =-5V	150	-	-
Forward-Current Transfer Ratio Comparison	h _{FE1-1} / h _{FE1-2}	3076	I_{C} =-10 μ A ; V_{CE} =-5 V	0.9	1.1	-
Base-Emitter Voltage Differential	IV _{BE1} - V _{BE2} I	3066	I _C =-10μA V _{CE} =-5V Test Condition B	-	5	mV
Current Gain Bandwidth Product	f _T	3206	I _C =-1mA V _{CE} =-5V f=30MHz Note 2	60	240	MHz
Small-Signal Short-Circuit Forward-Current Transfer Ratio	h _{fe}	3206	I _C =-1mA V _{CE} =-5V f=1kHz Note 2	150	600	-
Output Capacitance	C _{obo}	3236	V _{CB} =-5V I _E =0A f=1MHz Note 2	-	6	pF
Input Capacitance	C _{ib}	3240	V _{EB} =-500mV I _C =0A f=1MHz Note 2	-	12	pF



Characteristics	Symbols	•		Limits		Units
		Test Method		Min	Max	
Small-Signal Input Impedance	h _{ie}	3201	I _C =-1mA V _{CE} =-5V f=1kHz Note 2	3.7	20	kΩ
Small-Signal Output Impedance	h _{oe}	3216	I _C =-1mA V _{CE} =-5V f=1kHz Note 2	-	100	μmho
Noise Figure	NF	3246	I_C =-10μA V_{CE} =-5V R_S =10kΩ BW=15.7kHz Note 2	-	4	dB

NOTES:

- 1. Pulse measurement: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
- 2. For AC characteristics read and record measurements shall be performed on a sample of 32 components with 0 failures allowed. Alternatively a 100% inspection may be performed.

2.4.2 <u>High and Low Temperatures Electrical Measurements</u>

Characteristics	, , , , , , , , , , , , , , , , , , , ,		Lin	nits	Units	
		Test Method	Note 1	Min	Max	
Collector-Base Cut-off Current	I _{CBO}	3036	T _{amb} =+150(+0-5)°C V _{CB} =-45V, Bias Condition D	-	-10	μА
Forward-Current Transfer Ratio 2	h _{FE2}	3076	T _{amb} =-55(+5-0)°C I _C =-1mA V _{CE} =-5V	70	-	-
Forward-Current Transfer Ratio Comparison	h _{FE1-1} / h _{FE1-2}	3076	T_{amb} =-55 to +125°C I_{C} =-10 μ A ; V_{CE} =-5V	0.9	1.1	-
Base-Emitter Voltage Differential Change	IΔ(V _{BE1} - V _{BE2})ΔT _{amb} l 1	3066	T_{amb} =-55(+5-0)°C to +25±3°C I_{C} =-10 μ A V_{CE} =-5V	-	800	μV
	$ \Delta(V_{BE1}^{-}-V_{BE2})\Delta T_{amb} $	3066	T _{amb} =+25±3°C to +125(+5-0)°C I _C =-10μA V _{CE} =-5V	-	1000	

NOTES:

1. Read and record measurements shall be performed on a sample of 5 components with 0 failures allowed. Alternatively a 100% inspection may be performed.

2.5 PARAMETER DRIFT VALUES

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 ±3°C.



The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The drift values (Δ) shall not be exceeded for each characteristic specified. The corresponding absolute limit values for each characteristic shall not be exceeded.

Characteristics	Symbols		Limits		Units
		Drift Absolute		olute	
		Value Δ	Min	Max	
Collector-Base Cut-off Current	I _{CBO}	±1.5	-	-10	nA
Collector-Emitter Saturation Voltage	V _{CE(sat)}	±15 or (1) ±15%	-	-500	mV
Forward-Current Transfer Ratio 1	h _{FE1}	±15%	100	300	-

NOTES:

1. Whichever is the greater referred to the initial value.

2.6 <u>INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS</u>

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 ±3°C.

The test methods and test conditions shall be as per the corresponding test defined in either Room Temperature Electrical Measurements or High and Low Temperature Electrical Measurements, as applicable.

The limit values for each characteristic shall not be exceeded.

Characteristics	Symbols	Limits		Units	
		Min	Max		
Collector-Base Cut-off Current	I _{CBO}	-	-10	nA	
Collector-Emitter Saturation Voltage	V _{CE(sat)}	-	-500	mV	
Forward-Current Transfer Ratio 1	h _{FE1}	100	300	-	
Forward-Current Transfer Ratio Comparison	h _{FE1-1} / h _{FE1-2}	0.85	1.15	-	
Base-Emitter Voltage Differential	IV _{BE1} -V _{BE2} I	-	5	mV	
Base-Emitter Voltage Differential Change (Note 1)	l∆(V _{BE1} - V _{BE2})∆T _{amb} l1	-	1	mV	
	$ \Delta(V_{BE1}^{-}-V_{BE2})\Delta T_{amb} 2$	-	1.2		

NOTES:

1. To be measured after Operating Life test only.



2.7 <u>HIGH TEMPERATURE REVERSE BIAS BURN-IN CONDITIONS</u>

Characteristics	Symbols	Test Conditions	Units
Case Temperature	T _{case}	+150 (+0 -5)	°C
Collector-Base Voltage	V _{CB}	-60	V
Emitter-Base Voltage	V _{EB}	-6	V
Duration	t	72 minimum	hrs

2.8 POWER BURN-IN CONDITIONS

Characteristics	Symbols	Test Conditions	Units
Ambient Temperature	T _{amb}	+25±3	°C
Power Dissipation (Both Sections)	P _{totB}	As per Maximum Ratings P _{totB1} derated at the chosen T _{amb}	W
Collector-Base Voltage	V _{CB}	-30	V

2.9 OPERATING LIFE CONDITIONS

The conditions shall be as specified for Power Burn-in.



APPENDIX 'A'

AGREED DEVIATIONS FOR STMICROELECTRONICS (F)

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS
Deviations from Production Control- Chart F2	Special In-process Control Internal Visual Inspection. For CCP packages the criteria specified for voids in the fillet and minimum die mounting material around the visible die perimeter for die mounting defects may be omitted providing that a radiographic inspection to verify the die-attach process is performed on a sample basis in accordance with STMicroelectronics procedure 0076637.
Deviations from Room Temperature Electrical Measurements	All AC characteristics (Room Temperature Electrical Measurement Note 2) may be considered guaranteed but not tested if successful pilot lot testing has been performed on the wafer lot which includes AC characteristic measurements per the Detail Specification. A summary of the pilot lot testing shall be provided if required by the Purchase Order.
Deviations from High and Low Temperatures Electrical Measurements	All characteristics specified may be considered guaranteed but not tested if successful pilot lot testing has been performed on the wafer lot which includes characteristic measurements at high and low temperatures per the Detail Specification. A summary of the pilot lot testing shall be provided if required by the Purchase Order.