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DIODES, POWER RECTIFIER, SCHOTTKY BARRIER

BASED ON TYPE STPS20100

ESCC Detail Specification No. 5106/016

Issue 3	June 2007



Document Custodian: European Space Agency - see https://escies.org



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DOCUMENTATION CHANGE NOTIC

(Refer to https://escies.org for ESCC DCR content)

DCR No.	CHANGE DESCRIPTION
271	Specification up issued to incorporate editorial and technical changes per DCRs.



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1. <u>GENERAL</u>

1.1 <u>SCOPE</u>

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 <u>APPLICABLE DOCUMENTS</u>

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. 5000
- (b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 The ESCC Component Number The ESCC Component Number shall be constituted as follows:

Example: 510601601

- Detail Specification Reference: 5106016
- Component Type Variant Number: 01 (as required)

1.4.2 <u>Component Type Variants</u>

The component type variants applicable to this specification are as follows:

Variant Number	Based on Type	Case	Description	Lead/Terminal Material and Finish	Weight max g
01	STPS20100	TO-254	Single diode	H9	10
02	STPS20100	TO-254	Dual diode, common anode	H9	10
03	STPS20100	TO-254	Dual diode, common cathode	H9	10
04	STPS20100	TO-254	Dual diode, series, centre tapped	H9	10



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Variant Number	Based on Type	Case	Description	Lead/Terminal Material and Finish	Weight max g
05	STPS20100	SMD.5	Single diode	Q14	2
06	STPS20100	SMD1	Single diode	Q14	3
07	STPS20100	SMD1	Dual diode, common cathode	Q14	3

The lead/terminal material and finish shall be in accordance with the requirements of ESCC Basic Specification No. 23500.

1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Symbols	Maximum Ratings	Unit	Remarks
Forward Surge Current (per Diode)	I _{FSM}	250	A	Note 1
Repetitive Peak Reverse Voltage	V _{RRM}	100	V	Note 2
Repetitive Peak Reverse Current	I _{RRM}	1	A	Note 3
Average Output Rectified Current	۱ ₀		A	50% duty cycle Notes 4, 9
All Variants (per Diode)		20		
Variants 02, 03 and 07 (per Device)		40		
RMS Forward Current (Per Diode)	I _{F(rms)}	30	A	
Operating Temperature Range (Case Temperature)	Т _{ор}	-65 to +175	°C	Note 5
Junction Temperature	Т _Ј	+175	°C	
Storage Temperature Range	T _{stg}	-65 to +175	°C	Note 5
Soldering Temperature	T _{sol}		°C	
For TO-254		+260		Note 6
For SMD.5 and SMD1		+245		Note 7
Critical Rate of Rise of Reverse Voltage	dV/dt	10000	V/µs	
Thermal Resistance, Junction to Case	R _{th(j-c)}		°C/W	Notes 8, 9
Variants 01, 05 and 06		1.65		
Variants 02 to 04 and 07 (per Diode)		1.65		
Variants 02, 03 and 07 (per Device)		0.85		



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NOTES:

- 1. Sinusoidal pulse of 10ms duration.
- 2. Pulsed, duration 5ms, f = 50Hz.
- 3. Pulsed, duration $2\mu s$, f = 1kHz.
- 4. For $T_{case} > +140^{\circ}$ C, derate linearly to 0A at +175°C.
- 5. For Variants with hot solder dip lead finish all testing performed at $T_{amb} > +125^{\circ}C$ shall be carried out in a 100% inert atmosphere.
- 6. Duration 10 seconds maximum at a distance of not less than 1.5mm from the device body and the same lead shall not be resoldered until 3 minutes have elapsed.
- 7. Duration 5 seconds maximum and the same package shall not be resoldered until 3 minutes have elapsed.
- 8. Package mounted on infinite testsink.
- 9. The "per Device" ratings apply only as follows:-
 - Variants 02: when both cathode terminals are tied together. Variants 03 and 07: when both anode terminals are tied together.

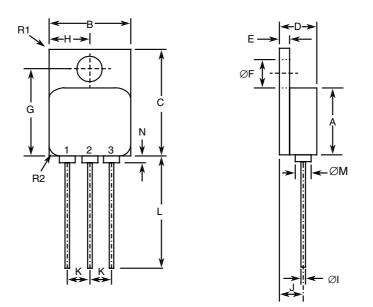
1.6 HANDLING PRECAUTIONS

The TO-254 package contains Beryllium Oxide (BeO) and therefore it must not be ground, machined, sandblasted or subjected to any mechanical operation which will produce dust. The case must not be subjected to any chemical process (e.g. etching) which will produce fumes.

1.7 <u>PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION</u>

Consolidated Notes are given following the case drawings and dimensions.

1.7.1 Metal Flange Mount Package (TO-254) - 3 lead



Symbols	Dimensions mm		Notes
Gymbola	Min	Мах	Notes
A	13.59	13.84	
В	13.59	13.84	
С	20.07	20.32	

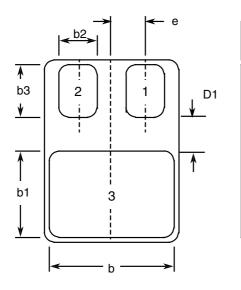


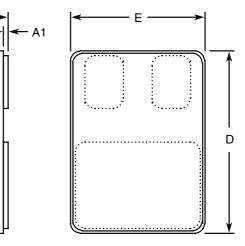
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Symbolo	Dimensio	Notes	
Symbols	Min	Max	Notes
D	6.3	6.7	
E	1	1.35	
ØF	3.5	3.9	
G	16.89	17.4	
Н	6.86	BSC	
ØI	0.89	1.14	2
J	3.81 BSC		
К	3.81	BSC	
L	12.95	14.5	
ØM	3.05 Typical		2
N	-	0.71	2
R1	- 1		3
R2	1.65 T	ypical	4

Α-

1.7.2 Surface Mount Package (SMD.5) - 3 terminal





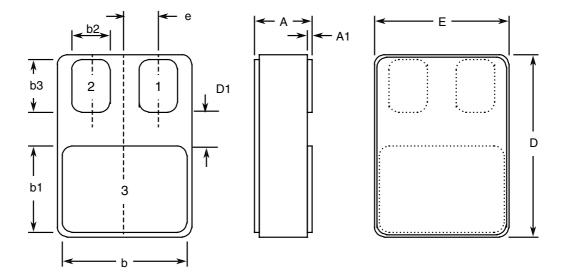
Symbols	Dimensio	Notes	
Gymbola	Min	Мах	Notes
Α	2.84	3.15	
A1	0.25	0.51	
b	7.13	7.39	
b1	5.58	5.84	
b2	2.28	2.54	5



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Symbols	Dimensio	Notes	
Symbols	Min	Max	Notes
b3	2.92	3.18	5
D	10.03	10.28	
D1	0.76	-	5
E	7.39	7.64	
е	1.91 BSC		5

1.7.3 Surface Mount Package (SMD1) - 3 terminal



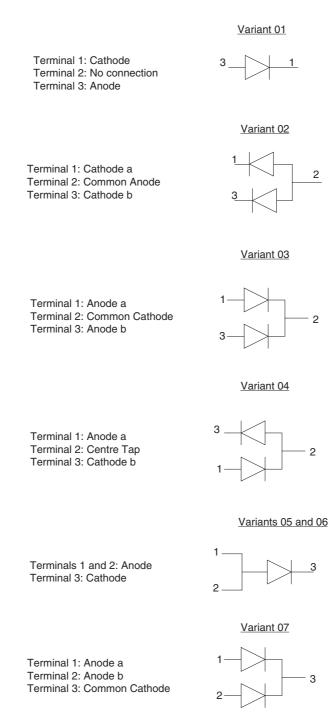
Symbols	Dimensions mm		Notes	
Symbols	Min	Мах	NOLES	
A	3.3	3.61		
A1	0.25	0.51		
b	9.4	9.65		
b1	10.41	10.67		
b2	3.43	3.68	5	
b3	3.86	4.11	5	
D	15.75	16		
D1	0.76	-	5	
E	11.3	11.56		
е	2.67 BSC		5	

- 1.7.4 Notes to Physical Dimensions and Terminal Identification
 - 1. The terminal identification is specified by the component's geometry. See Functional Diagram for the terminal connections.
 - 2. 3 places.



- 3. Radius of heatsink flange corner, 4 places.
- 4. Radius of body corner, 4 places.
- 5. 2 places.

1.8 FUNCTIONAL DIAGRAM



NOTES:

- 1. For TO-254, the case is not connected to any lead.
- 2. For SMD.5 and SMD1, the lid is not connected to any terminal.



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1.9 <u>MATERIALS AND FINISHES</u>

Materials and finishes shall be as follows:

a) Case

For the metal flange mount package the case shall be hermetically sealed and have a metal body. The leads pass through ceramic eyelets brazed into the frame and the lid shall be welded. For the surface mount packages the case shall be hermetically sealed and have a ceramic body with a Kovar lid.

b) Leads/Terminals As specified in Component Type Variants.

2. <u>REQUIREMENTS</u>

2.1 <u>GENERAL</u>

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

- 2.1.1 Deviations from the Generic Specification
- 2.1.1.1 Deviation from Screening Tests Chart F3
 - (a) High Temperature Reverse Bias Burn-in and the subsequent Final Measurements for HTRB shall be omitted.
- 2.1.1.2 Deviations from Qualification and Periodic Tests Chart F4
 - (a) Constant Acceleration is not applicable.
 - (b) For SMD.5 and SMD1, Terminal Strength is not applicable.

2.2 <u>MARKING</u>

The marking shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and as follows.

The information to be marked on the component shall be:

- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- (b) The ESCC Component Number.
- (c) Traceability information.
- (d) Warning sign for Beryllium Oxide (TO-254 only).

2.3 <u>TERMINAL STRENGTH</u>

The test conditions for Terminal Strength, tested as specified in the ESCC Generic Specification, shall



be as follows:

For TO-254, Test Condition: A, tension, with an applied force of 10N and a duration of 10s.

2.4 ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES

Electrical measurements shall be performed at room, high and low temperatures. Consolidated notes are given after the tables.

2.4.1 Room Temperature Electrical Measurements The measurements shall be performed at T_{amb} =+22 ±3°C.

Characteristics	Symbols MIL-STD-750	Test Conditions	Limits		Units	
		Test Method	Test Method Note 1	Min	Max	
Reverse Current	I _R	4016	DC Method V _R = 100V	-	30	μA
Forward Voltage	V _{F1}	4011	Pulse Method I _F = 10A Note 2	-	780	mV
	V _{F2}	4011	Pulse Method I _F = 20A Note 2	-	1	V
Capacitance	С	4001	V _R = 10V f = 1MHz	-	700	pF
Thermal Impedance, Junction to Case	Z _{th(j-c)}	3101	$I_{H} = 15 \text{ to } 40\text{A}$ $t_{H} = 50\text{ms}$ $I_{M} = 50\text{mA}$ $t_{md} = 100\mu\text{s}$ Note 3		ate ∆V _F , lote 4)	°C/W

2.4.2 <u>High and Low Temperatures Electrical Measurements</u>

Characteristics	Symbols	MIL-STD-750		Limits		Units
		Test Method		Min	Max	
Reverse Current	I _R	4016	T _{case} =+125(+0-5) ^o C DC Method V _R =100V	-	20	mA
Forward Voltage 2	rward Voltage 2 V _{F2} 4011		T _{case} =+125(+0-5) ^o C Pulse Method I _F =20A Note 2	-	900	mV
			T _{case} =-55(+5 -0) ^o C Pulse Method I _F =20A Note 2	-	1.1	V



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2.4.3 Notes to Electrical Measurement Tables

- Measurement per each diode. 1.
- Pulse Width \leq 300µs, Duty Cycle \leq 2%. 2.
- Performed only during Screening Tests Parameter Drift Values (Initial Measurements), go-no-go. 3.
- 4. The limits for ΔV_F shall be defined by the Manufacturer on every lot in accordance with MIL-STD-
- 750 Method 3101 and shall guarantee the $R_{th(j-c)}$ limits specified in Maximum Ratings. Read and record measurements shall be performed on a sample of 5 components with 0 failures 5. allowed. Alternatively a 100% inspection may be performed.

2.5 PARAMETER DRIFT VALUES

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 ±3°C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The drift values (Δ) shall not be exceeded for each characteristic specified. The corresponding absolute limit values for each characteristic shall not be exceeded.

Characteristics	Symbols		Limits Absolute		Units
		Drift			
		Value Δ	Min	Max	
Reverse Current	I _R	±4 or (1) ±100%	-	30	μA
Forward Voltage 1	V _{F1}	±10	-	780	mV

NOTES:

2.6 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

Unless otherwise specified, the measurements shall be performed at T_{amb} =+22 ±3°C.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The limit values for each characteristic shall not be exceeded.

Characteristics	Symbols	Limits		Units
		Min	Max	
Reverse Current	I _R	-	30	μΑ
Forward Voltage 1	V _{F1}	-	780	mV

^{1.} Whichever is the greater referred to the initial value.



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2.7 POWER BURN-IN CONDITIONS

Characteristics	Symbols	Test Conditions	Units
Case Temperature	T _{case}	+125	°C
Reverse Voltage	V _R	80	V

2.8 OPERATING LIFE CONDITIONS

The conditions shall be as specified for Power Burn-in.



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APPENDIX 'A'

AGREED DEVIATIONS FOR STMICROELECTRONICS (F)

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS	
Deviations from	Special In-process Control Internal Visual Inspection.	
Production Control-	Wedge bonds equal to 1.1 wire diameter are acceptable for bonding with a	
Chart F2	V-Groove tool.	