LIST OF DISCONTINUED ESCC DOCUMENTS

AND SPECIFICATIONS

ESCC REP002

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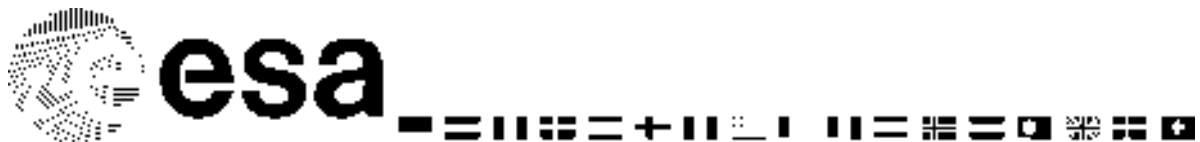
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DOCUMENTATION CHANGE NOTICE

(Refer to https://escies.org for ESCC DCR content)

DCR No.	CHANGE DESCRIPTION
329, 419	REP002 upissued to list specifications per DCRs.

TABLE OF CONTENTS

ESCC

REP002

**LIST OF DISCONTINUED
ESCC DOCUMENTS
AND SPECIFICATIONS**

Page: 4

Issue: 4

<u>INTRODUCTION</u>

This list comprises all discontinued ESCC documents and specifications which have been transferred from REP001 and are not subject to configuration control.

The REP002 specifications are former REP001 specifications which are covering obsolete components, technologies, test methods... no longer used or existing. These specifications are no longer updated and distributed. They do not represent the current ESCC policy and therefore cannot be used for procurement or manufacturing of parts.
The REP002 is published once a year.

The ESCC documents and specifications are classified according to 5 levels, viz.:

LEVEL 0   -   Object and Basic Rules of the SCC System.

LEVEL 1   -   1(1) Organisation Documents.

               1(2) Implementation Documents.

               1(3) Procedures.

LEVEL 2   -   Basic Specifications and Sectional Documents.

LEVEL 3   -   Generic Specifications.

LEVEL 4   -   Detail Specifications.

Where relevant, the following symbols are used in the specification title or component type:

   - u for μ

   - ohms for Ω

   - deg/degree for °.

ESCC **REP002**	**LIST OF DISCONTINUED** **ESCC DOCUMENTS** **AND SPECIFICATIONS**	Page: 5 Issue: 4

ESCC Document or Spec. Number	Specification Title **Class of Publication: 03** **Basic Specifications**	Status	
		Issue	Date
23000	Requirements for the Extension of Qualification Approval of Standard Electronic Components for Space Application	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 12 **R.F. Coils**	Issue	Date
3201/006	R.F. Coil Fixed, based on type S150	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 58 **Attenuators and Loads**	Issue	Date
3403/007	Flange Loads High Power 50 Ohms, based on type EMC8745	1	0210

ESCC Document or Spec. Number	Specification Title		Status	
			Issue	Date
	Class of Publication: 16 **Relays Non-Latching**			
3601/001	Relays Electromagnetic Non-Latching 28Vdc 10A 2PDT		2	0409

ESCC Document or Spec. Number	Specification Title		Status	
			Issue	Date
	Class of Publication: 17 **Relays Latching**			
3602/001	Relays Electromagnetic Latching 28Vdc 10A 2PDT		2	0409

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 59 **Toggle Switches**	Issue	Date
3701/001	Toggle Switches, based on series 12100 and 11100	1	0210

ESCC

List of Discontinued ESCC Documents and Specifications

Issue: 4

Page: 11

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 21 **Resistors Film and Metal Oxide**	Issue	Date
4001/016	Resistors Fixed Chips Film, based on types CHP HR0505	1	0210
4001/017	Resistors Fixed Chips Film, Based on types CHP HR0705	1	0210
4001/018	Resistors Fixed Chips Film, based on types CHP HR0805	1	0210
4001/019	Resistors Fixed Chips Film, based on types CHP HR1010	1	0210
4001/020	Resistors Fixed Chips Film, based on type CHP HR1206	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 42 **IC's Sil Mon Bi: Linear**	Issue	Date
9101/001	Operational Amplifier, based on type LM 101A	1	0210
9101/005	Operational Amplifier, based on type LM108A	1	0210
9101/006	Operational Amplifier Buffer, based on type LM 118	1	0210
9101/008	Dual Bipolar Operational Amplifiers, based on type LM747	1	0210
9101/009	Fast Sample and Hold Operational Amplifier, based on type HA-2420	1	0210
9101/010	Operational Amplifier, based on type HA2520-2	1	0210
9101/011	Low Power Quad Bipolar Operational Amplifier, based on type LM124 and LM124A	1	0210
9101/017	JFET Input Operational Amplifier, based on type LF155 LF155A LF156 LF156A and LF157	1	0210
9101/022	Bipolar Operational Amplifiers, based on type LM11	1	0210
9101/033	Bipolar Operational Amplifier, based on type OP400A	1	0210
9102/001	Voltage Regulator, based on type LM104	1	0210
9102/002	Voltage Regulator, based on type LM105	1	0210
9102/005	3-Terminal Adjustable Positive Regulator, based on type LM117	1	0210
9102/007	3-Terminal Adjustable Negative Regulator, based on type LM137	1	0210
9102/013	High Efficiency Linear Regulator, based on type UC1834	1	0210
9103/002	Voltage Comparator, based on type LM111	1	0210
9103/004	Quad Bipolar Voltage Comparator, based on types LM139 and LM139A	1	0210
9108/001	Operational Amplifier, based on type LM110	1	0210
9108/002	Precision Timer, based on type LM122	1	0210
9108/007	Regulating Pulse Width Modulator, based on type SG1524	1	0210
9108/011	Regulating Pulse Width Modulator, based on type SG1525A	1	0210
9108/018	Integrated Circuits Silicon Monolithic Current Mode Pulse Width Modulator Controller, based on type UC1843	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 44 **IC's Sil Mon Bi Low Power Schottky: 54LS Series**	Issue	Date
9201/014	Quad 2-Input Exclusive OR Gates, based on types 54LS86 and 54LS86A	1	0210
9201/015	Quad 2-Input Positive NAND Gate, based on type 54LS00	1	0210
9201/016	Quad 2-Input Positive NOR Gate, based on type 54LS02	1	0210
9201/017	Triple 3-Input Positive NAND Gate, based on type 54LS10	1	0210
9201/018	Dual 4-Input Positive NAND Gate, based on type 54LS20	1	0210
9201/019	8-Input Positive NAND Gate, based on type 54LS30	1	0210
9201/025	2-Wide 3-Input 2-Wide 2-Input Positive AND/OR Invert Gate, based on type 54LS51	1	0210
9201/026	4-Wide 3-2-2-3 Input Positive AND/OR Invert Gate, based on type 54LS54	1	0210
9201/028	Triple 3-Input Positive NOR Gate, based on type 54LS27	1	0210
9201/035	Quad 2-Input Positive AND Gate, based on type 54LS08	1	0210
9201/036	Quadruple 2-Input High Voltage Interface Positive NAND Gate, based on type 54LS26	1	0210
9201/038	Quad 2-Input Positive OR Gate, based on type 54LS32	1	0210
9201/039	Dual 4-Input Positive NAND Gate, based on type 54LS40	1	0210
9201/049	Triple 3-Input Positive AND Gate, based on type 54LS11	1	0210
9201/050	Quad 2-Input Exclusive NOR Gates, based on type 54LS266	1	0210
9201/053	Dual 4-Input Positive and Gate, based on type 54LS21	1	0210
9201/069	Quad 2-Input Positive NAND Gate with Open Collector Output, based on type 54LS03	1	0210
9201/073	Quad 2-Input Positive AND Gate with Open Collector Output, based on type 54LS09	1	0210
9201/081	Quad 2-Input Exclusive OR Gates, based on type 54LS386 and 54LS386A	1	0210
9202/005	Arithmetic Logic Unit/Function Generator, based on type 54LS181	1	0210
9202/009	Hex Inverter, based on type 54LS04	1	0210
9202/010	Quad 2-Line-to-1-Line Selectors/Multiplexers, based on type 54LS257A	1	0210
9202/013	Hex Schmitt Trigger Inverter, based on type 54LS14	1	0210
9202/015	1-of-8 Data Selectors/Multiplexers, based on type 54LS151	1	0210
9202/016	Dual 4-Line-to-1-Line Data Selector/Multiplexer, based on type 54LS153	1	0210
9202/017	Quad 2-Input Positive NAND Buffer with Open Collector Outputs, based on type 54LS38	1	0210
9202/027	Quad 2-Line-to-1-Line Selectors/Multiplexers, based on type 54LS157	1	0210
9202/028	Quad 2-Line-to-1-Line Selectors/Multiplexers, based on type 54LS158	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 44	Issue	Date
	IC's Sil Mon Bi Low Power Schottky: 54LS Series		
9202/029	Dual 4-line-to-1-Line Data Selectors/Multiplexers with 3-State Outputs, based on type 54LS253	1	0210
9202/030	4-Bit Binary Full Adder with Fast Carry, based on type 54LS83A	1	0210
9202/031	4-Bit Magnitude Comparator, based on type 54LS85	1	0210
9202/032	Low Power Bipolar Schottky 4-Bit Binary Full Adder with Fast Carry, based on type 54LS283	1	0210
9202/033	Dual Monostable Multivibrators, based on type 54LS221	1	0210
9202/052	Data Selectors/Multiplexers with 3-State Outputs, based on type 54LS251	1	0210
9202/053	Quad Set-Reset Latches, based on types 54LS279 and 54LS279A	1	0210
9202/054	Hex Bus Driver with 3-State Outputs, based on type 54LS367A	1	0210
9202/055	Hex Bus Driver with 3-State Outputs, based on type 54LS368A	1	0210
9202/064	Dual Carry-Save Full Adders, based on type 54LS183	1	0210
9202/066	Crystal Controlled Oscillator, based on type 54LS321	1	0210
9202/070	8-bit Addressable Latches, based on type 54LS259B	1	0210
9203/008	4-Bit Bistable Latches with Complementary Outputs, based on type 54LS75	1	0210
9203/013	Dual D-Type Flip-Flop with Preset and Clear, based on type 54LS74A	1	0210
9203/014	Dual J-K Flip-Flop with Clear, based on type 54LS73A	1	0210
9203/015	Dual J-K Flip-Flop with Preset and Clear, based on type 54LS76A	1	0210
9203/016	Dual J-K Flip-Flop with Preset and Clear, based on type 54LS112A	1	0210
9203/018	Hex D-Type Flip-Flop with Clear, based on type 54LS174	1	0210
9203/019	Quad D-Type Flip-Flops with Clear, based on type 54LS175	1	0210
9203/020	Dual J-K Flip-Flops with Clear, based on type 54LS107A	1	0210
9203/021	Dual J-K Negative Edge-Triggered Flip-Flop with Preset, based on type 54LS113A	1	0210
9203/024	Dual J-K Flip-Flop with Preset and Clear, based on type 54LS109A	1	0210
9203/025	Dual J-K Negative Edge-Triggered Flip-Flop, based on type 54LS114A	1	0210
9203/030	Octal D-Type Flip-Flop with Clear, based on type 54LS273	1	0210
9203/031	Octal D-Type Flip-Flop, based on type 54LS374	1	0210
9203/034	Octal D-Type Latches, based on type 54LS373	1	0210
9203/035	Octal D-Type Flip-Flop with Enable, based on type 54LS377	1	0210
9203/039	4-Bit Bistable Latches with Complementary Outputs, based on type 54LS375	1	0210
9204/007	Divide-by-2 and Divide-by-6 Counter, based on type 54LS92	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 44 **IC's Sil Mon Bi Low Power Schottky: 54LS Series**	Issue	Date
9204/008	4-Bit Binary Counter, based on type 54LS93	1	0210
9204/010	Decade Counter, based on type 54LS90	1	0210
9204/014	Synchronous Counter with Direct Clear, based on type 54LS160A	1	0210
9204/015	Synchronous Counter with Direct Clear, based on type 54LS161A	1	0210
9204/016	Synchronous 4-Bit Counter with Synchronous Clear, based on type 54LS163A	1	0210
9204/017	Programmable Synchronous 4-Bit Up/Down Binary Counter, based on type 54LS193	1	0210
9204/018	Presettable Divide-by-2 and Divide-by-5 Counter, based on type 54LS196	1	0210
9204/019	Presettable Divide-by-2 and Divide-by-8 Counter, based on type 54LS197	1	0210
9204/033	Dual 4-Bit Decade and Binary Counter, based on type 54LS390	1	0210
9204/034	Dual 4-Bit Binary Counter, based on type 54LS393	1	0210
9205/003	Dual Decoder/Demultiplexer, based on types 54LS139 and 54LS139A	1	0210
9205/004	Decoder/Demultiplexer, based on type 54LS138	1	0210
9205/008	BCD-to-Decimal Decoder, based on type 54LS42	1	0210
9205/009	Dual 2-Line-to-4-Line Decoder/Demultiplexer, based on type 54LS155 and 54LS155A	1	0210
9207/002	Dual Monostable Multivibrator, based on type 54LS123	1	0210
9207/004	Retriggerable Monostable Multivibrator, based on type 54LS122	1	0210
9208/001	9-Bit Odd/Even Parity Generator/Checker, based on type 54LS280	1	0210
9301/001	4-by-4 Register File, based on type 54LS670	1	0210
9306/009	4-Bit Parallel Shift Register, based on type 54LS95B	1	0210
9306/011	8-Bit Parallel-Out Serial Shift Register, based on type 54LS164	1	0210
9306/012	4-Bit Bidirectional Universal Shift Register, based on type 54LS194A	1	0210
9306/019	4-Bit Parallel-Access Shift Register, based on type 54LS195A	1	0210
9306/020	5-Bit Shift Register, based on type 54LS96	1	0210
9306/029	Parallel Load 8-Bit Shift Register, based on types 54LS165 and 54LS165A	1	0210
9306/030	8-Bit Shift Register, based on types 54LS166 and 54LS166A	1	0210
9306/035	16-Bit Shift Register, based on type 54LS673	1	0210
9306/036	16-bit Shift Register, based on type 54LS674	1	0210
9401/007	Quad 2-Input Positive NAND Buffer, based on type 54LS37	1	0210
9401/008	Quad 2-Input Positive NOR Buffer, based on type 54LS28	1	0210

ESCC Document or Spec. Number	Specification Title **Class of Publication: 44** **IC's Sil Mon Bi Low Power Schottky: 54LS Series**	Status	
		Issue	Date
9401/009	Hex Inverter, based on type 54LS05	1	0210
9401/011	Quad Bus Buffer Gate with 3-State Outputs, based on type 54LS125A	1	0210
9401/014	Octal Buffer and Line Driver with 3-State Outputs, based on type 54LS240	1	0210
9401/016	Octal Buffer/line Driver with 3-State Outputs, based on type 54LS241	1	0210
9401/017	Hex Buffer Driver, based on type 54LS365A	1	0210
9402/003	Octal Buffer/Line Driver/Line Receiver with 3-State Outputs, based on type 54LS244	1	0210
9405/002	Octal Bus Transceivers with 3-State Outputs, based on type 54LS245	1	0210
9405/003	Quad Bus-Transceiver with Inverted 3-State Outputs, based on type 54LS242	1	0210
9405/004	Quad Bus Transceivers with Non-Inverted 3-State Outputs, based on type 54LS243	1	0210
9406/002	BCD-to-Decimal Decoder, based on type 54LS145	1	0210
9408/015	Quad 2-Input Multiplexer with Storage, based on type 54LS398	1	0210
9408/016	Quad 2-Input Multiplexer with Storage, based on type 54LS399	1	0210
9409/001	Dual 4-Input Positive NAND Schmitt Trigger, based on type 54LS13	1	0210
9409/004	Quad 2-Input Positive NAND Schmitt Trigger, based on type 54LS132	1	0210
9410/003	8-Line to 3-Line Priority Encoder, based on type 54LS148	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 56 **IC's Sil Mon Bi Schottky: 54 S Series**	Issue	Date
9201/056	Quad 2-Input Positive NAND Gates, based on type 54S00	1	0210
9201/057	Quad 2-Input Positive AND Gate with Open Collector Output, based on type 54S09	1	0210
9201/058	Triple 3-Input Positive NAND Gate, based on type 54S10	1	0210
9201/059	Dual 4-Input Positive NAND Gate, based on type 54S20	1	0210
9201/060	8-Input Positive NAND Gate, based on type 54S30	1	0210
9201/074	Quad 2-Input Positive NOR Gate, based on type 54S02	1	0210
9201/077	Dual 4-Input Positive NAND Gate with Open Collector Outputs, based on type 54S22	1	0210
9201/078	Dual 2-Wide 2-Input AND-OR INVERT Gates, based on type 54S51	1	0210
9201/079	Quadruple 2-Input Exclusive OR Gate, based on type 54S86	1	0210
9201/083	Dual 5-Input Positive NOR Gate, based on type 54S260	1	0210
9201/112	Positive OR Gate, based on type 54S32	1	0210
9202/059	9-Bit Odd/Even Parity Generator/Checker, based on type 54S280	1	0210
9203/026	Dual D Positive Edge Triggered Flip-Flop with Preset and Clear, based on type 54S74	1	0210
9203/027	Dual J-K Negative-Edge-Triggered Flip-Flop with Preset, based on type 54S113	1	0210
9203/028	Dual J-K Negative Edge Triggered Flip-Flop with Preset and Clear, based on type 54S112	1	0210
9203/032	Hex D-Type Flip-Flop with Clear, based on type 54S174	1	0210
9203/033	Quadruple D-Type Flip-Flop with Clear, based on type 54S175	1	0210
9203/041	Octal D-Type 3-State Positive Edge-Triggered Flip-Flop, based on type 54S374	1	0210
9204/029	Synchronous 4-Bit Binary Counter with Synchronous Clear, based on type 54S163	1	0210
9204/044	Presettable Divide-by-2 and Divide-by-5 Counter, based on type 54S196	1	0210
9306/024	4-Bit Parallel Access Shift Register, based on type 54S195	1	0210
9306/031	4-Bit Bidirectional Universal Shift Register, based on type 54S194	1	0210
9401/012	Hex Inverter, based on type 54S04	1	0210
9401/015	Quad 2-Input Positive NAND Buffer, based on type 54S37	1	0210
9402/004	Dual 4-Input Positive NAND 50 Ohm Line Driver, based on type 54S140	1	0210
9408/007	8-to-1 Line Data Selector/Multiplexer, based on type 54S151	1	0210
9408/008	4-Line-to-1-Line Data Selector/Multiplexer, based on type 54S153	1	0210
9408/010	Decoder/Demultiplexer, based on type 54S138	1	0210

ESCC Document or Spec. Number	Specification Title	Status		
	Class of Publication: 56 **IC's Sil Mon Bi Schottky: 54 S Series**		Issue	Date
9408/020	Data Selector/ Multiplexer with 3-State Outputs, based on type 54S251		1	0210
9408/042	Quad 2-Line-to-1 Line Data Selectors/Multiplexers, based on type 54S158		1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 47 **IC's Sil Mon: Others**	Issue	Date
9204/063	Integrated Circuits Monolithic Silicon on Sapphire CMOS ION Counter, based on type 11918	1	0210
9301/015	CMOS Silicon Gate Static 16K (2048x8 Bit) Asynchronous Random Access Memory with 3-State Outputs, based on types HM65162 and HM65162B	1	0210
9301/018	CMOS Silicon Gate Static 16K (16348x1Bit) Asynchronous Random Access Memory with 3-State Outputs, based on types HM65262 and HM65262B	1	0210
9301/026	CMOS Silicon Gate Static 64K (65536 x1 Bit) Asynchronous Random Access Memory with 3-State Outputs, based on type HM65687	1	0210
9301/029	CMOS Silicon Gate Static 64K (8192/8 bit) Asynchronous Random Access Memory with 3-State Outputs, based on type HM65664	1	0210
9301/030	CMOS Silicon Gate Static 256K (32768x8bit) Asynchronous Random Access Memory with 3 State Outputs, based on type HM65656	1	0210
9301/032	CMOS Silicon Gate Static 9K (1024x9 bit) First-In First-Out (FIFO Memory with 3 State Outputs, based on type M67202FV	1	0210
9301/034	CMOS Silicon Gate Static 64K (4096x16 bit) Dual Port Memory with 3 State Outputs, based on type M67024EV	1	0210
9301/038	CMOS Silicon Gate Static 256K (262144x1Bit) Asynchronous Random Access Memory with 3-State Outputs, based on type M65697EV	1	0210
9301/041	CMOS Silicon Gate Static (512 x 9 Bit) First-In First Out Memory with 3-state Outputs, based on type M67201FV	1	0210
9304/003	High Performance Programmable Array Logic Circuit, based on type PAL16L8	1	0210
9304/005	High Performance Programmable Array Logic Circuit, based on type PAL22V10	1	0210
9304/006	High Performance Programmable Array Logic Circuit, based on type PAL20R8	1	0210
9304/007	High Performance Programmable Array Logic (PAL) Circuit, based on type PAL20L8	1	0210
9402/012	Quad EIA-422 Line Driver with 3 State Outputs, based on type AM26LS31 and 55ALS192	1	0210
9403/003	Quad EIA-422/423 Line Receiver with 3-State Outputs, based on type AM26LS32	1	0210
9407/001	8-Bit High Speed Multiplying D/A Converter, based on type DAC08A	1	0210
9408/001	2-Channel High Speed Driver with SPST JFET Switches, based on type DG181	1	0210
9408/002	16-Channel CMOS Analogue Multiplexer, based on type HI506A	1	0210
9408/003	Dual 8-Channel CMOS Analogue Multiplexer, based on type HI507A	1	0210
9408/019	CMOS 16-Channel Multiplexer Radiation-Hardened, based on type HS1840RH	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 60 **IC's Sil Mon Bi Advanced Low Power Schottky 54ALS**	Issue	Date
9201/088	Quad 2-Input Positive NAND Gates, based on type 54ALS00A	1	0210
9201/089	Quad 2-Input Positive NOR Gates, based on type 54ALS02	1	0210
9201/091	Quad 2-Input Positive OR Gates, based on type 54ALS32	1	0210
9201/092	Quad 2-Input Positive AND Gates, based on type 54ALS08	1	0210
9201/093	Triple 3-Input Positive NAND Gates, based on types 54ALS10 and 54ALS10A	1	0210
9201/094	Triple 3-Input Positive AND Gates, based on types 54ALS11 and 54ALS11A	1	0210
9201/095	Dual 4-Input Positive NAND Gates, based on type 54ALS20A	1	0210
9201/096	Dual 4-Input Positive AND Gates, based on types 54ALS21 and 54ALS21A	1	0210
9201/097	Triple 3-Input Positive NOR Gates, based on type 54ALS27	1	0210
9201/098	8-Input Positive NAND Gates, based on types 54ALS30 and 54ALS30A	1	0210
9201/099	Quadruple 2-Input Exclusive OR Gates, based on type 54ALS86	1	0210
9201/100	Quad 2-Input Positive-NAND Buffer Gates with Increased Output Drive, based on type 54ALS37A & 1000A	1	0210
9201/101	Quad 2-Input Positive-NOR Buffer Gates with Increased Output Drive, based on types 54ALS28A & 1002A	1	0210
9201/102	Triple 3-Input Positive AND Buffer Gates with Increased Output Drive, based on type 54ALS1011A	1	0210
9201/104	Quad 2-Input Positive AND Gates with Open Collector Outputs, based on type 54ALS09	1	0210
9201/115	Quad 2-Input Positive-NAND Buffer Gates with Increased Output Drive and Open Collector Outputs, based on types 54ALS38/38A/1003/1003A	1	0210
9201/116	Dual 4-Input Positive-NAND Buffer Gates with Increased Output Drive, based on types 54ALS40A/1020A	1	0210
9202/067	Octal D-Type Transparent Latches with 3-State Buffered Outputs, based on type 54ALS573	1	0210
9202/071	Octal D-type Transparent Latches with 3-State Outputs, based on type 54ALS373	1	0210
9203/042	Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54ALS574 &574A	1	0210
9203/043	Dual D-Type Positive-Edge-Triggered Flip-Flops with Clear and Preset, based on types 54ALS74 and 74A	1	0210
9203/046	Dual 4-Bit D-Type Edge-Triggered Flip-Flops with 3-State Buffered Outputs, based on types 54ALS874 and 54ALS874A	1	0210
9203/047	Hex D-Type Positive Edge-Triggered Flip-Flops with Clear, based on type 54ALS174	1	0210
9203/048	Quad D-Type Positive Edge-Triggered Flip-Flops with Clear, based on type 54ALS175	1	0210
9203/049	Dual J-K Positive Edge-Triggered Flip-Flops with Clear and Preset, based on types 54ALS109 and 54ALS109A	1	0210
9203/055	Dual J-K Negative Edge-Triggered Flip-Flops with Clear and Preset, based on type 54ALS112A	1	0210

ESCC Document or Spec. Number	Specification Title **Class of Publication: 60** **IC's Sil Mon Bi Advanced Low Power Schottky 54ALS**	Status	
		Issue	Date
9203/056	Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54ALS576	1	0210
9204/055	Synchronous 4-Bit Binary Counters with Direct Clear, based on types 54ALS161A and 54ALS161B	1	0210
9204/057	Synchronous 4-Bit Binary Counter with Synchronous Clear, based on types 54ALS163A and 54ALS163B	1	0210
9204/058	Synchronous 4-Bit Up/Down Binary Counter (Dual Clock with Clear), based on type 54ALS193	1	0210
9205/012	3-Line to 8-Line Decoders/Demultiplexers, based on type 54ALS138	1	0210
9205/025	3-Line to 8-Line Decoders/Demultiplexers with Address Latches and Inverted Outputs, based on type 54ALS137	1	0210
9209/003	8-Bit Identity Comparators, based on type 54ALS520	1	0210
9306/039	8-Bit Universal Shift/Storage Registers with Direct Overriding Clear and 3-State Outputs, based on type 54ALS299	1	0210
9401/021	Quad 2-Input Positive AND Buffers with Increased Output Drive, based on types 54ALS1008 and 54ALS1008A	1	0210
9401/023	Hex Inverters with Open Collector Outputs, based on types 54ALS05 and 54ALS05A	1	0210
9401/024	Hex Inverters, based on types 54ALS04A and 54ALS04B	1	0210
9401/028	Octal Buffers and Line Drivers with 3-State Outputs, based on types 54ALS241 and 54ALS241A	1	0210
9401/029	Octal Buffers and Line Drivers with 3-State Outputs, based on type 54ALS541	1	0210
9401/031	Triple 3-Input Positive NAND Buffers with Increased Output Drive, based on type 54ALS1010A	1	0210
9401/032	Octal Buffers and Line Drivers with 3-State Outputs, based on type 54ALS240A	1	0210
9401/036	Hex Inverter Buffers with Increased Output Drive, based on type 54ALS1004	1	0210
9402/005	Octal Buffers and Line Drivers with 3-State Outputs, based on types 54ALS244A and 54ALS244B	1	0210
9405/005	Octal Bus Transceivers with 3-State Outputs, based on type 54ALS245A & 645A	1	0210
9405/009	Octal Bus Transceivers with Independant Registers for A and B Buses True Data Paths and 3-State Buffered Outputs, based on type 54ALS646	1	0210
9405/018	Quad Bus Transceivers with Inverted 3-State Outputs, based on type 54ALS242B	1	0210
9405/019	Quad Bus Transceivers with 3-State Ouputs, based on type 54ALS243A	1	0210
9408/029	1-of-8 Data Selectors/Multiplexers, based on type 54ALS151	1	0210
9408/031	Dual 4-Line to 1-Line Data Selectors/Multiplexers with 3-State Outputs, based on type 54ALS253	1	0210
9408/032	Quad 2-Line to 1-Line Data Selector/Multiplexer with 3-State Outputs, based on type 54ALS257	1	0210
9408/033	Quad 2-Line to 1-Line Data Selector/Multiplexer with 3-State Outputs, based on type 54ALS258	1	0210
9408/034	Dual 4-Line to 1-Line Data Selector/Multiplexer with 3-State Outputs, based on type 54ALS353	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
	Class of Publication: 18 **Complex Devices (Microprocessors...)**	Issue	Date
9512/001	CMOS Digital Signal Processor, based on type 320C25	1	0210
9521/001	CMOS 8-Bit Micro-Controller, based on type 80C31	1	0210
9543/003	Silicon On Sapphire CMOS Programmable Direct Memory Access Controller, based on type MAS28137	1	0210
9544/003	Silicon On Sapphire CMOS Programmable Communication Interface, based on type MAS28151	1	0210
9544/004	Silicon on Sapphire CMOS Virtual Channel Multiplexer, based on type 12396	1	0210
9544/005	Silicon on Sapphire CMOS Virtual Channel Assembler, based on type 12399	1	0210
9544/006	CMOS Silicon on Sapphire OBDH-Remote Bus Interface RBI Circuit with 3-State Outputs, based on type 12663	1	0210
9544/007	Integrated Circuit, Monolithic, Silicon on Sapphire, CMOS, Local Time Management System Based on Type MS-13196	1	0210

ESCC Document or Spec. Number	Specification Title	Status	
		Issue	Date
	Class of Publication: 85 **54HSC/54HST- Silicon on Sapphire HCMOS**		
9201/144	Silicon on Sapphire Monolithic HCMOS Quad 2-Input AND Gates, based on types 54HSC08 & 54HST08	1	0210
9201/145	Silicon on Sapphire Monolithic HCMOS Quad 2-Input OR Gates, based on types 54HSC32 & 54HST32	1	0210
9401/063	Silicon on Sapphire Monolithic HCMOS Hex Inverters, based on types 54HSC04 & 54HST04	1	0210

ESCC Document or Spec. Number	Specification Title	Status		
	Class of Publication: 81 **CCD PhotoMOS Area Arrays (9610/...)**	Issue	Date	
9610/001	Charge Coupled Devices Silicon Photosensitive Area Array Image Sensor 286Linesx382Pixels, based on type TH7863A	1	0210	
9610/003	Charge Coupled Devices, Silicon, Photosensitive, Area Array, Image Sensor, 286 Lines x 382 Pixels, Based on Type Th 7863D	1	0210	