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# CAPACITORS, FIXED, CHIPS, CERAMIC DIELECTRIC, TYPE II

# **BASED ON TYPE 0603**

# **ESCC Detail Specification No. 3009038**

Issue 1	April 2012



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ISSUE 1

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**ISSUE 1** 

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# 1. <u>GENERAL</u>

### 1.1 <u>SCOPE</u>

This specification details the ratings, physical and electrical characteristics, test and inspection data for Capacitors, Fixed, Chips, Ceramic Dielectric, Type II, based on Type 0603. It shall be read in conjunction with ESCC Generic Specification No. 3009, the requirements of which are supplemented herein.

1.2 <u>COMPONENT TYPE VARIANTS AND RANGE OF COMPONENTS</u> The variants and the range of components covered by this specification are given in Table 1(a).

### 1.3 MAXIMUM RATINGS

The maximum ratings, which shall not be exceeded at any time during use or storage, applicable to the components specified herein, are as scheduled in Table 1(b).

- 1.4 <u>PARAMETER DERATING INFORMATION</u> Not applicable
- 1.5 <u>PHYSICAL DIMENSIONS</u> The physical dimensions of the capacitors specified herein are shown in Figure 2.
- 1.6 <u>FUNCTIONAL DIAGRAM</u> The functional diagram for the capacitors specified herein is shown in Figure 3.

#### 2. APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

(a) ESCC Generic Specification No. 3009 for Capacitors, Fixed, Chips, Ceramic Dielectric, Types I and II.

#### 3. TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic specification No. 21300 shall apply.



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Variant	Style	Capacitance Range,	Temperature Characteristic	Terminal Material and Finish		Weight	
Number	Otyle	Tolerance, Rated Voltage	for V <sub>T</sub> =U <sub>R</sub> (%)	End Terminations	Termination Finish	Max (g)	
01	0603	Note 1	-30, +20	Ag/Pd	No Finish	0.1	
03	0603	Note 1	-30, +20	Ag/Pd/Pt	No finish	0.1	
06	0603	Note 1	-30, +20	Ag + Ni barrier	Sn/Pb coating (Note 3)	0.1	
07	0603	Note 1	Not Applicable (Note 2)	Ag + Ni barrier	Sn/Pb coating (Note 3)	0.1	
08	0603	Note 1	-30, +20	Ag + Ni barrier	Au plating	0.1	
09	0603	Note 1	Not Applicable (Note 2)	Ag/Pd	No Finish	0.1	
10	0603	Note 1	Not Applicable (Note 2)	Ag/Pd/Pt	No finish	0.1	
11	0603	Note 1	Not Applicable (Note 2)	Ag + Ni barrier	Au plating	0.1	

## TABLE 1(a) – COMPONENT TYPE VARIANTS AND RANGE OF COMPONENTS

#### NOTES:

1. Specified rated voltages, capacitance values and tolerances are as follows:

Rated Voltage U <sub>R</sub>				Value Series	
(V)	Min	Max	(± %)		
200	100	5600	5 10 20	E24 E12 E6	
100	10	12000	5 10 20	E24 E12 E6	
50	10	47000	5 10 20	E24 E12 E6	
25	330	56000	5 10 20	E24 E12 E6	
16	330	100000	5 10 20	E24 E12 E6	

2. X7R dielectric. Temperature Characteristic is typically -60%.

3. Sn/Pb coating, near eutectic with minimum 10% Pb.



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TABLE 1	(b)	– MAXIMUM RATINGS
	<u>`~</u> /	

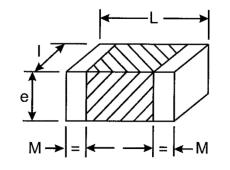
No.	Characteristics	Symbols	Maximum Ratings	Units	Remarks
1	Rated Voltage	$U_R$	16, 25, 50, 100, 200	V	Note 1
2	Operating Temperature Range	T <sub>op</sub>	-55 to +125	°C	Without derating. T <sub>amb</sub>
3	Storage Temperature Range	T <sub>stg</sub>	-55 to +125	°C	
4	Soldering Temperature	T <sub>sol</sub>	+260	°C	Note 2

- NOTES:1. As required; See Table 1(a).2. Duration 10s maximum.

# FIGURE 1 – PARAMETER INFORMATION

#### Not applicable

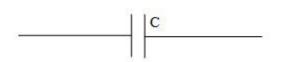
### FIGURE 2 – PHYSICAL DIMENSIONS



Cumhala	Dimensions (mm)			
Symbols	Min	Max		
L	1.4	1.8		
I	0.6	1		
е	-	1		
М	0.1	0.5		



# FIGURE 3 – FUNCTIONAL DIAGRAM



#### 4. REQUIREMENTS

#### 4.1 <u>GENERAL</u>

The complete requirements for procurement of the capacitors specified herein shall be as stated in this specification and ESCC Generic Specification No. 3009 for Capacitors, Fixed, Chips, Ceramic Dielectric, Types I and II. Deviations from the Generic Specification, applicable to this specification only, are listed in Para 4.2.

Deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirements and do not affect the components' reliability, are listed in the appendices attached to this specification.

#### 4.2 DEVIATIONS FROM GENERIC SPECIFICATION

- 4.2.1 <u>Deviations from Special In-Process Controls</u> None.
- 4.2.2 <u>Deviations from Final Production Tests (Chart II)</u>
  - (a) Para. 9.4.4, Electrical Measurements at Room Temperature.
    - Capacitance: In order to de-age the component, preconditioning may be performed prior to measurement of Capacitance, at the Manufacturer's discretion. Preconditioning shall consist of exposure to T<sub>amb</sub> = +150°C for a duration of 1 hour minimum followed by recovery at standard atmospheric conditions for 24 hours. Traceability of preconditioning shall be included in the data documentation.
    - Insulation Resistance: the duration of the applied voltage shall be sufficient to verify the specified insulation resistance limit is met, up to 1 minute maximum.
    - Voltage Proof: the duration of the applied voltage shall be sufficient to verify no component breakdown or flashover, up to 1 minute maximum.
  - (b) Para. 9.4.3, Electrical Measurements at High and Low Temperatures: For Variants 07, 09, 10 and 11, measurements of Temperature Characteristic with rated voltage applied, in accordance with Para. 9.12 are not applicable.



# 4.2.3 Deviations from Burn-in and Electrical Measurements (Chart III)

- (a) Para. 9.4.4, Electrical Measurements at Room Temperature.
  - Capacitance: In order to de-age the component, preconditioning may be performed prior to measurement of Capacitance, at the Manufacturer's discretion. Preconditioning shall consist of exposure to  $T_{amb}$  = +150°C for a duration of 1 hour minimum followed by recovery at standard atmospheric conditions for 24 hours. Traceability of preconditioning shall be included in the data documentation.
  - Insulation Resistance: the duration of the applied voltage shall be sufficient to verify the specified insulation resistance limit is met, up to 1 minute maximum.
  - Voltage Proof: the duration of the applied voltage shall be sufficient to verify no component breakdown or flashover, up to 1 minute maximum.
- (b) Para. 9.4.3, Electrical Measurements at High and Low Temperatures: For Variants 07, 09, 10 and 11, measurements of Temperature Characteristic with rated voltage applied, in accordance with Para. 9.12 are not applicable.

### 4.2.4 <u>Deviations from Qualification Tests (Chart IV)</u>

- (a) Para. 9.4.1.1, Capacitance: In order to de-age the component, preconditioning may be performed prior to measurement of Capacitance, at the Manufacturer's discretion. Preconditioning shall consist of exposure to  $T_{amb} = +150^{\circ}C$  for a duration of 1 hour minimum followed by recovery at standard atmospheric conditions for 24 hours. Traceability of preconditioning shall be included in the data documentation.
- (b) Para. 9.12, Temperature Characteristic: For Variants 07, 09, 10 and 11, measurements of Temperature Characteristic with rated voltage applied are not applicable.

#### 4.2.5 <u>Deviations from Lot Acceptance Tests (Chart V)</u>

- (a) Para. 9.4.1.1, Capacitance: In order to de-age the component, preconditioning may be performed prior to measurement of Capacitance, at the Manufacturer's discretion. Preconditioning shall consist of exposure to  $T_{amb} = +150^{\circ}C$  for a duration of 1 hour minimum followed by recovery at standard atmospheric conditions for 24 hours. Traceability of preconditioning shall be included in the data documentation.
- (b) Para. 9.12, Temperature Characteristic: For Variants 07, 09, 10 and 11, measurements of Temperature Characteristic with rated voltage applied are not applicable.

#### 4.3 MECHANICAL REQUIREMENTS

#### 4.3.1 Dimension Check

The dimensions of the components specified herein shall be verified in accordance with the requirements set out in Para. 9.3 of ESCC Generic Specification No. 3009 and shall conform to those shown in Figure 2 of this specification.

#### 4.3.2 Weight

The maximum weight of the components specified herein shall be as given in Table 1(a).

#### 4.3.3 Adhesion

The requirements for adhesion are specified in Para. 9.5 of ESCC Generic Specification No. 3009.



#### 4.4 MATERIALS AND FINISHES

The materials and finishes shall be as specified herein. Where a definite material is not specified, a material which will enable the capacitors specified herein to meet the performance requirements of this specification shall be used. Acceptance or approval of any constituent material does not guarantee acceptance of the finished product.

#### 4.4.1 <u>Terminal Material and Finish</u> The terminal material and finish shall be as specified in Table 1(a).

#### 4.5 <u>MARKING</u>

#### 4.5.1 General

The marking of all components delivered to this specification shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and the following paragraphs. When the component is too small to accommodate all of the marking specified, as much as space permits shall be marked and the marking information, in full, shall accompany each component in its primary package.

The information to be marked and the order of precedence shall be as follows:

- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- (b) The ESCC Component Number.
- (c) Electrical Characteristics and Ratings.
- (d) Traceability Information

#### 4.5.2 ESCC Component Number

The ESCC Component Number shall be constituted and marked as follows:

Example: 300903801B

- Detail Specification Number: 3009038
- Type Variant (see Table 1(a)): 01
- Testing Level (B or C, as applicable): B

#### 4.5.3 Electrical Characteristics and Ratings

The electrical characteristics and ratings to be marked in the following order of precedence are:

- (a) Capacitance value.
- (b) Tolerance.
- (c) Rated Voltage.

The information shall be constituted and marked as follows:

Example: 102KC

- Capacitance value (1nF): 102
- Tolerance (±10%): K
- Rated Voltage (50V): C



# 4.5.3.1 Capacitance Value

The capacitance value  $C_n$  shall be expressed by means of the following codes. The unit quantity for marking shall be in picofarads.

Capacitance Value C <sub>n</sub> (pF)	Code
XX	XX0
XX 10 <sup>1</sup>	XX1
XX 10 <sup>2</sup>	XX2
XX 10 <sup>3</sup>	XX3
XX 10 <sup>4</sup>	XX4

## 4.5.3.2 Tolerance

The tolerance on capacitance value shall be indicated by the code letters specified hereafter.

Tolerance	Code Letter
±5%	J
±10%	К
±20%	М

# 4.5.3.3 Rated Voltage

The rated voltage  $U_R$  shall be indicated by the code letters specified hereafter.

Rated Voltage U <sub>R</sub> (V)	Code Letter
16	Х
25	А
50	С
100	E
200	G

# 4.5.3.4 Traceability Information

Traceability information shall be marked in accordance with the requirements of ESCC Basic Specification No. 21700.

# 4.6 <u>ELECTRICAL MEASUREMENTS</u>

- 4.6.1 <u>Electrical Measurements at Room Temperature</u> The parameters to be measured at room temperature are scheduled in Table 2. Unless otherwise specified the measurements shall be performed at  $T_{amb}$  = +22±3°C.
- 4.6.2 <u>Electrical Measurements at High and Low Temperatures</u> The parameters to be measured at high and low temperatures are scheduled in Table 3. Unless otherwise specified the measurements shall be performed at T<sub>amb</sub> = +125(+0 -5) °C and -55(+5 -0) °C respectively.
- 4.6.3 <u>Circuits for Electrical Measurements</u> Not applicable.



#### 4.7 BURN-IN TESTS

#### 4.7.1 Parameter Drift Values

The parameter drift values applicable to Burn-in are as specified in Table 4 of this specification. Unless otherwise stated, measurements shall be performed at  $T_{amb}$  = +22±3°C.

The parameter drift values ( $\Delta$ ) applicable to the parameters scheduled shall not be exceeded. In addition to these drift value requirements for a given parameter, the appropriate limit values specified in Table 2 shall not be exceeded.

#### 4.7.2 <u>Conditions for Burn-in</u>

The requirements for Burn-in are specified in Section 7 of ESCC Generic Specification No. 3009. The conditions for Burn-in shall be as specified in Table 5 of this specification.

Upon completion of Burn-in, a recovery period of 24 hours minimum is necessary before performance of the end-measurements.

4.7.3 <u>Electrical Circuit for Burn-in (Figure 5)</u> Not applicable

No	Characteristics	Symbolo	ESCC 3009	Lin	nits	Linito	Domorko
No. Characteristics		Symbols	Test Conditions	Min	Max	Units	Remarks
1	Capacitance	С	Para. 9.4.1.1	See Table 1(a)		pF	
2	Tangent of Loss Angle	Тցδ	Para. 9.4.1.2	-	250 x 10 <sup>-4</sup>	-	
3	Insulation	Ri	Para. 9.4.1.3	100	-	GΩ	For $C_n \le 10000 pF$
	Resistance	Ri x C <sub>n</sub>		1000	-	S	For $C_n > 10000 pF$
4	Voltage Proof	VP	Para. 9.4.1.4	2.5U <sub>R</sub>	-	V	

### TABLE 2 – ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE



# TABLE 3 – ELECTRICAL MEASUREMENTS AT HIGH AND LOW TEMPERATURES

No.	Characteristics	Symbolo	ESCC 3009 Test Conditions	Lin	nits	Units	Remarks
NO.	Characteristics	Symbols	ESCC 3009 Test Conditions	Min	Max	Units	Remains
3	Insulation Resistance	Ri	Para. 9.4.1.3 T <sub>amb</sub> =+125(+0 -5)°C	10 -		GΩ	For C <sub>n</sub> ≤ 10000pF Notes 1 and 2
		Ri x C <sub>n</sub>		100	-	s	For C <sub>n</sub> > 10000pF Notes 1 and 2
5(i)	Temperature Characteristic	TC	Para. 9.12 For $V_T$ = no voltage applied	-20	+20	%	5 parts for each capacitance value Notes 2, 5
			For $V_T = U_R$	Note 4			
5(ii)	Temperature Characteristic	TC	Para. 9.12 For $V_T$ = no voltage applied	-20	+20	%	5 parts for each dielectric lot Notes 3, 5
			For $V_T = U_R$	Note 4			NOLES 3, 3

#### NOTES:

- 1. Single sample. Inspection Level S3, AQL 2.5%.
- 2. Applicable to Level 'B' only.
- 3. Applicable to Level 'C' only.
- 4. See Table 1(a) for TC limits for  $V_T = U_R$ .
- 5. In the event of any failure a 100% inspection may be performed. In the case of a 100% inspection, a 1% total percent defective is allowed.

# FIGURE 4 – CIRCUIT FOR ELECTRICAL MEASUREMENTS

Not applicable.

#### TABLE 4 – PARAMETER DRIFT VALUES

N	0.	Characteristics	Symbols	Spec. and/or Test Method	Test Conditions	Change Limits (∆)	Units
·	1	Capacitance Change	∆C/C	As per Table 2	As per Table 2	±15	%

#### TABLE 5 - CONDITIONS FOR BURN-IN AND OPERATING TESTS

No.	Characteristics	Symbols	Conditions	Units
1	Ambient Temperature	T <sub>amb</sub>	+125(+0 -3)	°C
2	Test Voltage	V <sub>T</sub>	2U <sub>R</sub>	V

#### FIGURE 5 – ELECTRICAL CIRCUIT FOR BURN-IN AND OPERATING LIFE

Not applicable.



- 4.8 <u>ENVIRONMENTAL AND ENDURANCE TESTS (CHARTS IV AND V OF ESCC</u> <u>GENERIC SPECIFICATION NO. 3009)</u>
- 4.8.1 <u>Measurements and Inspections on Completion of Environmental Tests</u> The parameters to be measured and inspections to be performed on completion of environmental tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at  $T_{amb}$ =+22±3°C.
- 4.8.2 <u>Measurements and Inspections at Intermediate Points During Endurance Tests</u> The parameters to be measured and inspections to be performed at intermediate points during endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at  $T_{amb}$ =+22±3°C.
- 4.8.3 <u>Measurements and Inspections on Completion of Endurance Tests</u> The parameters to be measured and inspections to be performed on completion of endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at  $T_{amb}$ =+22±3°C.
- 4.8.4 <u>Conditions for Operating Life (Part of Endurance Testing)</u> The requirements for Operating Life testing are specified in Section 9 of ESCC Generic Specification No. 3009. The conditions for Operating Life testing shall be as specified in Table 5 for the Burn-in test.
- 4.8.5 <u>Electrical Circuit for Operating Life Tests (Figure 5)</u> Not applicable.

No.	ESCC Generic S	pec. N° 3009	Measurements A	Symbols	Limits		Units	
	Environmental And Endurance Tests (1)	Test Method And Conditions	Identification	Conditions		Min.	Max.	
01	Mounting	Para 9.15	Final Examination Terminals Final Measurements	Good Tinning	-	-   -		-
			Capacitance Tangent of Loss Angle	Table 2 Item 1 Table 2 Item 2	C Tgõ	Record Values Table 2 Item 2		pF -
			Insulation Resistance	Table 2 Item 3	Ri	Table 2 Item 3		GΩ
02	Adhesion	Para. 9.5	Final Examination Visual Examination Capacitance	Damage or loosening Table 2 Item 1	- C	- Table 2	- 2 Item 1	- pF
03	Solderability	Para. 9.6	Final Examination	Para. 9.6.	-	-	-	-

#### TABLE 6 – MEASUREMENTS AND INSPECTIONS ON COMPLETION OF ENVIRONMENTAL TESTS AND AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING



No.	ESCC Generic S	pec. N° 3009	Measurements A	And Inspections	Symbols	Lin	nits	Units
	Environmental And Endurance Tests (1)	Test Method And Conditions	Identification	Conditions		Min.	Max.	
04	Rapid Change of Temperature	Para. 9.7	Initial Measurements Capacitance	Table 2 Item 1	С	Item 1	Item 1 Value	
			Final Measurements Visual Examination	Recovery period 24±2 hours No damage	-	-	-	-
			Capacitance Change	Table 2 Item 1	<u>∧C</u> C	-10	+10	%
			Tangent of Loss Angle	Table 2 Item 2	Tgδ	-	(2)	-
05	Climatic Test Sequence	Para. 9.8	Initial Measurements					
			Capacitance	Table 2 Item 1	С	Item 1	Value	pF
			Final Measurements	Recovery period 1 to 24 hours			1	
			Visual Inspection	Para. 9.8.6	-	-	-	-
			Capacitance Change	Table 2 Item 1	<u>лС</u> С	-10	+10	%
			Tangent of Loss Angle	Table 2 Item 2	Tgδ	-	(2)	-
			Insulation Resistance	Table 2 Item 3	Ri	3 (3)	-	GΩ
06	Damp Heat Steady State	Para. 9.9	Initial Measurements Capacitance	Table 2 Item 1	С	Item 1	Value	pF
			Final Measurements Visual Inspection	Recovery period 6 to 24 hours No damage	_	-	-	-
			Capacitance Change	Table 2 Item 1	ΔC	-10	+10	%
			Tangent of Loss Angle	Table 2 Item 2	<u>ΔC</u> C Tgδ	-	(2)	-
			Insulation Resistance	Table 2 Item 3	Ri	3 (3)	-	GΩ



No.	ESCC Generic S	pec. N° 3009	Measurements A	Measurements And Inspections			nits	Units
	Environmental And Endurance Tests (1)	Test Method And Conditions	Identification	Conditions		Min.	Max.	
07	Operating Life	Para. 9.10	Initial Measurements					
			Capacitance	Table 2 Item 1	С	Item 1	Value	pF
			Intermediate Measurements (@ 1000hours Chart IV)	Recovery period 1 hour min				
			Capacitance Change	Table 2 Item 1	<u>∧C</u> C	-15	+15	%
			Insulation Resistance	Table 2 Item 3	Ri	10 (4)	-	GΩ
			Final Measurements	Recovery period 24±2 hours				
			Capacitance Change	Table 2 Item 1	<u>∧C</u> C	-15	+15	%
			Tangent of Loss Angle	Table 2 Item 2	Tgδ	-	(2)	-
			Insulation Resistance	Table 2 Item 3	Ri	10 (4)	-	GΩ
			Voltage Proof	Table 2 Item 4	VP	Table 2	ltem 4	V
			Visual Examination	No damage	-	-	-	-
08	Temperature Characteristic	Para. 9.12	Capacitance Changes	Table 3 Item 5(i) or 5(ii)	TC	Tab Item 5(i	le 3 ) or 5(ii)	%

# NOTES:

- 1. The tests in this Table refer to either Chart IV or V and shall be used as applicable.

- 2. Twice the value specified in Table 2. 3. Limit applies for  $C_n \le 10000$  pF. For  $C_n > 10000$  pF, Ri x  $C_n = 30$  s minimum. 4. Limit applies for Cn  $\le 10000$  pF. For Cn > 10000 pF, Ri x Cn = 100 s minimum.