LIST OF DISCONTINUED ESCC

DOCUMENTS AND SPECIFICATIONS

ESCC REP002

ISSUE 7
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CHANGE DESCRIPTION

This issue supersedes Issue 6, dated December 2011 and incorporates changes as per DCR566, DCR600, DCR625 and DCR661.

INTRODUCTION

This list comprises all discontinued ESCC documents and specifications which have been transferred from REP001 and are not subject to configuration control.

The REP002 specifications are former REP001 specifications which are covering obsolete components, technologies, test methods... no longer used or existing. These specifications are no longer updated and distributed. They do not represent the current ESCC policy and therefore cannot be used for procurement or manufacturing of parts.

The ESCC documents and specifications are classified according to 5 levels, viz:

LEVEL 0 - Policy

LEVEL 1 - 1(0) Organisation Documents.

        1(1) Support Documents.

        1(2) Implementation Documents.

LEVEL 2 - Basic Specifications.

LEVEL 3 - Generic Specifications.

LEVEL 4 - Detail Specifications.

However, to facilitate retrieval, they are not classified according to the above levels, but by the various classes of publication listed in the table of contents.

The ESCC documents are available from the ESCC web site: https://spacecomponents.org
The ESCC specifications are available from the ESCIES web site: https://escies.org.

CD-ROM images are also provided on the ESCIES web site.

If access to the web sites is not possible, queries may be directed to the secretariat:

ESCC Executive Secretariat
ESA/ESTEC (TEC-QES)
P.O. Box 299
2200 AG Noordwijk
The Netherlands

Specification Family :   **BASIC SPECIFICATIONS**

Class of Publication Code :   **3   BASIC SPECIFICATIONS**

Number		Specification Title	Date
23000	1	Requirements for the Extension of Qualification Approval of Standard Electronic Components for Space Application	2010-02-01

Number		Specification Title	Date
4001/001	1	Resistors Fixed Film Non-Hermetically Sealed, based on type RNC55	2002-10-31
4001/002	1	Resistors Fixed Film Non-Hermetically Sealed, based on type RNC60	2002-10-31
4001/003	1	Resistors Fixed Film Non-Hermetically Sealed, based on type RNC65	2002-10-31
4001/005	1	Resistors Fixed Film Non-Hermetically Sealed, based on type RLR05	2002-10-31
4001/006	1	Resistors Fixed Film Non-Hermetically Sealed, based on type RLR07	2002-10-31
4001/007	1	Resistors Fixed Film Non-Hermetically Sealed, based on type RLR20	2002-10-31
4001/009	1	Resistors Fixed Film Non-Hermetically Sealed, based on type RNC50	2002-10-31
4001/010	1	Resistors Fixed Film Non-Hermetically Sealed, based on type RNC70	2002-10-31
4001/016	1	Resistors Fixed Chips Film, based on types CHP HR0505	2002-10-31
4001/017	1	Resistors Fixed Chips Film, Based on types CHP HR0705	2002-10-31
4001/018	1	Resistors Fixed Chips Film, based on types CHP HR0805	2002-10-31
4001/019	1	Resistors Fixed Chips Film, based on types CHP HR1010	2002-10-31
4001/020	1	Resistors Fixed Chips Film, based on type CHP HR1206	2002-10-31
4001/021	1	Resistors Fixed Surface Mount Film Non-Hermetically sealed, based on types SMC3	2002-10-31

Number		Specification Title	Date
4001/024	1	Resistors, Fixed, Thick Film,Radial Leads and Surface Mount, Non-Hermetically Sealed, based on type RTO HR 50	2002-10-31

Specification Family :   **RESISTORS AND THERMISTORS**

Class of Publication Code :   **57 THERMISTORS**

Number		Specification Title	Date
4006/001	1	Thermistors (Resistors Thermally Sensitive) Range 2000 to 15000 Ohms at +25 degree C with a Temperature Range of -40 to +160 degree C	2011-11-17
4006/012	1	Thermistors(thermally sensitive resistors) NTC Chip Style, based on series 196-xxxxAG-001	2011-11-17

Specification Family :   **INDUCTORS**

Class of Publication Code :   **12 R.F. COILS**

Number		Specification Title	Date
3201/006	1	R.F. Coil Fixed, based on type S150	2002-10-31

Specification Family :   **CAPACITORS**

Class of Publication Code :   **90 CAPACITOR MICROWAVE SILICON DICE (5711/...)**

Number		Specification Title	Date
5711/001	1	Capacitors Microwave Silicon Dice Mos, based on Types 101MC,201MC, 250MC, 400MC, 401Mc and 501MC	2002-10-31

Specification Family :   **RELAYS AND SWITCHES**

Class of Publication Code :   **16 RELAYS NON-LATCHING**

Number		Specification Title	Date
3601/001	2	Relays Electromagnetic Non-Latching 28Vdc 10A 2PDT	2004-10-31

Specification Family :   **RELAYS AND SWITCHES**

Class of Publication Code :   **17 RELAYS LATCHING**

Number		Specification Title	Date
3602/001	2	Relays Electromagnetic Latching 28Vdc 10A 2PDT	2004-09-23

Specification Family :   **RELAYS AND SWITCHES**

Class of Publication Code :   **59 TOGGLE SWITCHES**

Number		Specification Title	Date
3701/001	1	Toggle Switches, based on series 12100 and 11100	2002-10-31

Specification Family : **CRYSTALS AND SAW DEVICES**

Class of Publication Code : **69 SURFACE ACOUSTIC (SAW) DEVICES**

Number		Specification Title	Date
3502/001	1	Surface Acoustic Wave (SAW) Bandpass Filter Operating in the Frequency Range 10-200 MHz	2011-11-17

Specification Family :   **MISCELLANEOUS PASSIVE**

Class of Publication Code :   **58 ATTENUATORS AND LOADS**

Number		Specification Title	Date
3403/007	1	Flange Loads High Power 50 Ohms, based on type EMC8745	2002-10-01

Specification Family :   **DISCRETE SEMICONDUCTORS**

Class of Publication Code :   **38 DIODES RECTIFIER**

Number		Specification Title	Date
5103/028	1	Diode Rectifier Schottky, based on types 1N5817 to 1N5819	2011-11-17

Specification Family : **INTEGRATED CIRCUITS**

Class of Publication Code : **18 COMPLEX DEVICES (MICROPROCESSORS...)**

Number		Specification Title	Date
9512/001	1	CMOS Digital Signal Processor, based on type 320C25	2002-10-01
9521/001	1	CMOS 8-bit Micro-Controller, based on type 80C31	2002-10-01
9543/003	1	Silicon On Sapphire CMOS Programmable Direct Memory Access Controller, based on type MAS28137	2002-10-31
9544/003	1	Silicon On Sapphire CMOS Programmable Communication Interface, based on type MAS28151	2002-10-31
9544/004	1	Silicon on Sapphire CMOS Virtual Channel Multiplexer, based on type 12396	2002-10-31
9544/005	1	Silicon on Sapphire CMOS Virtual Channel Assembler, based on type 12399	2002-10-31
9544/006	1	CMOS Silicon on Sapphire OBDH-Remote Bus Interface RBI Circuit with 3-State Outputs, based on type 12663	2002-10-31
9544/007	1	Integrated Circuit, Monolithic, Silicon on Sapphire, CMOS, Local Time Management System Based on Type MS-13196	2002-10-31

Specification Family :　　　**INTEGRATED CIRCUITS**

Class of Publication Code :　　**42 IC'S SIL MON BI: LINEAR**

Number		Specification Title	Date
9101/001	1	Operational Amplifier, based on type LM 101A	2002-10-31
9101/005	1	Operational Amplifier, based on type LM108A	2002-10-31
9101/006	1	Operational Amplifier Buffer, based on type LM 118	2002-10-31
9101/008	1	Dual Bipolar Operational Amplifiers, based on type LM747	2002-10-31
9101/009	1	Fast Sample and Hold Operational Amplifier, based on type HA-2420	2002-10-31
9101/010	1	Operational Amplifier, based on type HA2520-2	2002-10-31
9101/011	1	Low Power Quad Bipolar Operational Amplifier, based on type LM124 and LM124A	2002-10-31
9101/017	1	JFET Input Operational Amplifier, based on type LF155 LF155A LF156 LF156A and LF157	2002-10-31
9101/022	1	Bipolar Operational Amplifiers, based on type LM11	2002-10-31
9101/033	1	Bipolar Operational Amplifier, based on type OP400A	2002-10-31
9102/001	1	Voltage Regulator, based on type LM104	2002-10-31
9102/002	1	Voltage Regulator, based on type LM105	2002-10-31
9102/005	1	3-Terminal Adjustable Positive Regulator, based on type LM117	2002-10-31
9102/007	1	3-Terminal Adjustable Negative Regulator, based on type LM137	2002-10-31

Specification Family : **INTEGRATED CIRCUITS**

Class of Publication Code : **42 IC'S SIL MON BI: LINEAR**

Number		Specification Title	Date
9102/013	1	High Efficiency Linear Regulator, based on type UC1834	2002-10-31
9103/002	1	Voltage Comparator, based on type LM111	2002-10-31
9103/004	1	Quad Bipolar Voltage Comparator, based on types LM139 and LM139A	2002-10-31
9108/001	1	Operational Amplifier, based on type LM110	2002-10-31
9108/002	1	Precision Timer, based on type LM122	2002-10-31
9108/007	1	Regulating Pulse Width Modulator, based on type SG1524	2002-10-31
9108/011	1	Regulating Pulse Width Modulator, based on type SG1525A	2002-10-31
9108/018	1	Integrated Circuits Silicon Monolithic Current Mode Pulse Width Modulator Controller, based on type UC1843	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **44 IC'S SIL MON BI LOW POWER SCHOTTKY: 54LS SERIES**

Number		Specification Title	Date
9201/014	1	Quad 2-Input Exclusive OR Gates, based on types 54LS86 and 54LS86A	2002-10-31
9201/015	1	Quad 2-Input Positive NAND Gate, based on type 54LS00	2002-10-31
9201/016	1	Quad 2-Input Positive NOR Gate, based on type 54LS02	2002-10-31
9201/017	1	Triple 3-Input Positive NAND Gate, based on type 54LS10	2002-10-31
9201/018	1	Dual 4-Input Positive NAND Gate, based on type 54LS20	2002-10-31
9201/019	1	8-Input Positive NAND Gate, based on type 54LS30	2002-10-31
9201/025	1	2-Wide 3-Input 2-Wide 2-Input Positive AND/OR Invert Gate, based on type 54LS51	2002-10-31
9201/026	1	4-Wide 3-2-2-3 Input Positive AND/OR Invert Gate, based on type 54LS54	2002-10-31
9201/028	1	Triple 3-Input Positive NOR Gate, based on type 54LS27	2002-10-31
9201/035	1	Quad 2-Input Positive AND Gate, based on type 54LS08	2002-10-31
9201/036	1	Quadruple 2-Input High Voltage Interface Positive NAND Gate, based on type 54LS26	2002-10-31
9201/038	1	Quad 2-Input Positive OR Gate, based on type 54LS32	2002-10-31
9201/039	1	Dual 4-Input Positive NAND Gate, based on type 54LS40	2002-10-31
9201/049	1	Triple 3-Input Positive AND Gate, based on type 54LS11	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **44 IC'S SIL MON BI LOW POWER SCHOTTKY: 54LS SERIES**

Number		Specification Title	Date
9201/050	1	Quad 2-Input Exclusive NOR Gates, based on type 54LS266	2002-10-31
9201/053	1	Dual 4-Input Positive and Gate, based on type 54LS21	2002-10-31
9201/069	1	Quad 2-Input Positive NAND Gate with Open Collector Output, based on type 54LS03	2002-10-31
9201/073	1	Quad 2-Input Positive AND Gate with Open Collector Output, based on type 54LS09	2002-10-31
9201/081	1	Quad 2-Input Exclusive OR Gates, based on type 54LS386 and 54LS386A	2002-10-31
9202/005	1	Arithmetic Logic Unit/Function Generator, based on type 54LS181	2002-10-31
9202/009	1	Hex Inverter, based on type 54LS04	2002-10-31
9202/010	1	Quad 2-Line-to-1-Line Selectors/Multiplexers, based on type 54LS257A	2002-10-31
9202/013	1	Hex Schmitt Trigger Inverter, based on type 54LS14	2002-10-31
9202/015	1	1-of-8 Data Selectors/Multiplexers, based on type 54LS151	2002-10-31
9202/016	1	Dual 4-Line-to-1-Line Data Selector/Multiplexer, based on type 54LS153	2002-10-31
9202/017	1	Quad 2-Input Positive NAND Buffer with Open Collector Outputs, based on type 54LS38	2002-10-31
9202/027	1	Quad 2-Line-to-1-Line Selectors/Multiplexers, based on type 54LS157	2002-10-31
9202/028	1	Quad 2-Line-to-1-Line Selectors/Multiplexers, based on type 54LS158	2002-10-31

Specification Family : **INTEGRATED CIRCUITS**

Class of Publication Code : **44 IC'S SIL MON BI LOW POWER SCHOTTKY: 54LS SERIES**

Number		Specification Title	Date
9202/029	1	Dual 4-line-to-1-Line Data Selectors/Multiplexers with 3-State Outputs, based on type 54LS253	2002-10-31
9202/030	1	4-Bit Binary Full Adder with Fast Carry, based on type 54LS83A	2002-10-31
9202/031	1	4-Bit Magnitude Comparator, based on type 54LS85	2002-10-31
9202/032	1	Low Power Bipolar Schottky 4-Bit Binary Full Adder with Fast Carry, based on type 54LS283	2002-10-31
9202/033	1	Dual Monostable Multivibrators, based on type 54LS221	2002-10-31
9202/052	1	Data Selectors/Multiplexers with 3-State Outputs, based on type 54LS251	2002-10-31
9202/053	1	Quad Set-Reset Latches, based on types 54LS279 and 54LS279A	2002-10-31
9202/054	1	Hex Bus Driver with 3-State Outputs, based on type 54LS367A	2002-10-31
9202/055	1	Hex Bus Driver with 3-State Outputs, based on type 54LS368A	2002-10-31
9202/064	1	Dual Carry-Save Full Adders, based on type 54LS183	2002-10-31
9202/066	1	Crystal Controlled Oscillator, based on type 54LS321	2002-10-31
9202/070	1	8-bit Addressable Latches, based on type 54LS259B	2002-10-31
9203/008	1	4-Bit Bistable Latches with Complementary Outputs, based on type 54LS75	2002-10-31
9203/013	1	Dual D-Type Flip-Flop with Preset and Clear, based on type 54LS74A	2002-10-31

Specification Family : **INTEGRATED CIRCUITS**

Class of Publication Code : **44 IC'S SIL MON BI LOW POWER SCHOTTKY: 54LS SERIES**

Number		Specification Title	Date
9203/014	1	Dual J-K Flip-Flop with Clear, based on type 54LS73A	2002-10-31
9203/015	1	Dual J-K Flip-Flop with Preset and Clear, based on type 54LS76A	2002-10-31
9203/016	1	Dual J-K Flip-Flop with Preset and Clear, based on type 54LS112A	2002-10-31
9203/018	1	Hex D-Type Flip-Flop with Clear, based on type 54LS174	2002-10-31
9203/019	1	Quad D-Type Flip-Flops with Clear, based on type 54LS175	2002-10-31
9203/020	1	Dual J-K Flip-Flops with Clear, based on type 54LS107A	2002-10-31
9203/021	1	Dual J-K Negative Edge-Triggered Flip-Flop with Preset, based on type 54LS113A	2002-10-31
9203/024	1	Dual J-K Flip-Flop with Preset and Clear, based on type 54LS109A	2002-10-31
9203/025	1	Dual J-K Negative Edge-Triggered Flip-Flop, based on type 54LS114A	2002-10-31
9203/030	1	Octal D-Type Flip-Flop with Clear, based on type 54LS273	2002-10-31
9203/031	1	Octal D-Type Flip-Flop, based on type 54LS374	2002-10-31
9203/034	1	Octal D-Type Latches, based on type 54LS373	2002-10-31
9203/035	1	Octal D-Type Flip-Flop with Enable, based on type 54LS377	2002-10-31
9203/039	1	4-Bit Bistable Latches with Complementary Outputs, based on type 54LS375	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **44 IC'S SIL MON BI LOW POWER SCHOTTKY: 54LS SERIES**

Number		Specification Title	Date
9204/007	1	Divide-by-2 and Divide-by-6 Counter, based on type 54LS92	2002-10-31
9204/008	1	4-Bit Binary Counter, based on type 54LS93	2002-10-31
9204/010	1	Decade Counter, based on type 54LS90	2002-10-31
9204/014	1	Synchronous Counter with Direct Clear, based on type 54LS160A	2002-10-31
9204/015	1	Synchronous Counter with Direct Clear, based on type 54LS161A	2002-10-31
9204/016	1	Synchronous 4-Bit Counter with Synchronous Clear, based on type 54LS163A	2002-10-31
9204/017	1	Programmable Synchronous 4-Bit Up/Down Binary Counter, based on type 54LS193	2002-10-31
9204/018	1	Presettable Divide-by-2 and Divide-by-5 Counter, based on type 54LS196	2002-10-31
9204/019	1	Presettable Divide-by-2 and Divide-by-8 Counter, based on type 54LS197	2002-10-31
9204/033	1	Dual 4-Bit Decade and Binary Counter, based on type 54LS390	2002-10-31
9204/034	1	Dual 4-Bit Binary Counter, based on type 54LS393	2002-10-31
9205/003	1	Dual Decoder/Demultiplexer, based on types 54LS139 and 54LS139A	2002-10-31
9205/004	1	Decoder/Demultiplexer, based on type 54LS138	2002-10-31
9205/008	1	BCD-to-Decimal Decoder, based on type 54LS42	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **44 IC'S SIL MON BI LOW POWER SCHOTTKY: 54LS SERIES**

Number		Specification Title	Date
9205/009	1	Dual 2-Line-to-4-Line Decoder/Demultiplexer, based on type 54LS155 and 54LS155A	2002-10-31
9207/002	1	Dual Monostable Multivibrator, based on type 54LS123	2002-10-31
9207/004	1	Retriggerable Monostable Multivibrator, based on type 54LS122	2002-10-31
9208/001	1	9-Bit Odd/Even Parity Generator/Checker, based on type 54LS280	2002-10-31
9301/001	1	4-by-4 Register File, based on type 54LS670	2002-10-31
9306/009	1	4-Bit Parallel Shift Register, based on type 54LS95B	2002-10-31
9306/011	1	8-Bit Parallel-Out Serial Shift Register, based on type 54LS164	2002-10-31
9306/012	1	4-Bit Bidirectional Universal Shift Register, based on type 54LS194A	2002-10-31
9306/019	1	4-Bit Parallel-Access Shift Register, based on type 54LS195A	2002-10-31
9306/020	1	5-Bit Shift Register, based on type 54LS96	2002-10-31
9306/029	1	Parallel Load 8-Bit Shift Register, based on types 54LS165 and 54LS165A	2002-10-31
9306/030	1	8-Bit Shift Register, based on types 54LS166 and 54LS166A	2002-10-31
9306/035	1	16-Bit Shift Register, based on type 54LS673	2002-10-31
9306/036	1	16-bit Shift Register, based on type 54LS674	2002-10-31

Specification Family :   INTEGRATED CIRCUITS

Class of Publication Code :   44 IC'S SIL MON BI LOW POWER SCHOTTKY: 54LS SERIES

Number		Specification Title	Date
9401/007	1	Quad 2-Input Positive NAND Buffer, based on type 54LS37	2002-10-31
9401/008	1	Quad 2-Input Positive NOR Buffer, based on type 54LS28	2002-10-31
9401/009	1	Hex Inverter, based on type 54LS05	2002-10-31
9401/011	1	Quad Bus Buffer Gate with 3-State Outputs, based on type 54LS125A	2002-10-31
9401/014	1	Octal Buffer and Line Driver with 3-State Outputs, based on type 54LS240	2002-10-31
9401/016	1	Octal Buffer/line Driver with 3-State Outputs, based on type 54LS241	2002-10-31
9401/017	1	Hex Buffer Driver, based on type 54LS365A	2002-10-31
9402/003	1	Octal Buffer/Line Driver/Line Receiver with 3-State Outputs, based on type 54LS244	2002-10-31
9405/002	1	Octal Bus Transceivers with 3-State Outputs, based on type 54LS245	2002-10-31
9405/003	1	Quad Bus-Transceiver with Inverted 3-State Outputs, based on type 54LS242	2002-10-31
9405/004	1	Quad Bus Transceivers with Non-Inverted 3-State Outputs, based on type 54LS243	2002-10-31
9406/002	1	BCD-to-Decimal Decoder, based on type 54LS145	2002-10-31
9408/015	1	Quad 2-Input Multiplexer with Storage, based on type 54LS398	2002-10-31
9408/016	1	Quad 2-Input Multiplexer with Storage, based on type 54LS399	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **44 IC'S SIL MON BI LOW POWER SCHOTTKY: 54LS SERIES**

Number		Specification Title	Date
9409/001	1	Dual 4-Input Positive NAND Schmitt Trigger, based on type 54LS13	2002-10-31
9409/004	1	Quad 2-Input Positive NAND Schmitt Trigger, based on type 54LS132	2002-10-31
9410/003	1	8-Line to 3-Line Priority Encoder, based on type 54LS148	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **47 IC'S SIL MON: OTHERS**

Number		Specification Title	Date
9204/063	1	Integrated Circuits Monolithic Silicon on Sapphire CMOS ION Counter, based on type 11918	2002-10-31
9301/015	1	CMOS Silicon Gate Static 16K (2048x8 Bit) Asynchronous Random Access Memory with 3-State Outputs, based on types HM65162 and HM65162B	2002-10-31
9301/018	1	CMOS Silicon Gate Static 16K (16348x1Bit) Asynchronous Random Access Memory with 3-State Outputs, based on types HM65262 and HM65262B	2002-10-31
9301/026	1	CMOS Silicon Gate Static 64K (65536 x1 Bit) Asynchronous Random Access Memory with 3-State Outputs, based on type HM65687	2002-10-31
9301/029	1	CMOS Silicon Gate Static 64K (8192/8 bit) Asynchronous Random Access Memory with 3-State Outputs, based on type HM65664	2002-10-31
9301/030	1	CMOS Silicon Gate Static 256K (32768x8bit) Asynchronous Random Access Memory with 3 State Outputs, based on type HM65656	2002-10-31
9301/032	1	CMOS Silicon Gate Static 9K (1024x9 bit) First-In First-Out (FIFO Memory with 3 State Outputs, based on type M67202FV	2002-10-31
9301/034	1	CMOS Silicon Gate Static 64K (4096x16 bit) Dual Port Memory with 3 State Outputs, based on type M67024EV	2002-10-31
9301/038	1	CMOS Silicon Gate Static 256K (262144x1Bit) Asynchronous Random Access Memory with 3-State Outputs, based on type M65697EV	2002-10-31
9301/041	1	CMOS Silicon Gate Static (512 x 9 Bit) First-In First Out Memory with 3-state Outputs, based on type M67201FV	2002-10-31
9304/003	1	High Performance Programmable Array Logic Circuit, based on type PAL16L8	2002-10-31
9304/005	1	High Performance Programmable Array Logic Circuit, based on type PAL22V10	2002-10-31
9304/006	1	High Performance Programmable Array Logic Circuit, based on type PAL20R8	2002-10-31
9304/007	1	High Performance Programmable Array Logic (PAL) Circuit, based on type PAL20L8	2002-10-31

Specification Family : **INTEGRATED CIRCUITS**

Class of Publication Code : **47 IC'S SIL MON: OTHERS**

Number		Specification Title	Date
9402/012	1	Quad EIA-422 Line Driver with 3 State Outputs, based on type AM26LS31 and 55ALS192	2002-10-31
9403/003	1	Quad EIA-422/423 Line Receiver with 3-State Outputs, based on type AM26LS32	2002-10-31
9407/001	1	8-Bit High Speed Multiplying D/A Converter, based on type DAC08A	2002-10-31
9408/001	1	2-Channel High Speed Driver with SPST JFET Switches, based on type DG181	2002-10-31
9408/002	1	16-Channel CMOS Analogue Multiplexer, based on type HI506A	2002-10-31
9408/003	1	Dual 8-Channel CMOS Analogue Multiplexer, based on type HI507A	2002-10-31
9408/019	1	CMOS 16-Channel Multiplexer Radiation-Hardened, based on type HS1840RH	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **56 IC'S SIL MON BI SCHOTTKY: 54 S SERIES**

Number		Specification Title	Date
9201/056	1	Quad 2-Input Positive NAND Gates, based on type 54S00	2002-10-31
9201/057	1	Quad 2-Input Positive AND Gate with Open Collector Output, based on type 54S09	2002-10-31
9201/058	1	Triple 3-Input Positive NAND Gate, based on type 54S10	2002-10-31
9201/059	1	Dual 4-Input Positive NAND Gate, based on type 54S20	2002-10-31
9201/060	1	8-Input Positive NAND Gate, based on type 54S30	2002-10-31
9201/074	1	Quad 2-Input Positive NOR Gate, based on type 54S02	2002-10-31
9201/077	1	Dual 4-Input Positive NAND Gate with Open Collector Outputs, based on type 54S22	2002-10-31
9201/078	1	Dual 2-Wide 2-Input AND-OR INVERT Gates, based on type 54S51	2002-10-31
9201/079	1	Quadruple 2-Input Exclusive OR Gate, based on type 54S86	2002-10-31
9201/083	1	Dual 5-Input Positive NOR Gate, based on type 54S260	2002-10-31
9201/112	1	Positive OR Gate, based on type 54S32	2002-10-31
9202/059	1	9-Bit Odd/Even Parity Generator/Checker, based on type 54S280	2002-10-31
9203/026	1	Dual D Positive Edge Triggered Flip-Flop with Preset and Clear, based on type 54S74	2002-10-31
9203/027	1	Dual J-K Negative-Edge-Triggered Flip-Flop with Preset, based on type 54S113	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **56 IC'S SIL MON BI SCHOTTKY: 54 S SERIES**

Number		Specification Title	Date
9203/028	1	Dual J-K Negative Edge Triggered Flip-Flop with Preset and Clear, based on type 54S112	2002-10-31
9203/032	1	Hex D-Type Flip-Flop with Clear, based on type 54S174	2002-10-31
9203/033	1	Quadruple D-Type Flip-Flop with Clear, based on type 54S175	2002-10-31
9203/041	1	Octal D-Type 3-State Positive Edge-Triggered Flip-Flop, based on type 54S374	2002-10-31
9204/029	1	Synchronous 4-Bit Binary Counter with Synchronous Clear, based on type 54S163	2002-10-31
9204/044	1	Presettable Divide-by-2 and Divide-by-5 Counter, based on type 54S196	2002-10-31
9306/024	1	4-Bit Parallel Access Shift Register, based on type 54S195	2002-10-31
9306/031	1	4-Bit Bidirectional Universal Shift Register, based on type 54S194	2002-10-31
9401/012	1	Hex Inverter, based on type 54S04	2002-10-31
9401/015	1	Quad 2-Input Positive NAND Buffer, based on type 54S37	2002-10-31
9402/004	1	Dual 4-Input Positive NAND 50 Ohm Line Driver, based on type 54S140	2002-10-31
9408/007	1	8-to-1 Line Data Selector/Multiplexer, based on type 54S151	2002-10-31
9408/008	1	4-Line-to-1-Line Data Selector/Multiplexer, based on type 54S153	2002-10-31
9408/010	1	Decoder/Demultiplexer, based on type 54S138	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **56 IC'S SIL MON BI SCHOTTKY: 54 S SERIES**

Number		Specification Title	Date
9408/020	1	Data Selector/ Multiplexer with 3-State Outputs, based on type 54S251	2002-10-31
9408/042	1	Quad 2-Line-to-1 Line Data Selectors/Multiplexers, based on type 54S158	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **60 IC'S SIL MON BI ADVANCED LOW POWER SCHOTTKY 54ALS**

Number		Specification Title	Date
9201/088	1	Quad 2-Input Positive NAND Gates, based on type 54ALS00A	2002-10-31
9201/089	1	Quad 2-Input Positive NOR Gates, based on type 54ALS02	2002-10-31
9201/091	1	Quad 2-Input Positive OR Gates, based on type 54ALS32	2002-10-31
9201/092	1	Quad 2-Input Positive AND Gates, based on type 54ALS08	2002-10-31
9201/093	1	Triple 3-Input Positive NAND Gates, based on types 54ALS10 and 54ALS10A	2002-10-31
9201/094	1	Triple 3-Input Positive AND Gates, based on types 54ALS11 and 54ALS11A	2002-10-31
9201/095	1	Dual 4-Input Positive NAND Gates, based on type 54ALS20A	2002-10-31
9201/096	1	Dual 4-Input Positive AND Gates, based on types 54ALS21 and 54ALS21A	2002-10-31
9201/097	1	Triple 3-Input Positive NOR Gates, based on type 54ALS27	2002-10-31
9201/098	1	8-Input Positive NAND Gates, based on types 54ALS30 and 54ALS30A	2002-10-31
9201/099	1	Quadruple 2-Input Exclusive OR Gates, based on type 54ALS86	2002-10-31
9201/100	1	Quad 2-Input Positive-NAND Buffer Gates with Increased Output Drive, based on type 54ALS37A & 1000A	2002-10-31
9201/101	1	Quad 2-Input Positive-NOR Buffer Gates with Increased Output Drive, based on types 54ALS28A & 1002A	2002-10-31
9201/102	1	Triple 3-Input Positive AND Buffer Gates with Increased Output Drive, based on type 54ALS1011A	2002-10-31

Specification Family :　　　**INTEGRATED CIRCUITS**

Class of Publication Code :　　**60 IC'S SIL MON BI ADVANCED LOW POWER SCHOTTKY 54ALS**

Number		Specification Title	Date
9201/104	1	Quad 2-Input Positive AND Gates with Open Collector Outputs, based on type 54ALS09	2002-10-31
9201/115	1	Quad 2-Input Positive-NAND Buffer Gates with Increased Output Drive and Open Collector Outputs, based on types 54ALS38/38A/1003/1003A	2002-10-31
9201/116	1	Dual 4-Input Positive-NAND Buffer Gates with Increased Output Drive, based on types 54ALS40A/1020A	2002-10-31
9202/067	1	Octal D-Type Transparent Latches with 3-State Buffered Outputs, based on type 54ALS573	2002-10-31
9202/071	1	Octal D-type Transparent Latches with 3-State Outputs, based on type 54ALS373	2002-10-31
9203/042	1	Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54ALS574 &574A	2002-10-31
9203/043	1	Dual D-Type Positive-Edge-Triggered Flip-Flops with Clear and Preset, based on types 54ALS74 and 74A	2002-10-31
9203/046	1	Dual 4-Bit D-Type Edge-Triggered Flip-Flops with 3-State Buffered Outputs, based on types 54ALS874 and 54ALS874A	2002-10-31
9203/047	1	Hex D-Type Positive Edge-Triggered Flip-Flops with Clear, based on type 54ALS174	2002-10-31
9203/048	1	Quad D-Type Positive Edge-Triggered Flip-Flops with Clear, based on type 54ALS175	2002-10-31
9203/049	1	Dual J-K Positive Edge-Triggered Flip-Flops with Clear and Preset, based on types 54ALS109 and 54ALS109A	2002-10-31
9203/055	1	Dual J-K Negative Edge-Triggered Flip-Flops with Clear and Preset, based on type 54ALS112A	2002-10-31
9203/056	1	Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54ALS576	2002-10-31
9204/055	1	Synchronous 4-Bit Binary Counters with Direct Clear, based on types 54ALS161A and 54ALS161B	2002-10-31

Specification Family : **INTEGRATED CIRCUITS**

Class of Publication Code : **60 IC'S SIL MON BI ADVANCED LOW POWER SCHOTTKY 54ALS**

Number		Specification Title	Date
9204/057	1	Synchronous 4-Bit Binary Counter with Synchronous Clear, based on types 54ALS163A and 54ALS163B	2002-10-31
9204/058	1	Synchronous 4-Bit Up/Down Binary Counter (Dual Clock with Clear), based on type 54ALS193	2002-10-31
9205/012	1	3-Line to 8-Line Decoders/Demultiplexers, based on type 54ALS138	2002-10-31
9205/025	1	3-Line to 8-Line Decoders/Demultiplexers with Address Latches and Inverted Outputs, based on type 54ALS137	2002-10-31
9209/003	1	8-Bit Identity Comparators, based on type 54ALS520	2002-10-31
9306/039	1	8-Bit Universal Shift/Storage Registers with Direct Overriding Clear and 3-State Outputs, based on type 54ALS299	2002-10-31
9401/021	1	Quad 2-Input Positive AND Buffers with Increased Output Drive, based on types 54ALS1008 and 54ALS1008A	2002-10-31
9401/023	1	Hex Inverters with Open Collector Outputs, based on types 54ALS05 and 54ALS05A	2002-10-31
9401/024	1	Hex Inverters, based on types 54ALS04A and 54ALS04B	2002-10-31
9401/028	1	Octal Buffers and Line Drivers with 3-State Outputs, based on types 54ALS241 and 54ALS241A	2002-10-31
9401/029	1	Octal Buffers and Line Drivers with 3-State Outputs, based on type 54ALS541	2002-10-31
9401/031	1	Triple 3-Input Positive NAND Buffers with Increased Output Drive, based on type 54ALS1010A	2002-10-31
9401/032	1	Octal Buffers and Line Drivers with 3-State Outputs, based on type 54ALS240A	2002-10-31
9401/036	1	Hex Inverter Buffers with Increased Output Drive, based on type 54ALS1004	2002-10-31

Specification Family : **INTEGRATED CIRCUITS**

Class of Publication Code : **60 IC'S SIL MON BI ADVANCED LOW POWER SCHOTTKY 54ALS**

Number		Specification Title	Date
9402/005	1	Octal Buffers and Line Drivers with 3-State Outputs, based on types 54ALS244A and 54ALS244B	2002-10-31
9405/005	1	Octal Bus Transceivers with 3-State Outputs, based on type 54ALS245A & 645A	2002-10-31
9405/009	1	Octal Bus Transceivers with Independant Registers for A and B Buses True Data Paths and 3-State Buffered Outputs, based on type 54ALS646	2002-10-31
9405/018	1	Quad Bus Transceivers with Inverted 3-State Outputs, based on type 54ALS242B	2002-10-31
9405/019	1	Quad Bus Transceivers with 3-State Ouputs, based on type 54ALS243A	2002-10-31
9408/029	1	1-of-8 Data Selectors/Multiplexers, based on type 54ALS151	2002-10-31
9408/031	1	Dual 4-Line to 1-Line Data Selectors/Multiplexers with 3-State Outputs, based on type 54ALS253	2002-10-31
9408/032	1	Quad 2-Line to 1-Line Data Selector/Multiplexer with 3-State Outputs, based on type 54ALS257	2002-10-31
9408/033	1	Quad 2-Line to 1-Line Data Selector/Multiplexer with 3-State Outputs, based on type 54ALS258	2002-10-31
9408/034	1	Dual 4-Line to 1-Line Data Selector/Multiplexer with 3-State Outputs, based on type 54ALS353	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **75 ADVANCED CMOS 54ACSERIES**

Number		Specification Title	Date
9201/125	3	Advanced CMOS Quad 2-Input NAND Gates, based on type 54AC00	2005-12-21
9201/126	3	Advanced CMOS Quad 2-Input AND Gates, based on type 54AC08	2005-12-21
9201/127	3	Advanced CMOS Quad 2-Input OR Gates, based on type 54AC32	2005-12-21
9201/128	3	Advanced CMOS Quad 2-Input NAND Gates, based on type 54ACT00	2005-12-21
9201/131	2	Advanced CMOS Quad 2-Input and Gates, based on type 54ACT08	2005-12-15
9201/134	1	Advanced CMOS Quad 2-Input NOR Gates, based on type 54ACT02	2002-10-31
9201/135	2	Advanced CMOS Quad 2-Input OR Gates, based on type 54ACT32	2005-12-15
9201/136	2	Advanced CMOS Quad 2-Input Exclusive-OR Gates, based on type 54AC86	2005-12-20
9201/137	1	Advanced CMOS Dual 4-Input NAND Gates, based on type 54AC20	2002-10-31
9201/138	3	Advanced CMOS Triple 3-Input AND Gates, based on type 54AC11	2005-12-21
9201/139	3	Advanced CMOS Triple 3-Input NAND Gates, based on type 54AC10	2005-12-21
9201/140	1	Advanced CMOS Quad 2-Input NOR Gates, based on type 54AC02	2002-10-31
9201/142	2	Advanced CMOS Triple 3-Input AND Gates, based on type 54ACT11	2005-12-15
9201/143	3	Advanced CMOS Quad 2-Input Exclusive-OR Gates, based on type 54ACT86	2005-12-21

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **75 ADVANCED CMOS 54ACSERIES**

Number		Specification Title	Date
9203/067	1	Advanced CMOS Hex D-Type Edge Triggered Flip-Flops with Clear, based on type 54AC174	2002-10-31
9203/068	1	Advanced CMOS Dual D-Type Positive Edge-Triggered Flip-Flops with Preset and Clear, based on type 54AC74	2002-10-31
9203/069	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54AC374	2002-10-31
9203/074	1	Advanced CMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54ACT373	2002-10-31
9203/075	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54AC574	2002-10-31
9203/076	1	Advanced CMOS Octal D-Type Flip-Flops with Clock Enable, based on type 54AC377	2002-10-31
9203/077	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with Clear, based on type 54AC273	2002-10-31
9203/078	1	Advanced CMOS Quad D-Type Edge-Triggered Flip-Flops with Clear, based on type 54AC175	2002-10-31
9203/079	1	Advanced CMOS Dual D-Type Positive Edge-Triggered Flip-Flops with Preset and Clear, based on type 54ACT74	2002-10-31
9203/080	1	Advanced CMOS Dual J-K Positive Edge-Tiggered Flip-Flops with Preset and Clear, based on type 54ACT109	2002-10-31
9203/081	1	Advanced CMOS Hex D-Type Edge-Triggered Flip-Flops with Clear, based on type 54ACT174	2002-10-31
9203/082	1	Advanced CMOS Quad D-Type Edge-Triggered Flip-Flops, based on type 54ACT175	2002-10-31
9203/083	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with Clear, based on type 54ACT273	2002-10-31
9203/084	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54ACT574	2002-10-31

Specification Family : **INTEGRATED CIRCUITS**

Class of Publication Code : **75 ADVANCED CMOS 54ACSERIES**

Number		Specification Title	Date
9203/085	1	Advanced CMOS Octal D-Type Edge-Triggered Flip-Flops with 3-State Outputs, based on type 54ACT374	2002-10-31
9203/086	1	Advanced CMOS Dual J-K Positive Edge-Triggered Flip-Flops with Preset and Clear, based on type 54AC109	2002-10-31
9203/087	1	Advanced CMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54AC573	2002-10-31
9203/088	1	Advanced CMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54AC373	2002-10-31
9203/091	1	Advanced CMOS Octal D-Type Transparent Latches with 3-State Outputs, based on type 54ACT573	2002-10-31
9204/075	1	Advanced CMOS Synchronous 4-Bit Binary Counter, based on type 54ACT163	2002-10-31
9204/081	1	Advanced CMOS Synchronous 4-Bit Binary Counter with Direct Clear, based on type 54AC161	2002-10-31
9204/082	1	Advanced CMOS Synchronous 4-Bit Binary Counter, based on type 54AC163	2002-10-31
9204/083	1	Advanced CMOS Synchronous 4-Bit Up/Down Binary Counter, based on type 54AC169	2002-10-31
9204/085	1	Advanced CMOS Synchronous 4-Bit Binary Counter with Direct CLear, based on type 54ACT161	2002-10-31
9204/087	1	Advanced CMOS Synchronous Presettable 4-Bit Decade Counter with Direct Clear, based on type 54ACT160	2002-10-31
9205/024	2	Advanced CMOS Dual 2-Line to 4-Line Decoders/Demultiplexers with Inverted Outputs, based on type 54AC139	2005-12-20
9209/006	1	Advanced CMOS 8-Bit Identity Comparator, based on type 54AC521	2002-10-31
9306/056	1	Advanced CMOS 8-Input Universal Shift/Storage Registers with Common Parallel I/O Pins Direct Clear and 3-State Outputs, based on type 54AC299	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **75 ADVANCED CMOS 54ACSERIES**

Number		Specification Title	Date
9306/057	1	Advanced CMOS 8-Input Universal Shift/Storage Registers with Common Parallel I/O Inputs Direct Clear and 3-State Outputs, based on type 54ACT299	2002-10-31
9401/042	1	Advanced CMOS Octal Bus Buffer with 3-State Outputs, based on type 54AC240	2002-10-31
9401/043	1	Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54AC244	2002-10-31
9401/050	1	Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54ACT244	2002-10-31
9401/051	1	Advanced CMOS Hex Inverter, based on type 54AC04	2002-10-31
9401/053	1	Advanced CMOS Hex Inverter Schmitt Trigger Inverters, based on type 54ACT14	2002-10-31
9401/056	1	Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54ACT541	2002-10-31
9401/057	1	Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54ACT240	2002-10-31
9401/058	1	Advanced CMOS Octal Bus Buffers with 3-State Outputs, based on type 54AC541	2002-10-31
9401/059	1	Advanced CMOS Hex Inverters, based on type 54ACT04	2002-10-31
9401/061	1	Advanced CMOS Quad Bus Buffers with 3-State Outputs, based on type 54AC125	2002-10-31
9401/062	1	Advanced CMOS Octal Bus Buffers with Inverted 3-State Outputs, based on type 54AC540	2002-10-31
9405/015	1	Advanced CMOS Octal Bus Transceivers with 3- State Outputs, based on type 54ACT245	2002-10-31
9405/017	1	Advanced CMOS Octal Bus Transceivers with 3-State Outputs, based on type 54AC245	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **75 ADVANCED CMOS 54ACSERIES**

Number		Specification Title	Date
9408/060	2	Advanced CMOS 3 to 8 Line Decoders/Demultiplexers with Inverted Outputs, based on type 54ACT138	2005-12-15
9408/061	1	Advanced CMOS Dual 2-Line to 4-Line Decoders/Demultiplexers with Inverted Outputs, based on type 54ACT139	2002-10-31
9408/062	1	Advanced CMOS Quad 2-Line to 1-Line Data Selectors/ Multiplexers with 3-State Outputs, based on type 54ACT257	2002-10-31
9408/063	3	Advanced CMOS 3 to 8 Line Decoders/Demultiplexers with Inverted Outputs, based on type 54AC138	2005-12-21
9408/066	1	Advanced CMOS Quad 2- Line to 1-Line Data Selectors/ Multiplexers with Inverted Outputs, based on type 54ACT158	2002-10-31
9408/067	1	Advanced CMOS 8-Line to 1-Line Data Selectors/Multiplexers, based on type 54AC151	2002-10-31
9408/068	1	Advanced CMOS Dual 4-Line to 1-Line Data Selectors/ Multiplexers, based on type 54AC153	2002-10-31
9408/069	1	Advanced CMOS Quad 2-Line to 1-Line Data Selectors/ Multiplexers, based on type 54AC157	2002-10-31
9408/070	1	Advanced CMOS 4-Line to 1-Line Data Selectors/Multiplexers with 3-State Outputs, based on type 54AC253	2002-10-31
9408/071	1	Advanced CMOS Quad 2-Line to-1 Line Data Selectors/ Multiplexers with 3-State Outputs, based on type 54AC257	2002-10-31
9408/074	1	Advanced CMOS Quad 2-Line to 1-Line Data Selectors/ Multiplexers with Inverted Ouputs, based on type 54AC158	2002-10-31
9409/008	1	Advanced CMOS Hex Schmitt Trigger Inverters, based on type 54AC14	2002-10-31

Specification Family :   **INTEGRATED CIRCUITS**

Class of Publication Code :   **85 54HSC/54HST- SILICON ON SAPPHIRE HCMOS**

Number		Specification Title	Date
9201/144	1	Silicon on Sapphire Monolithic HCMOS Quad 2-Input AND Gates, based on types 54HSC08 & 54HST08	2002-10-31
9201/145	1	Silicon on Sapphire Monolithic HCMOS Quad 2-Input OR Gates, based on types 54HSC32 & 54HST32	2002-10-31
9401/063	1	Silicon on Sapphire Monolithic HCMOS Hex Inverters, based on types 54HSC04 & 54HST04	2002-10-31

Specification Family :   **OPTOELECTRONICS**

Class of Publication Code :   **81 CCD PHOTOMOS AREA ARRAYS (9610/...)**

Number		Specification Title	Date
9610/001	1	Charge Coupled Devices Silicon Photosensitive Area Array Image Sensor 286Linesx382Pixels, based on type TH7863A	2002-10-31
9610/002	1	Charge Coupled Devices, Silicon, Photosensitive, Area Array, Image Sensor, 512 Lines x 512 Pixels, Front Illuminated, Frame Transfer, Multi Pinned Phases, Based on Type TH 7890M	2011-11-17
9610/003	1	Charge Coupled Devices, Silicon, Photosensitive, Area Array, Image Sensor, 286 Lines x 382 Pixels, Based on Type Th 7863D	2002-10-31

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