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RELAY, ELECTROMAGNETIC, LATCHING, 50VDC, 2A, 2PDT, HALF-SIZE CRYSTAL CAN

BASED ON TYPE GP250

ESCC Detail Specification No. 3602/010

Issue 4 December 2013





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DOCUMENTATION CHANGE NOTICE

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DCR No.	CHANGE DESCRIPTION
711	Specification updated to incorporate editorial and technical changes per DCR.
	Specification converted to MSWORD. Changes in presentation are possible.





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1 **GENERAL**

1.1 SCOPE

This specification details the ratings, physical and electrical characteristics, and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

(a) ESCC Generic Specification No. 3602.

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 The ESCC Component Number

The ESCC Component Number shall be constituted as follows:

Example: 36020100126V

Detail Specification Reference: 3602010

• Component Type Variant Number: 01 (as required)

Characteristic code: Rated Coil Voltage (26.5Vdc): 26V (as required)

1.4.1.1 Characteristics and/or Ratings Codes

Characteristics and/or ratings to be codified as part of the ESCC Component Number shall be as follows:

(a) Rated Coil Voltage expressed by means of the following codes:

Rated Coil Voltage (Vdc)	Code
26.5	26V
12	12V



1.4.2 Component Type Variants and Range of Components

The component type variants and range of components applicable to this specification are as follows:

Variant Number	Case and Terminal Description (Note 1)	Rated Coil Voltage	Weight max
		(Vdc)	(g)
01	Plain Case (No Mount)	26.5, 12	8.5
	Solder Pin Terminals		
02	Raised Vertical Flange Mount	26.5, 12	8.5
	Solder Pin Terminals		
03	Horizontal Flange Mount	26.5, 12	8.5
	Solder Hook Terminals		
04	Horizontal Flange Mount	26.5, 12	8.5
	Solder Pin Terminals		
05	Plain Case (No Mount)	26.5, 12	8.5
	Solder Hook Terminals		
06	Raised Vertical Flange Mount	26.5, 12	8.5
	Solder Hook Terminals		

NOTES:

1.5 <u>MAXIMUM RATINGS</u>

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Symbols	Maximum Ratings	Units	Remarks
Coil Voltage Range	V_{CR}	26.5 to 32 11 to 14.8	Vdc	Rated Coil Voltage: 26.5Vdc Rated Coil Voltage: 12Vdc
Rated Resistive Load Contact Current	I _{CR}	2	А	50Vdc resistive Note 1
Rated Inductive Load Contact Current	I _{CL}	400	mA	28Vdc inductive Inductance: 320mH Note 1
Overload Current	I _{OVERLOA}	4	А	50Vdc resistive
Operating Temperature Range	T _{op}	-65 to +125	°C	T _{amb}
Storage Temperature Range	T_{stg}	-65 to +125	°C	T _{amb}
Soldering Temperature	T _{sol}	+260	°C	Note 2

NOTES:

1. Relays should not be used in change-over mode where the potential difference between stationary contacts is greater than 10V and the switched current is greater than 100mA.

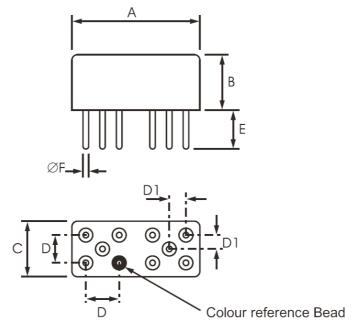
^{1.} See Physical Dimensions and Terminal Identification.



2. Duration 10 seconds maximum at a distance not less than 3mm from the device body. The same terminal shall not be resoldered until 3 minutes have elapsed.

1.6 PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION

1.6.1 Plain Case (No Mount) and Solder Pin Terminals (Variant 01)



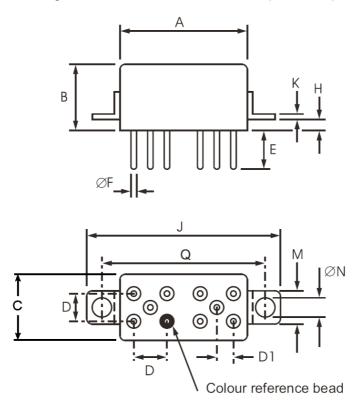
Symbols	Dimensions (mm)		
	Min	Max	
Α	-	20.57	
В	-	10.41	
С	-	10.41	
D	4.95	5.21	
D1	2.4	2.7	
E	4.24	5.4	
ØF	0.66	0.86	

NOTES:

Terminal identification is specified by reference to the colour reference bead. See Functional Diagram.



1.6.2 <u>Raised Vertical Flange Mount and Solder Pin Terminals (Variant 02)</u>



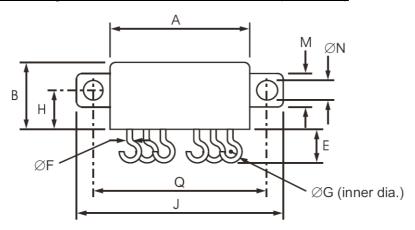
Symbols	Dimensions (mm)		
	Min	Max	
A	-	20.57	
В	-	10.41	
С	-	10.41	
D	4.95	5.21	
D1	2.4	2.7	
E	4.24	5.4	
ØF	0.66	0.86	
Н	3.1	3.3	
J	-	32.9	
K	0.5	0.8	
М	6.22	8.1	
ØN	3	3.55	
Q	26.6	27.4	

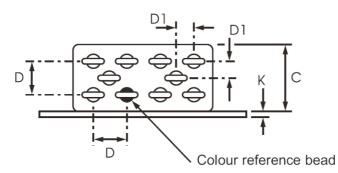
NOTES:

1. Terminal identification is specified by reference to the colour reference bead. See Functional Diagram.



1.6.3 Horizontal Flange Mount and Solder Hook Terminals (Variant 03)





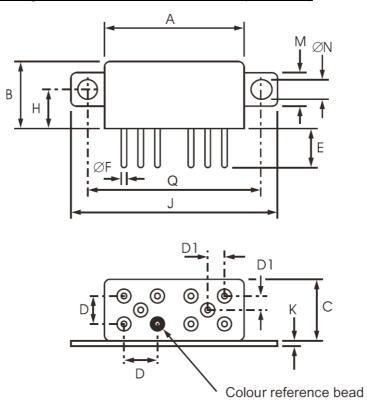
Symbols	Dimensions (mm)		
	Min	Max	
А	-	20.57	
В	-	10.41	
С	-	10.41	
D	4.95	5.21	
D1	2.4	2.7	
E	4.11	4.83	
ØF	0.66	0.86	
ØG	-	1.8	
Н	5.97	6.73	
J	-	32.64	
K	0.38	0.9	
М	6.22	8.1	
ØN	3	3.55	
Q	26.8	27.2	

NOTES:

1. Terminal identification is specified by reference to the colour reference bead. See Functional Diagram.



1.6.4 Horizontal Flange Mount and Solder Pin Terminals (Variant 04)



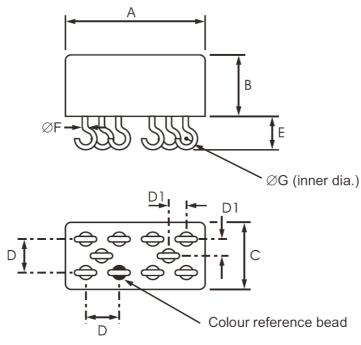
Symbols	Dimensions (mm)			
	Min	Max		
А	-	20.57		
В	-	10.41		
С	-	10.41		
D	4.95	5.21		
D1	2.4	2.7		
E	4.24	5.4		
ØF	0.66	0.86		
Н	5.97	6.73		
J	-	32.64		
K	0.38	0.9		
М	6.22	8.1		
ØN	3	3.55		
Q	26.8	27.2		

NOTES:

1. Terminal identification is specified by reference to the colour reference bead. See Functional Diagram.



1.6.5 Plain Case (No Mount) and Solder Hook Terminals (Variant 05)

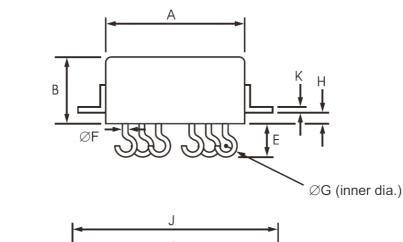


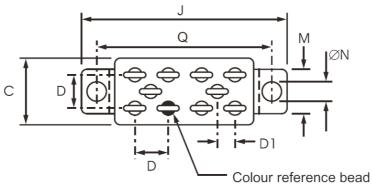
Symbols	Dimensions (mm)		
	Min	Max	
А	-	20.57	
В	-	10.41	
С	-	10.41	
D	4.95	5.21	
D1	2.4	2.7	
E	4.11	4.83	
ØF	0.66	0.86	
ØG	-	1.8	

NOTES: Terminal identification is specified by reference to the colour reference bead. See Functional Diagram.



1.6.6 Raised Vertical Flange Mount and Solder Hook Terminals (Variant 06)





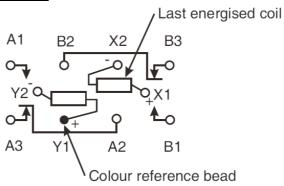
Symbols	Dimensions (mm)		
	Min	Max	
Α	-	20.57	
В	-	10.41	
С	-	10.41	
D	4.95	5.21	
D1	2.4	2.7	
E	4.11	4.83	
ØF	0.66	0.86	
ØG	-	1.8	
Н	3.1	3.3	
J	-	32.9	
K	0.5	0.8	
M	6.22	8.1	
ØN	3	3.55	
Q	26.6	27.4	

NOTES:

1. Terminal identification is specified by reference to the colour reference bead. See Functional Diagram.



1.7 FUNCTIONAL DIAGRAM



NOTES:

- As viewed from the terminal side.
- 2. Individual terminal designations are for reference purposes only.

1.8 MATERIALS AND FINISHES

1.8.1 <u>Case</u>

Copper nickel, hermetically sealed. Tin-lead alloy plating may be used.

1.8.2 Terminals

The lead material and finish shall by type D3, D4, F3 or F4 in accordance with the requirements of ESCC Basic Specification No. 23500.

2 **REQUIREMENTS**

2.1 GENERAL

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

2.1.1 <u>Deviations from the Generic Specification</u>

None.

2.2 MARKING

The marking shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and as follows.

The information to be marked on the component shall be:

- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- (b) The ESCC Component Number.



(c) Traceability information.

2.3 <u>TERMINAL STRENGTH</u>

The test conditions for Terminal Strength, tested as specified in the ESCC Generic Specification, shall be as follows:

(a) Pull Test

Applied Force: 15N

2.4 <u>ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES</u>

Electrical measurements shall be performed at room, high and low temperatures. Consolidated notes are given after the tables.

2.4.1 Room Temperature Electrical Measurements

The measurements shall be performed at $T_{amb} = +22 \pm 3^{\circ}C$.

Characteristics	Symbols	Symbols Test Method and Conditions	Rated Coil	Limits		Units
		Conditions	Voltage (Vdc)	Min	Max	
Latch Voltage	U _L	ESCC No. 3602 Note 1	26.5 12	7.5 3.5	13.5 6.8	V
Reset Voltage	U _R	ESCC No. 3602 Note 1	26.5 12	7.5 3.5	13.5 6.8	V
Latch Time	t _L	ESCC No. 3602	All	-	4	ms
Reset Time	t _R	ESCC No. 3602	All	-	4	ms
Bounce Time	t _B	ESCC No. 3602	All	-	2	ms
Insulation Resistance	Rı	ESCC No. 3602 V _{TEST} = 100Vdc	All	10	-	GΩ
Voltage Proof (Test Voltage)	VP	ESCC No. 3602 Maximum Leakage	All	1000 (Note 2)	-	Vrms
		Current I _{LVP} = 1mA		500 (Note 3)	-	
Voltage Proof Leakage Current	I _{LVP}	ESCC No. 3602 Note 4	All	1	1	mA
Contact Voltage Drop	V _D	ESCC No. 3602 I _{TEST} = 100mA max	All	-	0.05 x I _{TEST}	V
Coil Resistance	R _B	ESCC No. 3602 Both Coils	26.5 12	648 135	792 165	Ω



2.4.2 High and Low Temperatures Electrical Measurements

Characteristics	Symbols	Test Method and		Limits		Units
		Conditions	Voltage (Vdc)	Min	Max	
Latch Voltage	U _L	ESCC No. 3602 T _{amb} = +125 (+0 -5)°C and -65 (+5 -0)°C Note 1	26.5 12	-	18 9.8	V
Reset Voltage	U _R	ESCC No. 3602 T _{amb} = +125 (+0 -5)°C and -65 (+5 -0)°C Note 1	26.5 12	-	18 9.8	V
Latch Time	t _L	ESCC No. 3602 T _{amb} = +125 (+0 -5)°C and -65 (+5 -0)°C	All	1	5	ms
Reset Time	t _R	ESCC No. 3602 T _{amb} = +125 (+0 -5)°C and -65 (+5 -0)°C	All	-	5	ms
Bounce Time	t _B	ESCC No. 3602 T _{amb} = +125 (+0 -5)°C and -65 (+5 -0)°C	All	-	2	ms
Insulation Resistance	R _I	ESCC No. 3602 T _{amb} = +125 (+0 -5)°C V _{TEST} = 100Vdc	All	100	-	ΜΩ
Contact Voltage Drop	V _D	ESCC No. 3602 T _{amb} = +125 (+0 -5)°C and -65 (+5 -0)°C I _{TEST} = 100mA max	All	-	0.05 x I _{TEST}	V

2.4.3 Notes to Electrical Measurements Tables

- 1. The coil voltage rise time shall be less than $0.1t_L$ or $0.1t_R$. The coil voltage shall be maintained for a minimum duration of $10t_L$ or $10t_R$.
- 2. Points of application (1000V):
 - Between terminals (except coil) and case, coil de-energised.
 - Between terminals (except coil) and case, coil energised with Rated Coil Voltage.
 - Between coil and other terminals.
 - Between switching circuits, coil de-energised.
 - Between switching circuits, coil energised with Rated Coil Voltage.
- 3. Points of application (500V):
 - Between open contacts, coil de-energised.
 - Between open contacts, coil energised with Rated Coil Voltage.
 - Between coil and case.
 - Between coils.
- 4. Measured during Voltage Proof test.



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2.5 PARAMETER DRIFT VALUES

Parameter Drift Values shall be measured as specified in the ESCC Generic Specification.

Unless otherwise specified, the measurements shall be performed at $T_{amb} = +22 \pm 3^{\circ}C$.

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The corresponding absolute limit values for each characteristic shall not be exceeded.

Characteristics	Symbols		Units		
		Drift Value	Absolute		
		Δ	Min	Max	
Latch Voltage	U _L	Note 1	Note 2	Note 2	V
Reset Voltage	U_R	Note 1	Note 2	Note 2	V

NOTES:

- Drift Value (Δ) limits are not specified. Drift Values shall be recorded for information purposes
- The limit specified in Room Temperature Electrical Measurements shall apply.

INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS 2.6

Unless otherwise specified, the measurements shall be performed at T_{amb} = +22 ±3°C.

Unless otherwise specified the test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

Test Reference per	Characteristics	Symbols	Limits		Units
ESCC No. 3602	(Note 1)		Min	Max	
Thermal Shock	During 5th Cycle				
	Latch Voltage	U_L	N	lote 2	V
	Reset Voltage	U_R	N	lote 2	V
	Latch Time	t∟	N	lote 2	ms
	Reset Time	t _R	Note 2		ms
	Final Measurements				
	Voltage Proof	VP	١	lote 3	Vrms
	Voltage Proof Leakage Current	I _{LVP}	١	lote 3	mA



Test Reference per	Characteristics	Symbols	L	_imits	Units
ESCC No. 3602	(Note 1)		Min	Max	
Low Level Sine	Final Measurements				
Vibration	Latch Voltage	U_L	N	Note 3	
	Latch Voltage Drift	$\Delta U_L/U_L$	N	lote 1	%
	Reset Voltage	U_R	N	lote 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	N	lote 1	%
Random Vibration	Final Measurements				
	Latch Voltage	U_L	N	lote 3	V
	Latch Voltage Drift	$\Delta U_L/U_L$	N	lote 1	%
	Reset Voltage	U_R	N	lote 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	N	lote 1	%
High Level Sine	Final Measurements				
Vibration	Latch Voltage	U_L	N	lote 3	V
	Latch Voltage Drift	$\Delta U_L/U_L$	N	lote 1	%
	Reset Voltage	U_R	N	lote 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	N	lote 1	%
Low Level Mechanical	Final Measurements				
Shock	Contact Voltage Drop	V_D	N	lote 3	V
	Latch Voltage	U_L	N	lote 3	V
	Latch Voltage Drift	$\Delta U_L/U_L$	N	lote 1	%
	Reset Voltage	U_R	N	lote 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	N	lote 1	%
	Voltage Proof	VP	N	lote 3	Vrms
	Voltage Proof Leakage Current	I _{LVP}	N	lote 3	mA
High Level	Final Measurements				
Mechanical Shock	Contact Voltage Drop	V_D	N	lote 3	V
	Latch Voltage	U_L	N	lote 3	V
	Latch Voltage Drift	$\Delta U_L/U_L$	N	lote 1	%
	Reset Voltage	U_R	١	lote 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	N	lote 1	%
	Voltage Proof	VP	١	lote 3	Vrms
	Voltage Proof Leakage Current	I _{LVP}	١	lote 3	mA



Test Reference per	Characteristics	Symbols	Limits		Units
ESCC No. 3602	(Note 1)		Min	Max	
Resistance to	Final Measurements				
Soldering Heat	Insulation Resistance	Rı	N	Note 3	
	Contact Voltage Drop	V_D	Ν	lote 3	V
	Latch Voltage	U_L	Ν	lote 3	V
	Reset Voltage	U_R	Ν	lote 3	V
	Coil Resistance	R _B	Ν	lote 3	Ω
Low Level Life	Final Measurements				
	Contact Voltage Drop	V_D	-	0.1 x I _{TEST}	V
	Insulation Resistance	Rı	5000	-	МΩ
	Voltage Proof	VP	Ν	lote 3	Vrms
	Voltage Proof Leakage Current	I _{LVP}	N	lote 3	mA
	Latch Voltage	U_L	Ν	lote 3	V
	Latch Voltage Drift	$\Delta U_L/U_L$	Ν	lote 1	%
	Reset Voltage	U_R	Ν	lote 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	Ν	lote 1	%
	Latch Time	t∟	N	lote 3	ms
	Reset Time	t _R	١	lote 3	ms
	Bounce Time	t _B	١	lote 3	ms
	Coil Resistance	R _B	N	lote 3	Ω





Test Reference per	Characteristics	Symbols	Limits		Units
ESCC No. 3602	(Note 1)		Min	Max	
Resistive Life	During Monitoring				
	Contact Voltage Drop	V_D	-	2.8	V
	Final Measurements			l	
	Contact Voltage Drop	V_D	-	0.1 x I _{TEST}	V
	Insulation Resistance	R _I	5000	-	МΩ
	Voltage Proof	VP	١	lote 3	Vrms
	Voltage Proof Leakage Current	I _{LVP}	١	Note 3	mA
	Latch Voltage	U _L	N	Note 3	V
	Latch Voltage Drift	$\Delta U_L/U_L$	N	Note 1	%
	Reset Voltage	U_R	١	Note 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	N	Note 1	%
	Latch Time	t _L	١	Note 3	ms
	Reset Time	t _R	N	lote 3	ms
	Bounce Time	t _B	N	Note 3	ms
	Coil Resistance	R _B	١	Note 3	Ω



Test Reference per	Characteristics (Note 1)	Symbols	Limits		Units
ESCC No. 3602			Min	Max	
Coil Life	During Step 1 of each Cycle				
	Contact Voltage Drop	V_D	N	Note 3	
	Coil Resistance	R _B	N	ote 3	Ω
	During Step 3 of 1st Cycle				
	Contact Voltage Drop	V_D	N	ote 2	V
	Latch Time	t∟	N	ote 2	ms
	Reset Time	t _R	N	ote 2	ms
	During Steps 4 & 5 of 4th Cycle				
	Latch Voltage	U _L	N	ote 2	V
	Reset Voltage	U_R	N	ote 2	V
	Final Measurements				
	Voltage Proof	VP	N	ote 3	Vrms
	Voltage Proof Leakage Current	I _{LVP}	N	ote 3	mA
	Insulation Resistance	Rı	N	ote 3	GΩ
	Contact Voltage Drop	V_D	N	ote 3	V
	Coil Resistance	R _B	N	ote 3	Ω
	Latch Time	t _L	N	ote 3	ms
	Reset Time	t _R	N	ote 3	ms
	Bounce Time	t _B	N	ote 3	ms



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Test Reference per	Characteristics	Symbols	L	imits	Units
ESCC No. 3602	(Note 1)		Min	Max	
Intermediate Current	During Monitoring				
	Contact Voltage Drop	V_D	-	300	mV
	Final Measurements			Ī	
	Insulation Resistance	R _I	5000	-	МΩ
	Voltage Proof	VP	N	lote 3	Vrms
	Voltage Proof Leakage Current	I _{LVP}	N	lote 3	mA
	Latch Voltage	U_L	N	lote 3	V
	Latch Voltage Drift	$\Delta U_L/U_L$	N	lote 1	%
	Reset Voltage	U_R	N	lote 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	N	lote 1	%
	Latch Time	t∟	N	lote 3	ms
	Reset Time	t _R	N	lote 3	ms
	Bounce Time	t _B	N	lote 3	ms
	Coil Resistance	R _B	N	lote 3	Ω
	Contact Voltage Drop	V_D	-	0.1 x I _{TEST}	V
Overload	During Monitoring				
	Contact Voltage Drop	V_D	-	1.4	V
	E' al Mara a sur safe				
	Final Measurements	.,,			.,
	Contact Voltage Drop	V _D	-	0.1 x I _{TEST}	V
	Insulation Resistance	R _I	5000	-	МΩ
	Voltage Proof	VP	N	lote 3	Vrms
	Voltage Proof Leakage Current	I _{LVP}	N	lote 3	mA
	Latch Voltage	U_L	N	lote 3	V
	Latch Voltage Drift	$\Delta U_L/U_L$	N	lote 1	%
	Reset Voltage	U_R	N	lote 3	V
	Reset Voltage Drift	$\Delta U_R/U_R$	N	lote 1	%
	Latch Time	t∟	N	lote 3	ms
	Reset Time	t _R	١	lote 3	ms
	Bounce Time	t _B	N	lote 3	ms
	Coil Resistance	R _B	N	lote 3	Ω



NOTES:

- Parameter Drift shall be calculated referenced to the measurement immediately prior to the test in question. An additional initial measurement may be performed prior to the test in question if considered necessary. Drift limits are not specified. Drift Values shall be recorded for information purposes only.
- 2. The limits specified in High and Low Temperatures Electrical Measurements, as applicable to the same test temperature, shall apply.
- 3. The limits specified in Room Temperature Electrical Measurements shall apply.

2.7 RUN-IN CONDITIONS

The test conditions for Run-in, tested as specified in the ESCC Generic Specification, shall be as follows:

(a) Test Temperature: +22 ±3°C.



APPENDIX A AGREED DEVIATIONS FOR LEACH INTERNATIONAL EUROPE (F)

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS
Materials and Finishes: Terminals	For components specified with terminal finish type 3, the tin-lead plating shall have a composition of 85 to 95% tin (remainder lead).
Deviations from the Generic	High Level Sine Vibration: Not Applicable
Specification:	High Level Mechanical Shock: Not Applicable
Qualification and Periodic Tests (Chart F4)	Chart F4: Coil Life subgroup test sequence (under Endurance Subgroup 1):
	Coil Life and the subsequent tests shall only be performed for
	Qualification. They are not required for Periodic Testing except
	in the case of any significant change to the design.