



REQUIREMENTS AND GUIDELINES FOR THE PROCESS IDENTIFICATION DOCUMENT (PID)

ESCC Basic Specification No. 22700

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1 **SCOPE**

This specification defines the general requirements for the Process Identification Document (PID) in terms of content, layout, configuration control and procedure for updating.

2 **APPLICABLE DOCUMENTS**

The following ESCC Specifications form part of, and shall be read in conjunction with, this specification. The relevant issues shall be those in effect on the date of preparation of the PID.

- No. [20100](#), Requirements for the Qualification of Standard Electronic Components for Space Application.
- No. [21300](#), Terms, Definitions, Abbreviations, Symbols and Units.
- No. [24300](#), Requirements for the Capability Approval of Electronic Component Technologies for Space Application.
- No. [24600](#), Minimum Quality System Requirements.

3 **INTRODUCTION**

The purpose of the PID is to ensure that a precise reference is established for an electronic component qualified or capability approved in accordance with the ESCC System.

This reference shall comprise the component's design configuration, materials used in manufacture, manufacturing processes and controls, and completely define all inspections and tests to be carried out during and after manufacture. The document shall enable the ESCC Executive to control the component's manufacture and ensure that all future components or capability approved products supplied by the Manufacturer will be identical to those for which approval was originally granted.

The PID shall also provide a standard reference against which any anomalies occurring after qualification approval can be examined and resolved.

4 **REQUIREMENTS**

4.1 **APPROVAL AND CONTENTS**

Formal approval of the PID shall be granted by the ESCC Executive and they shall sign the document as approved. Prior to this formal approval being granted, the Manufacturer's quality assurance representative shall sign the document to authenticate the contents.

The ESCC Executive shall identify the items in the PID that are to be maintained under Manufacturer's configuration control (e.g. Manufacturer's in-house inspection procedures) and shall specify those it wishes to hold in the PID as a part thereof.

4.1.1 **Requirements for Component Qualification**

The Manufacturer of the component to be qualified shall prepare the PID as required by ESCC Basic Specification No. [20100](#).

4.1.2 **Requirement for Capability Approval**

The Manufacturer for the capability approval shall prepare the PID as required by ESCC Basic Specification No. [24300](#).

4.2 PROPRIETARY INFORMATION

The ESCC Executive shall treat proprietary information as strictly confidential and shall not disclose any such information to a third party. Where proprietary processes are involved, the Manufacturer shall provide only such information as necessary for quality assurance purposes.

The Manufacturer shall, however, demonstrate that adequate documentation is available for processes declared confidential and that such documents are included in his configuration control system. These documents shall be referenced in the PID as Company Confidential and their title, document reference number, issue number and date shall be stated. During routine line surveys, the inspectors shall verify that the issues of these documents in use are the same as those declared in the PID.

A third party is defined as anyone not belonging to the ESCC Executive.

4.3 PID UPDATING

A PID shall represent the currently accepted manufacturing controls and inspection procedures. For this reason, the PID must be updated each time the ESCC Executive has approved the modification of a particular item in the document.

The PID and the documents referenced therein shall have their issues held at those effective on the date of approval of the PID by the ESCC Executive. Any intended modification of the PID or referenced documents held under the Manufacturer's configuration control, together with any quality and reliability implications shall be brought to the attention of the ESCC Executive for review and approval before implementation. Such modifications shall require a reissue of the PID and the relevant referenced documents.

When an intended modification of the PID affects the interchangeability of the components, a new ESCC variant or a new ESCC component is created.

4.4 ACCESS TO THE PID

Members of ESCC Executive involved with the component or product under qualification or capability approval shall have access to the PID at all times. Any Users may have access to the PID at the Manufacturer's plant.

The Manufacturer may disclose all or part of his PID to Users of his products without the permission of the ESCC Executive. Inspectors from industry, who are on loan to the ESCC Executive may have access to the PID only after the Manufacturer has given written permission to the ESCC Executive.

Proprietary information shall be covered in all cases and at all times by the requirements specified herein.

5 PID STRUCTURE

5.1 GENERAL

The PID shall be compiled in such a manner that efficient and speedy configuration control can be maintained and that any necessary amendment, modification and updating will require the minimum of effort.

The PID shall be written, as closely as practicable, in the Manufacturer's standard format for documents and shall meet the Document Control requirements of ESCC Basic Specification No. [24600](#).

Unless otherwise agreed by the ESCC Executive (and, in the interest of standardisation, this should be limited to special cases), all information to be recorded in the PID shall be grouped into appropriate sections. The following paragraphs detail the sections required. The sections and their contents are not necessarily exhaustive and are intended to give the Manufacturer and ESCC Executive a guide to the minimum mandatory information to be included in the PID.

5.2 SECTION 1 - GENERAL

This section shall comprise the title page(s), revision or amendment record sheet and the list of contents of the PID. In addition to a suitable title, fully referencing the component, series of components or capability domain and its boundaries.

Provision shall be made for the approval signatures on behalf of the Manufacturer and the ESCC Executive and the dates of such signatures.

The revision sheet shall follow normal practice and provide for the date of revision or amendment, section and pages affected and a short description of the change(s).

The list of contents shall detail the sections and their content to the degree necessary for easy reference to essential items.

5.3 SECTION 2 - MANUFACTURING ORGANISATIONS

This section shall comprise an organigram of the manufacturing plant in relation to management, engineering, production, quality assurance and marketing as applicable to the component(s) or capability domain and its boundaries which is/are the subject of the PID.

5.4 SECTION 3 - MANUFACTURING AND TESTING

For qualification of components, concise details of the appropriate manufacture and testing areas shall be included in this section.

For capability approval, concise details of the appropriate manufacture and testing areas in accordance with the requirements specification for capability approval of the relevant technology shall be included in this section.

This section shall also include:

- A flow chart showing how applicable purchase orders are processed;
- A specimen of each route sheet or traveller used during manufacture and testing.

5.5 SECTION 4 - DESCRIPTION

5.5.1 Section 4 for Qualification Approval - Component Description

For qualification approval this section shall comprise all the information necessary to fully define the component and its construction. It shall include an outline of the technologies involved, piece part composition and construction details. Drawings or photographs detailing the component and each of its constituent elements shall be included.

5.5.2 Section 4 for Capability Approval - Capability Domain and Boundaries Description

For capability approval, this section shall comprise all the information necessary to fully define the capability domain and its boundaries as required by ESCC Basic Specification No. 24300. It shall include an outline of the technology involved, compositions and construction details. Drawings or photographs detailing the test structures and each of their constituent elements shall be included.

5.6 SECTION 5 - CONTROL DOCUMENTATION

This section shall comprise the list of specifications, process flow chart and collection of specifications as follows:

(a) List of Specifications

List of specifications and procedures used for each process step, control inspection and test given in the production flow chart - with reference to the appropriate point of the flow chart and identified by issue status and date. Each activity shall be defined in such detail that it can be correctly performed, controlled and repeated. Specific reference shall be made to associated material, piece parts or inspection equipment.

(b) Production Flow Chart

The production flow chart shall identify all piece parts and all process, assembly, inspection and test operations applicable to production of the component (see Note 1). The points of application of quality controls shall be clearly indicated. The chart shall present these schematically in their correct sequence and, for each operation, make reference to the relevant identification, process, assembly or inspection document. The issue and date of documents applicable at the time of preparation of the flow chart shall be stated.

When preparing the chart, the following symbols are preferred:



- For process and assembly operations.



- For inspection and test operations.



- For quality control operations.

NOTES:

1. For capability approval, this shall be understood as the range of components within the defined domain capable of fabrication.

(c) Wafer Lot Acceptance

Semiconductor Manufacturers shall prepare a Wafer Lot Acceptance (WLA) procedure. This shall describe the Manufacturer's approach to WLA and identify the quantitative criteria for the quality control gate releasing the wafer lot for further processing. It shall be based on inspections and tests on completed wafers and, where applicable, results from in process inspections and tests and from process monitors and test structures.

The procedure shall be driven by quality targets taking into account control possibilities and resources and shall, to the extent applicable to the technology, cover the following items:

- (i) The in-process monitoring programme identifying critical process steps, the parameters and critical dimensions to be measured and the applicable limits.
- (ii) Where applicable, the analysis of data from process monitoring programmes by appropriate Statistical Process Control (SPC) methods to determine control effectiveness.
- (iii) The visual inspection and wafer probe acceptance criteria including wafer yield and, where applicable, wafer lot yield limits.
- (iv) Where applicable, the process monitors and other test structures used to verify that process parameters and critical dimensions are within the specified limits.

(d) Collection of Specifications

Collection of specifications containing those specifications, generally of inspection and control, which the Manufacturer and ESCC Executive have agreed jointly to be part of the PID to be delivered to the ESCC Executive. If in-house documents have been accepted in lieu of the applicable ESCC Specifications, such documents shall be included in this collection of specifications.

5.7 SECTION 6 - INSPECTION AND TEST EQUIPMENT

This section shall contain a list of all inspection, test and measuring equipment used for the manufacture and testing of the component (see Note 1) in accordance with the control documentation, including failure analysis. Where relevant, the listed equipment shall be adequately cross-referenced to the production flow chart and/or the activities for which it is used.

NOTES:

1. For capability approval, this shall be understood as the range of components within the defined domain capable of fabrication.

5.8 SECTION 7 - APPROVAL BY SIMILARITY AND SPECIFICATION OF TEST STRUCTURES

5.8.1 Section 7 for Qualification Approval - Qualification Approval by Similarity

For qualification approval, this section is only applicable when one or more components have been granted qualification approval on the basis of its/their similarity to a component to which qualification approval has been granted after a full test programme and which is the major subject of the PID.

In this section, all the components shall be described in terms of similarity to the component used as the test vehicle or example, and the extent of their agreed similarity specified. Full details of the rules applicable to the determination of the similarity shall be included together with any additional or reduced inspections and tests for the components concerned. In the case of semiconductors or integrated circuits, a photograph of each chip shall be included and shall form part of the basis for assessment of similarity. The photographs shall be to such a scale that the geometry and individual active elements may be identified.

5.8.2 Section 7 for Capability Approval - Specification of Test Structures

For capability approval, this section shall fulfil the pertinent requirements of ESCC Basic Specification No. [24300](#).

5.9 SECTION 8 - COMPONENT TYPES FOR CAPABILITY APPROVAL

Component types qualified to capability approval and manufactured in accordance with the relevant PID shall be listed in this section.

6 DISTRIBUTION

One copy of the PID, properly amended and maintained, shall be held by the Manufacturer, and one copy by the ESCC Executive. The Manufacturer may, at his discretion, prepare and maintain copies of the PID for other holders.