



**TERMS, DEFINITIONS, ABBREVIATIONS,
SYMBOLS AND UNITS FOR DISCRETE NON-
MICROWAVE SEMICONDUCTOR DEVICES**

ESCC Basic Specification No. 2135000

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1 SCOPE

This specification forms part of ESCC Basic Specification No. 21300, Terms, Definitions, Abbreviations, Symbols and Units, and covers discrete non-microwave semiconductor devices.

2 TERMS, DEFINITIONS AND ABBREVIATIONS

2.1 DIODES

2.1.1 Low Power Switching Diodes

Voltages	
Forward Continuous (Direct) Voltage (V_F)	Forward voltage is the voltage drop which results from the flow of forward current through the semiconductor diode.
Instantaneous Total Forward Voltage (V_F)	
Average Forward Voltage ($V_{F(AV)}$)	
Reverse Continuous (Direct) Voltage (V_R)	Reverse voltage is the voltage drop which results from the flow of reverse current through the semiconductor diode.
Instantaneous Total Reverse Voltage (V_R)	
Peak Reverse Voltage (V_{RM})	
Surge Reverse Voltage (V_{RSM})	
Breakdown Voltage ($V_{(BR)}$)	The breakdown voltage is the voltage measured at a specified current in the breakdown region.

Currents	
Forward Continuous (Direct) Current (I_F)	Forward current is the current flowing through the diode in the direction of lower resistance to the flow of steady direct current.
Instantaneous Forward Current (i_F)	
Peak Forward Current (F_M)	
Surge Forward Current (I_{FSM})	The surge current is the maximum current pulse which can be carried by the semiconductor or diode for the length of time, repetition frequency and waveform specified.
Average Output Rectified Current (I_o)	
Reverse Continuous (Direct) Current (I_R)	The reverse current is the current flowing through the diode in the direction of higher resistance to steady direct current when a specified reverse voltage is applied.
Instantaneous Reverse Current (i_R)	

Currents (Continued)
Peak Reverse Current (I_{RM})

Power
Surge Non-repetitive Power (I_{PSM})

Switching Parameters	
Forward Recovery Time (t_{frc})	The forward recovery time is the time required for the current or voltage to reach a specified condition after instantaneous switching from 0 or a specified reverse voltage to a specified forward biased condition.
Reverse Recovery Time (t_{rr})	The reverse recovery time is the time required for the current or voltage to reach a specified condition after instantaneous switching from a specified forward current to a specified reverse biased condition.
Reverse Recovery Current (i_{rr})	
Recovered Charge (Stored Charge) (Q_s)	

Sundry Quantities
Differential Resistance (r)
Damping Coefficient (δ or d)
Damping Resistance (r_δ or r_d)
Efficiency (η)
Rectification Efficiency (η_r)

2.1.2 Tunnel Diodes

Currents	
Peak Point Current (I_p)	
Valley Point Current (I_v)	Valley current is the current measured at the positive voltage at which the current has reached a minimum value from which it will increase if the voltage is increased further.
Peak-to-Valley Point Current Ratio (I_p/I_v)	
Reverse Continuous (Direct) Current (I_R)	

Voltages	
Peak Point Voltage (V_P)	Peak point voltage is the voltage at which peak point current occurs.
Valley Point Voltage (V_V)	Valley voltage is the voltage at which valley current occurs.
Projected Peak Point Voltage (V_{PP})	
Reverse Continuous (Direct) Voltage (V_R)	

2.1.3 Rectifier Diodes

Voltages	
Continuous (Direct) Forward Voltage (V_f)	Forward voltage is the voltage drop which results from the flow of forward current through the semiconductor diode.
Crest (Peak) Forward Voltage (V_{FM})	
Average Forward Voltage ($V_{F(AV)}$)	With I_o specified.
Continuous (Direct) Reverse Voltage (V_R)	
Crest (Peak) Working Reverse Voltage (V_{RWM})	
Repetitive Peak Reverse Voltage (V_{RRM})	Maximum recurrent reverse voltage.
Non-repetitive Peak Reverse Voltage (V_{RSM})	Peak transient reverse voltage.
Breakdown Voltage (V_{BR})	
Threshold Voltage (V_{TO})	

Currents	
Continuous (Direct) Forward Current (I_F)	
Repetitive Peak Forward Current (I_{FRM})	
Overload Forward Current ($I_{(OV)}$)	
Surge (Non-repetitive) Forward Current (I_{FSM})	The surge current is the maximum current pulse which can be carried by the semiconductor diode for the length of time, repetition frequency and waveform specified.
Average Output Rectified Current (I_o)	
Continuous (Direct) Reverse Current (I_R)	
Average Reverse Current ($I_{R(AV)}$)	With I_o specified.

Sundry Quantities
Slope Resistance (r_T)

2.1.4 Voltage Reference

Voltage Regulator Diode Current near Breakdown Knee (I_k)
Voltage Regulator Diode Current (I_z)
Voltage Regulator Impedance near Breakdown Knee (Z_k)

2.2 THYRISTORS

Principal Voltages - Anode-Cathode Voltages	
Continuous (Direct) Off-state Voltage (V_D)	The off-state is the condition of the device corresponding to the portion of the anode-cathode voltage current characteristic for forward currents less than the forward break-over current.
Peak Off-state Voltage (V_{DM})	
Crest (Peak) Working Off-state Voltage (V_{DWM})	
Repetitive Peak Off-state Voltage (V_{DRM})	
Non-repetitive Peak Off-state Voltage (V_{DSM})	
Break-over Continuous (Direct) Voltage (V_{BO})	The forward break-over voltage is the maximum positive voltage from anode to cathode for which the small signal is 0 for a specified gate current.
Continuous (Direct) On-state Voltage (V_T)	
Minimum On-state Voltage (V_{TMIN})	
On-state Threshold Voltage ($V_{T(TO)}$)	
Continuous (Direct) Reverse Voltage (V_R)	
Crest (Peak) Working Reverse Voltage (V_{RWM})	The working peak reverse voltage is the maximum instantaneous value of the reverse voltage, excluding all repetitive and non-repetitive transient voltages which occur across the device.
Repetitive Peak Reverse Voltage (V_{RRM})	The repetitive peak reverse voltage is the maximum instantaneous value of the reverse voltage, including all repetitive transient voltages, but excluding all non-repetitive transient voltages which occur across the device.
Non-repetitive Peak Reverse Voltage (V_{RSM})	The non-repetitive peak reverse voltage is the maximum instantaneous value of the reverse voltage, including all non-repetitive transient voltages, but excluding all repetitive transient voltages which occur across the device.

Principal Voltages - Anode-Cathode Voltages (Continued)	
Reverse Breakdown Voltage ($V_{(BR)}$)	The reverse breakdown voltage is the value of negative value from anode to cathode at which a transition commences from a region of high small signal resistance to a region of substantially lower small signal resistance between the anode and the cathode

Principal Currents - Anode-Cathode Currents	
Continuous (Direct) Off-state Current (I_D)	
Continuous (Direct) Break-over Current ($I_{(BO)}$)	The forward break-over current is the current through the device at forward break-over voltage.
Continuous (Direct) Holding Current (I_H)	The holding current is the minimum current through the anode required to maintain the device in the on-state for specified gate conditions and load.
Continuous (Direct) On-state Current (I_T)	The on-state is the condition of the device corresponding to the low impedance low voltage portion of the anode to cathode voltage current characteristic for positive anode to cathode voltage.
Overload On-state Current ($I_{(OV)}$)	
Repetitive Peak On-state Current (I_{TRM})	
Surge (Non-repetitive) On-state Current (I_{TSM})	
Continuous (Direct) Reverse Blocking Current (I_R)	
Repetitive Peak Reverse Current (I_{RRM})	

Gate Voltages (The gate voltage is the voltage between the gate and the terminal of the adjacent region)	
Forward Gate Continuous (Direct) Voltage (V_{FG})	
Peak Forward Gate Voltage (V_{FGM})	
Reverse Gate Continuous (Direct) Voltage (V_{RG})	
Peak Reverse Gate Voltage (V_{RGM})	
Gate Trigger Continuous (Direct) Voltage (V_{GT})	The gate trigger voltage is the voltage between the gate and the terminal of the adjacent region required to produce the gate trigger current.
Minimum Gate Trigger Voltage (V_{GTMIN})	
Gate Non-trigger Continuous (Direct) Voltage (V_{GD})	
Gate Turn-off Continuous (Direct) Voltage (V_{GQ})	The gate turn-off voltage is the voltage between the gate and the terminal of the adjacent region required to produce the gate turn-off current.

Gate Currents (The gate current is the current through the gate).	
Forward Gate Continuous (Direct) Current (I_{FG})	
Peak Forward Gate Current (I_{FGM})	
Reverse Gate Continuous (Direct) Current (I_{RG})	
Gate Trigger Continuous (Direct) Current (I_{GT})	The gate trigger current is the minimum gate current required to cause switching from the off-state to the on-state for a stated anode to cathode voltage.
Gate Non-trigger Continuous (Direct) Current (I_{GD})	
Gate Turn-off Continuous (Direct) Current (I_{GQ})	The gate turn-off current is the minimum gate current required to cause switching from the on-state to the off-state for a stated anode current in the on-state.

Time Quantities	
Gate-controlled Turn-on Time (t_{gt})	
Gate-controlled Turn-off Time (t_{gq})	
Circuit-commutated Recovery Time (t_q)	Circuit-commutated turn-off time.

Sundry Quantities	
On-state Slope Resistance (r_T)	

2.3

TRANSISTORS

Voltages	
Collector-Base (DC) Voltage (V_{CB})	
Collector-Emitter (DC) Voltage (V_{CE})	
Emitter-Base (DC) Voltage (V_{EB})	
Base-Emitter (DC) Voltage (V_{BE})	
Collector-Base (DC) Voltage (V_{CBO})	With $I_E = 0$; I_C specified.
Emitter-Base (DC) Voltage (V_{EBO})	With $I_C = 0$; I_E specified.
Collector-Emitter (DC) Voltage (V_{CEO})	With $I_B = 0$; I_C specified.
Collector-Emitter (DC) Voltage (V_{CER})	With $R_{BE} = R$; I_C specified.
Collector-Emitter (DC) Voltage (V_{CES})	With $V_{BE} = 0$; I_C specified.
Collector-Emitter (DC) Voltage (V_{CEX})	With $V_{BE} = X$ specified (reverse biased Emitter-Base); I_C specified.

Voltages (Continued)	
Floating Voltage, Emitter-Base (V_{EBfi})	With $I_E = 0$; V_{CB} specified. A floating potential is the DC voltage between an open-circuited terminal and the reference point when a DC voltage is applied to the other terminals as specified.
Punch-through (Penetration) Voltage (V_{pt})	
Saturation Voltage, Collector-Emitter (V_{CEsat})	With I_B specified; I_C specified. The saturation voltage is the DC voltage between the measurement electrode and the reference electrode for specified saturation conditions.
Saturation Voltage, Base-Emitter (V_{BEsat})	With I_B specified; I_C specified.

Breakdown Voltages (Open Circuit) (The abbreviation BV is in common use for these quantities) (Breakdown voltage is the voltage measured at a specified current in the breakdown region)	
Breakdown Voltage, Collector-Base ($V_{(BR)CBO}$)	With $I_E = 0$; I_C specified.
Breakdown Voltage, Emitter-Base ($V_{(BR)EBO}$)	With $I_C = 0$; I_C specified.
Breakdown Voltage, Collector-Emitter ($V_{(BR)CEO}$)	With $I_B = 0$; I_C specified.

Breakdown Voltages (Specified Circuit) (The abbreviation BV is in common use for these quantities) (Breakdown voltage is the voltage measured at a specified current in the breakdown region)	
Breakdown Voltage, Collector-Emitter ($V_{(BR)CER}$)	With $R_{BE} = R$; I_C specified.
Breakdown Voltage, Collector-Emitter ($V_{(BR)CEX}$)	With $R_{BE} = X$; I_C specified.

Breakdown Voltage (Short Circuit) (The abbreviation BV is in common use for this quantity) (Breakdown voltage is the voltage measured at a specified current in the breakdown region)	
Breakdown Voltage, Collector-Emitter ($V_{(BR)CES}$)	With $V_{BE} = 0$; I_C specified.

Currents	
Base (DC) Current (I_B)	
Collector (DC) Current (I_C)	

Currents (Continued)	
Emitter (DC) current (I_E)	
Collector Cut-off Current (I_{CBO})	With $I_E = 0$; V_{CB} specified.
Collector Cut-off Current (I_{CEO})	With $I_B = 0$; V_{CE} specified.
Emitter Cut-off Current (I_{EBO})	With $I_C = 0$; V_{EB} specified.
Collector Cut-off Current (I_{CER})	With $R_{BE} = R$; V_{CE} specified.
Collector Cut-off Current (I_{CES})	With $V_{BE} = 0$; V_{CE} specified.
Collector Cut-off Current (I_{CEX})	With $V_{BE} = X$; V_{CE} specified.
Base Cut-off Current (I_{BEX})	With $V_{BE} = X$; V_{CE} specified.

Powers	
Collector Power Dissipation (P_C)	With T_{amb} or T_{case} specified.
Total Input Power (DC or average) to all Electrodes (P_{tot})	With T_{amb} or T_{case} specified.

Electrical Parameters - Static Parameters (Specified for bias conditions)	
Static Value of the Forward Current Transfer Ratio: In Common Emitter Configuration (h_{21E} or h_{FE})	Static forward current transfer ratio is the ratio of the DC output current to the DC input current under specified test conditions. $h_{21E} = \frac{I_C}{I_B} = \frac{I_E}{I_B} - 1$ with $V_{CE} = \text{constant}$.

Electrical Parameters - Small Signal Parameters (Specified for bias and frequency conditions)	
Small signal value of the Open-Circuit Reverse Voltage Transfer Ratio: In Common Emitter configuration (h_{12e} or h_{re}) In Common Base configuration (h_{12b} or h_{rb})	Small signal open-circuit reverse voltage transfer ratio is the ratio of the AC input voltage to the AC output voltage, with 0 AC input current. $h_{12e} = \frac{V_{be}}{V_{ce}}$ with $I_b = \text{constant}$. $h_{12b} = \frac{V_{eb}}{V_{cb}}$ with $I_e = \text{constant}$.

Electrical Parameters - Small Signal Parameters (Specified for bias and frequency conditions) (Continued)	
<p>Small signal value of the Short-Circuit Forward Current Transfer Ratio:</p> <p>In Common Emitter configuration (h_{21e} or h_{fe})</p> <p>In Common Base configuration (h_{21b} or h_{fb})</p>	<p>Small signal short-circuit forward current transfer ratio is the ratio of the AC output current to the AC input current, with 0 AC output voltage.</p> $h_{21e} = \frac{I_c}{I_b} \text{ with } V_{ce} = \text{constant.}$ $h_{21b} = \frac{I_c}{I_e} \text{ with } V_{cb} = \text{constant.}$
<p>Small signal value of the Open-Circuit Output Admittance:</p> <p>In Common Emitter configuration (h_{22e} or h_{oe})</p> <p>In Common Base configuration (h_{22b} or h_{ob})</p>	<p>Small signal open-circuit output admittance is the ratio of the AC output current to the AC voltage applied to the output terminals, with 0 AC input current.</p> $h_{22e} = \frac{I_c}{V_{ce}} \text{ with } I_b = \text{constant.}$ $h_{22b} = \frac{I_c}{V_{cb}} \text{ with } I_e = \text{constant.}$

Electrical Parameters - Capacitance	
<p>Input Capacitance:</p> <p>In Common Emitter configuration (C_{11es} or C_{ies})</p> <p>In Common Base configuration (C_{11bs} or C_{ibs})</p>	<p>Output short circuited to AC.</p> $h_{11e} = \text{Re} (h_{11e}) + \frac{1}{i\omega C_{11es}}$ $h_{11b} = \text{Re} (h_{11b}) + \frac{1}{i\omega C_{11bs}}$
<p>Output Capacitance:</p> <p>In Common Base configuration (C_{22bo} or C_{obo})</p>	<p>Input open circuited to AC.</p> $h_{22b} = \text{Re} (h_{22b}) + i\omega C_{22bo}$
<p>Output Capacitance:</p> <p>In Common Emitter configuration (C_{22es} or C_{oes})</p> <p>In Common Base configuration (C_{22bs} or C_{obs})</p>	<p>Input short circuited to AC.</p> $y_{22e} = \text{Re} (y_{22e}) + i\omega C_{22es}$ $y_{22b} = \text{Re} (y_{22b}) + i\omega C_{22bs}$

Electrical Parameters - Frequency Parameters
<p>Cut-off Frequency:</p> <p>In Common Emitter configuration (fh_{21e} or fh_{fe})</p> <p>In Common Base configuration (fh_{21b} or fh_{fb})</p> <p>In Common Collector configuration (fh_{21c} or fh_{fc})</p>

Electrical Parameters - Frequency Parameters (Continued)	
Frequency of Unity Current Transfer Ratio (f_i)	$f_T = f \times h_{21e} = 1$
Transistor Frequency (f_T)	$f_T = f \times h_{21e}$ (h_{21e} is measured in a region where the roll-off is 6dB/octave).
Maximum Frequency of Oscillation (f_{max})	<p>The maximum frequency of oscillation is the highest frequency at which a transistor will oscillate under specified conditions.</p> <p>NOTES: This also approximates the frequency at which maximum available power gain has decreased to unity.</p>

Electrical Parameters - Switching Parameters	
Pulse Average Time	<p>Symbol under consideration.</p> <p>The average pulse time of a pulse is the time duration from a point on the leading edge which is 50% of the maximum amplitude to a point on the trailing edge which is 50% of the maximum amplitude.</p>
Pulse Time (t_p)	<p>The pulse time of a pulse is the time interval from the point on the leading edge which is 90% of the maximum amplitude to a point on the trailing edge which is 90% of the maximum amplitude.</p>
Duty Cycle	Symbol under consideration.
Delay Time (t_d)	<p>The delay time of a pulse is the time interval from a point at which the leading edge of the input pulse has risen to 10% of its maximum amplitude to a point at which the leading edge of the output pulse has risen to 10% of its maximum amplitude.</p>
Rise Time (t_r)	<p>The rise time of a pulse is that time duration during which the amplitude of its leading edge is increasing from 10% to 90% of the maximum amplitude.</p>
Carrier Storage Time (t_s)	<p>The storage time of a pulse is the time interval from a point 10% down from the maximum amplitude on the trailing edge of the input pulse to a point 10% down from the maximum amplitude on the trailing edge of the output pulse.</p>
Fall Time (t_f)	<p>The fall time of a pulse is that time duration during which the amplitude of its leading edge is decreasing from 90% to 10% of the maximum amplitude.</p>
Turn-on Time (t_d+t_r)	Under consideration.
Turn-off Time (t_s+t_f)	Under consideration.
Emitter Depletion Layer Capacitance (C_{Te})	
Collector Depletion Layer Capacitance (C_{Tc})	

Electrical Parameters - Switching Parameters (Continued)	
Collector Time Coefficient (τ_C)	
Rise Time Coefficient (τ_R)	
Fall Time Coefficient (τ_F)	
Stored Charge (Q_s)	
Transient Current Ratio in Saturation ($h_{21 E_{sat}}$ or $h_{FE_{sat}}$)	
Collector Emitter Saturation Resistance: Small Signal Value (r_{cesat}); Static Value ($r_{CE_{sat}}$)	


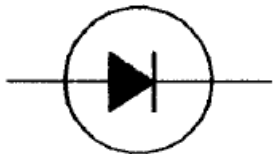
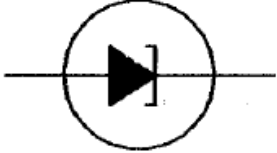
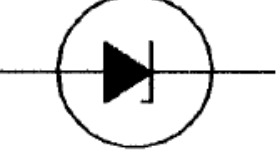

Electrical Parameters - Sundry Quantities	
Noise (N, n)	At a selected input frequency, the noise figure is the ratio of the total noise power per unit bandwidth (at a corresponding output frequency) delivered to the output termination, to the portion thereof contributed at the input frequency by the input termination, whose noise temperature is standard ($293+5^\circ K$) at all frequencies.
Noise Current (I_n)	
Noise Voltage (V_n)	
Noise Power (P_n)	
Effective Noise Bandwidth (B)	
Amplification	Symbol under consideration.
Current Amplification	Symbol under consideration.
Voltage Amplification	Symbol under consideration.


Electrical Parameters - Gain (G)	
Power Gain: For small signals For large signals	The large signal power gain is the ratio of the AC output power to the AC input power under specified large signal conditions. It is usually expressed in decibels (dB).
Insertion Power Gain	Symbol under consideration.
Transducer Power Gain	Symbol under consideration.
Available Power Gain	Symbol under consideration.
Maximum Power Gain	Symbol under consideration.
Efficiency (η)	
Collector Efficiency (η_c)	

Electrical Parameters - External Circuit Parameters
Emitter (DC) Voltage Supply (V_{EE})
Base (DC) Voltage Supply (V_{BB})
Collector (DC) Voltage Supply (V_{CC})
External Emitter Resistance (R_E)
External Base Resistance (R_B)
External Collector Resistance (R_C)
External Resistance connecting Base to Emitter (R_{BE})
Generator Resistance (R_G)
Load Resistance (R_L)
Load Capacitance (C_L)




3 GRAPHIC SYMBOLS

3.1 DIODES

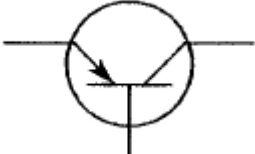
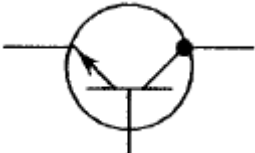

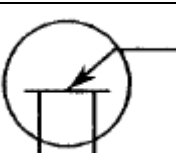
SYMBOL	DESCRIPTION
	P/N Diode
	Semiconductor rectifier diode
	Tunnel Diode
	Breakdown diode, uni-directional
	Breakdown diode bi-directional

SYMBOL	DESCRIPTION
	Bi-directional diode (transistor)

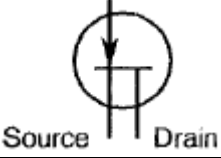

3.2 THYRISTORS

SYMBOL	DESCRIPTION
	Triode thyristor (general symbol)
	Reverse blocking triode thyristor, N-gate
	Reverse blocking triode thyristor, P-gate

3.3 TRANSISTORS

SYMBOL	DESCRIPTION
	PNP Transistor
	NPN Transistor with collector connected to the envelope
	Uni-junction transistor with P-type base
	Uni-junction transistor with N-type base

3.4 FIELD-EFFECT TRANSISTORS

SYMBOL	DESCRIPTION
 The symbol for an N-channel JFET consists of a circle with a vertical line through its center. A horizontal bar is drawn across the circle, intersecting the vertical line. Two vertical lines extend downwards from the intersection point, representing the source and drain terminals. An arrow points downwards from the top of the vertical line, indicating the direction of current flow. The word "Source" is written below the left terminal and "Drain" is written below the right terminal. <p>Source Drain</p>	Junction field-effect transistor with N-type channel
 The symbol for a P-channel JFET is similar to the N-channel symbol, but the arrow on the vertical line points upwards from the gate region. <p></p>	Junction field-effect transistor with P-type channel