



**DIODES, MICROWAVE, SILICON, PIN**

**BASED ON TYPE BXY42 - MESA**

**ESCC Detail Specification No. 5513/017**

Issue 4	November 2014
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DCR No.	CHANGE DESCRIPTION
864	Specification upissued to incorporate technical changes per DCR.

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## **1 GENERAL**

### **1.1 SCOPE**

This specification details the ratings, physical and electrical characteristics, test and inspection data for a Diode, Microwave, Silicon, PIN, based on Type BXY42 - Mesa. It shall be read in conjunction with ESCC Generic Specification No. 5010, the requirements of which are supplemented herein.

### **1.2 TYPE VARIANTS**

Variants of the basic diodes specified herein, which are also covered by this specification are given in Table 1(a).

### **1.3 MAXIMUM RATINGS**

The maximum ratings, which shall not be exceeded at any time during use or storage, applicable to the diodes specified herein, are as scheduled in Table 1(b).

### **1.4 PARAMETER DERATING INFORMATION**

The derating information applicable to the diodes specified herein is shown in Figure 1.

### **1.5 PHYSICAL DIMENSIONS**

The physical dimensions of the diodes specified herein are shown in Figure 2.

### **1.6 FUNCTIONAL DIAGRAM**

The functional diagram, showing lead identification, of the diodes specified herein, is shown in Figure 3.

### **1.7 HANDLING PRECAUTIONS**

These devices are susceptible to damage by electrostatic discharge. Therefore, suitable precautions shall be employed for protection during all phases of manufacture, testing, packaging, shipment and any handling.

These components are Categorised as Class 1 with a Minimum Critical Path Failure Voltage of 1000V.

## **2 APPLICABLE DOCUMENTS**

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. 5010 for Discrete Microwave Semiconductor Components.
- (b) MIL-STD-750, Test Methods for Semiconductor Devices.

## **3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS**

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

**TABLE 1(a) - TYPE VARIANTS**

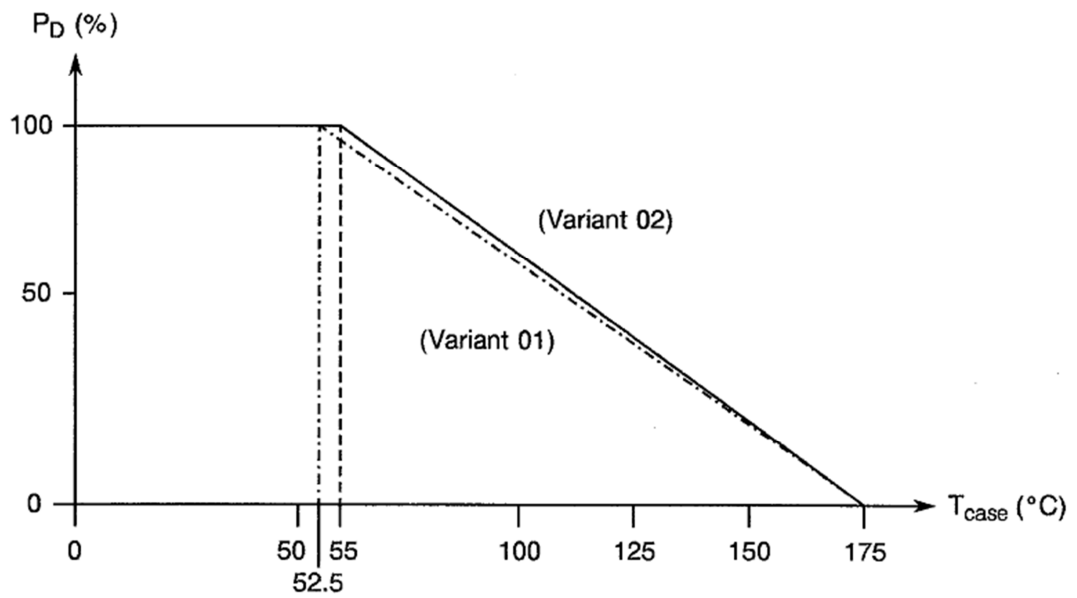
Variant	Based on Type	Figure	Terminal/Lead Material and Finish
01	BXY42	2(a)	D2
02	BXY42	2(b)	D2

**TABLE 1(b) MAXIMUM RATINGS**

No.	Characteristics	Symbol	Maximum Rating	Unit	Remarks
1	DC Reverse Voltage	$V_R$	-50	V	-
2	Peak Forward Current	$I_{FM}$	5	A	Note 1
3	Power Dissipation Variant 01 Variant 02	$P_D$	350 600	mW	Note 2 Note 3
4	Operating Temperature Range	$T_{op}$	-55 to +175	°C	$T_{case}$
5	Storage Temperature Range	$T_{stg}$	-65 to +175	°C	-
6	Soldering Temperature	$T_{sol}$	+250	°C	Note 4
7	Junction Temperature	$T_J$	+175	°C	-
8	Thermal Resistance (Junction to Case) Variant 01 Variant 02	$R_{TH(J-C)}$	350 200	°C/W	$I_F = 1.0mA$

**NOTES**

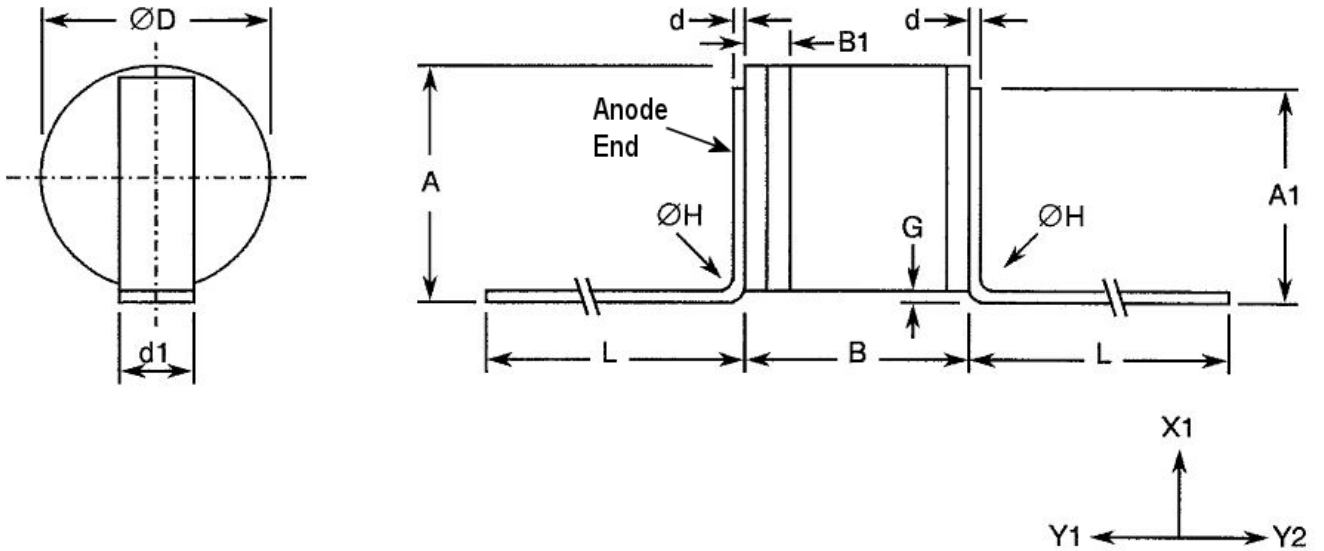
- At  $t_p = 1.0\mu s$ , Duty Cycle = 0.001%.
- At  $T_{case} = +52.5^\circ C$ . For derating at  $T_{case} > +52.5^\circ C$ , see Figure 1.
- At  $T_{case} = +55^\circ C$ . For derating at  $T_{case} > +55^\circ C$ , see Figure 1.
- Duration 5 seconds maximum and the same termination shall not be resoldered until 5 minutes have elapsed.

**FIGURE 1 - PARAMETER DERATING INFORMATION**Power Dissipation versus Temperature



**FIGURE 2 - PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATIONS**

**FIGURE 2(a) - VARIANT 01**

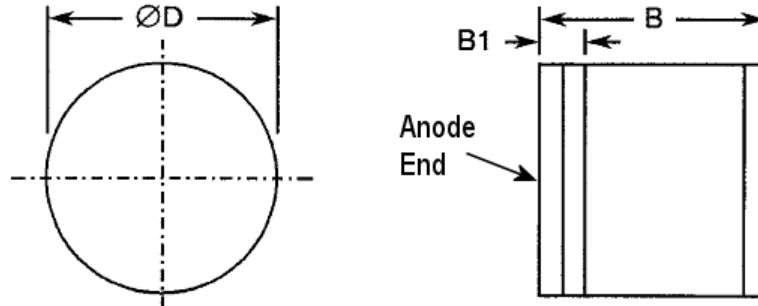


Symbol	Millimetres		Notes
	Min	Max	
A	1.4	1.95	
A1	1.05	1.25	
B	1.15	1.35	
B1	-	0.4	1
d	0.06	0.1	
d1	0.4	0.6	
$\varnothing D$	1.3	1.45	
G	0.1	0.5	
$\varnothing H$	-	0.3	
L	5.5	-	

**NOTES**

1. Terminal identification: the anode end is identified by the sealing ring and lid (dimension B1).

**FIGURE 2(b) - VARIANT 02**

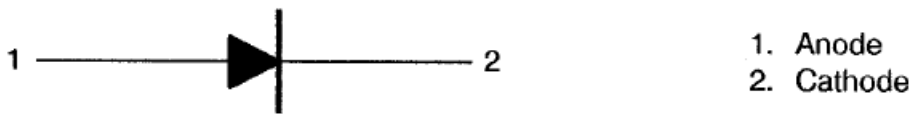


Symbol	Millimetres		Notes
	Min	Max	
B	1.15	1.35	
B1	-	0.4	1
ØD	1.3	1.45	

**NOTES**

- Terminal identification: the anode end is identified by the sealing ring and lid (dimension B1).

**FIGURE 3 - FUNCTIONAL DIAGRAM**



**4 REQUIREMENTS**

**4.1 GENERAL**

The complete requirements for procurement of the diodes specified herein shall be as stated in this specification and ESCC Generic Specification No. 5010 for Discrete Microwave Semiconductor Components. Deviations from the Generic Specification applicable to this specification only, are listed in Para. 4.2.

Deviations from the applicable Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirements and do not affect the components' reliability, are listed in the appendices attached to this specification.

**4.2 DEVIATIONS FROM GENERIC SPECIFICATION**

**4.2.1 Deviations from Special In-process Controls**

- Para. 5.2.2, Total Dose Irradiation Testing: Shall be performed during qualification and extension of qualification.
- Para. 5.2.2, Total Dose Irradiation Testing: Shall be performed during procurement on a lot acceptance basis at the total dose irradiation level specified in the purchase order.
- Para. 5.3, Wafer Lot Acceptance: Shall be performed as an SEM Inspection only.

#### 4.2.2 Deviations from Final Production Tests (Chart II)

- (a) Para. 9.2.2, Die Shear: Forces need not be recorded and the residual die area shall be  $\geq 50\%$  of the original die size.
- (b) Para. 9.6, Constant Acceleration: Shall not be performed.
- (c) Para. 9.14, Vibration, Variable Frequency: Shall not be performed.
- (d) Para. 9.7, PIND test shall be performed in accordance with condition 'A'; it may be performed at any point after indicated position in Chart II.

#### 4.2.3 Deviations from Burn-in and Electrical Measurements (Chart III)

- (a) Para. 9.9.1, Initial Parameter Drift Value Measurements: Parameters measured in Chart II shall not be repeated during electrical measurements at room temperature.
- (b) Para. 9.12, Radiographic Inspection: Shall be performed in X and Z axes only, exposure 100kV, duration 5 minutes.
- (c) Para. 9.12, Radiographic Inspection may be performed at any point after completed Power Burn-in (Chart III).

#### 4.2.4 Deviations from Qualification Tests (Chart IV)

- (a) Para. 9.2.2, Die Shear: Forces need not be recorded and the residual die area shall be  $\geq 50\%$  of the original die size.
- (b) Para. 9.15, Constant Acceleration: Shall not be performed.
- (c) Para. 9.20, Operating Life: Only one Operating Life Test shall be performed on 30 pieces.
- (d) Para. 9.23, Special Testing: Shall not be performed.

#### 4.2.5 Deviations from Lot Acceptance Tests (Chart V)

- (a) Para. 9.14, Vibration: Shall not be performed.
- (b) Para. 9.5, Thermal shock and Para. 9.16, Moisture Resistance, shall be done in sequence on all 6 components of the Environmental/Mechanical Subgroups.
- (c) Para. 9.15, Constant Acceleration: Shall not be performed.
- (d) Para. 9.20, Operating Life: Only one Operating Life Test shall be performed on 16 components.
- (e) Para. 9.23, Special Testing: Shall not be performed.

### 4.3 MECHANICAL AND ENVIRONMENTAL REQUIREMENTS

#### 4.3.1 Dimension Check

The dimensions of the diodes specified herein shall be checked. They shall conform to those shown in Figure 2.

#### 4.3.2 Weight

The maximum weight of the diodes specified herein shall be 0.02 grammes.

#### 4.3.3 Terminal Strength

The requirements for terminal strength testing are specified in Section 9 of ESCC Generic Specification No. 5010. The test conditions shall be as follows:

Test Condition : 'A' (Tension).

Force : 1.22 Newtons.

Duration : 5 seconds.

Applicable to Variant 01 only.

#### 4.4 MATERIALS AND FINISHES

The materials and finishes shall be as specified herein. Where a definite material is not specified, a material which will enable the diodes specified herein to meet the performance requirements of this specification shall be used. Acceptance or approval of any constituent material does not guarantee acceptance of the finished product.

##### 4.4.1 Case

Cylinder ring: Ceramic.

##### 4.4.2 Terminal and Lead Material and Finish

The terminals shall be Type 'D' with Type '2' finish in accordance with the requirements of ESCC Basic Specification No. 23500. The ribbon leads of Type Variant 01 shall be Type 'D' with Type '2' finish in accordance with the requirements of ESCC Basic Specification No. 23500.

#### 4.5 MARKING

##### 4.5.1 General

The marking of all components delivered to this specification shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and the following paragraphs. When the component is too small to accommodate all of the marking specified, as much as space permits shall be marked and the marking information, in full, shall accompany the component in its primary package.

The information to be marked and the order of precedence, shall be as follows:

- (a) Terminal Identification.
- (b) The ESCC Component Number.
- (c) Traceability Information.

##### 4.5.2 Cathode Identification

Cathode identification shall be as shown in Figures 2 and 3 of this specification.

##### 4.5.3 The ESCC Component Number

Each component shall bear the ESCC Component Number which shall be constituted and marked as follows:

Example: 551301701BF

- Detail Specification Number: 5513017
- Type Variant (see Table 1(a)): 01
- Testing Level (B or C, as applicable): B
- Total Dose Irradiation Level (if applicable): F

The Total Dose Irradiation Level designation shall be added for those devices for which a sample has been successfully tested to the level in question. For these devices, a code letter shall be added in accordance with the requirements of ESCC Basic Specification No. 22900.

##### 4.5.4 Traceability Information

Each component shall be marked in respect of traceability information as defined in ESCC Basic Specification No. 21700.

#### 4.6 ELECTRICAL MEASUREMENTS

##### 4.6.1 Electrical Measurements at Room Temperature

The parameters to be measured at room temperature are scheduled in Table 2. Unless otherwise specified, the measurements shall be performed at  $T_{amb} = +22 \pm 3^{\circ}\text{C}$ .

##### 4.6.2 Electrical Measurements at High and Low Temperatures

The parameters to be measured at high and low temperatures are scheduled in Table 3. Unless otherwise specified, the measurements shall be performed at  $T_{amb} = +125^{\circ}\text{C}$ .

##### 4.6.3 Circuits for Electrical Measurements

Circuits for use in performing electrical measurements listed in Table 2 of this specification are shown in Figure 4.

#### 4.7 BURN-IN TESTS

Burn-in shall be Category 3 of Chart III(a).

##### 4.7.1 Parameter Drift Values

The parameter drift values applicable to burn-in are specified in Table 4 of this specification. Unless otherwise stated, measurements shall be performed at  $T_{amb} = +22 \pm 3^{\circ}\text{C}$ . The parameter drift values ( $\Delta$ ) applicable to the scheduled parameters shall not be exceeded. In addition to these drift value requirements for a given parameter, the appropriate limit value specified in Table 2 shall not be exceeded.

##### 4.7.2 Conditions for High Temperature Reverse Bias Burn-in

The requirements for high temperature reverse bias burn-in are specified in Section 7 of ESCC Generic Specification No. 5010. The conditions for high temperature reverse bias burn-in shall be as specified in Table 5(a) of this specification.

##### 4.7.3 Electrical Circuit for High Temperature Reverse Bias Burn-in

The circuit for use in performing the H.T.R.B. burn-in tests is shown in Figure 5(a) of this specification.

##### 4.7.4 Conditions for Power Burn-in

The requirements for power burn-in are specified in Section 7 of ESCC Generic Specification No. 5010. The conditions for power burn-in shall be as specified in Table 5(b) of this specification.

##### 4.7.5 Electrical Circuit for Power Burn-in

The circuit for use in performing the power burn-in tests is shown in Figure 5(b) of this specification.

**TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - DC PARAMETERS**

No.	Characteristics	Symbol	MIL-STD-750 Test Method	Test Conditions	Limits		Unit
					min.	max.	
1	Reverse Current 1	$I_{R1}$	4016	$V_R = -50V$	-	10	$\mu A$
2	Reverse Current 2	$I_{R2}$	4016	$V_R = -40V$	-	5	nA
3	Forward Voltage	$V_F$	4011	$I_F = 100mA$	-	1.1	V

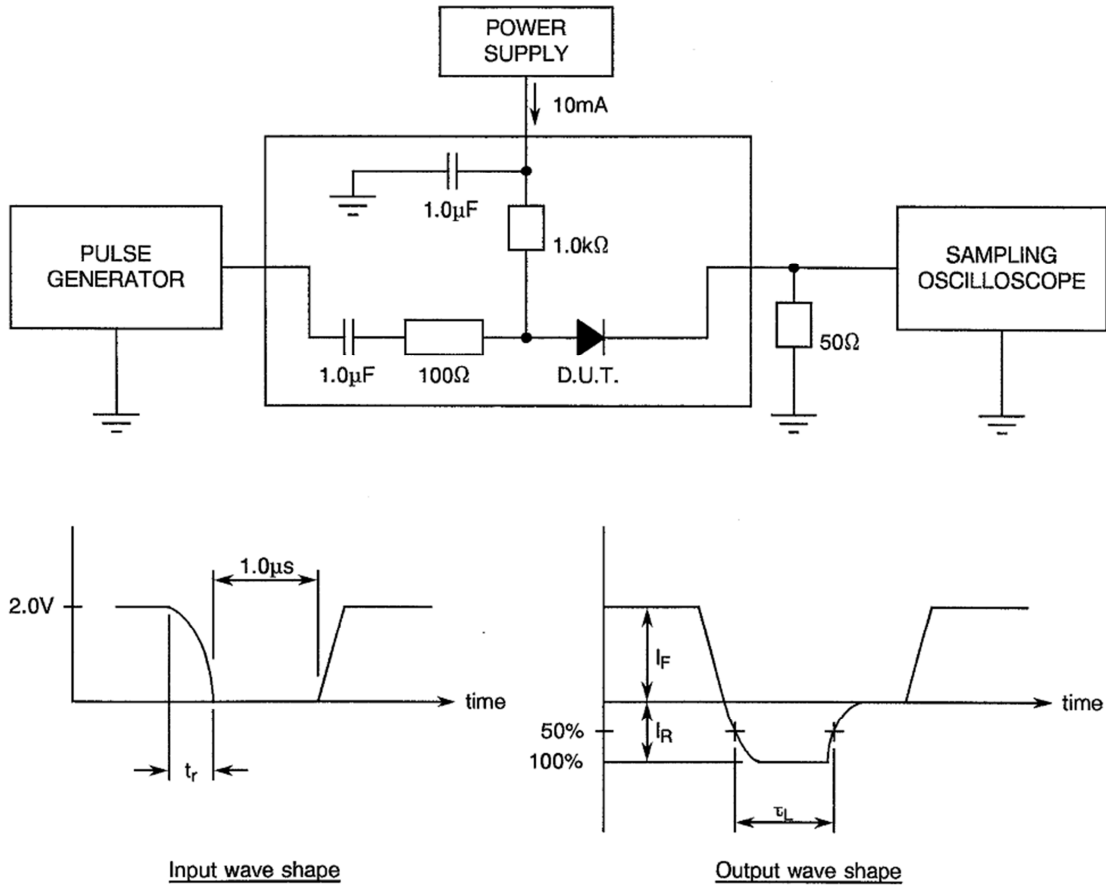
**TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - AC PARAMETERS**

No.	Characteristics	Symbol	MIL-STD-750 TEST METHOD	Test Fig.	Test Conditions	Limits		Unit
						min.	max.	
4	Total Capacitance	$C_T$	4001	-	$V_R = -20V$ $f = 1.0MHz$	-	0.24	pF
5	Forward Resistance 1	$R_{F1}$	4056	-	$f = 100MHz$ $I_{F1} = 1.0mA$	-	3.5	$\Omega$
6	Forward Resistance 2	$R_{F2}$	4056	-	$f = 100MHz$ $I_{F2} = 10mA$	-	2.5	$\Omega$
7	Minority Carrier Lifetime	$\tau_L$	-	4	$I_F = 10mA$ $I_R = -6.0mA$	35	-	ns

**TABLE 3 - ELECTRICAL MEASUREMENTS AT HIGH AND LOW TEMPERATURES**

No.	CHARACTERISTICS	SYMBOL	SPEC. AND/OR TEST METHOD	TEST CONDITIONS	LIMITS		UNIT
					MIN.	MAX.	
2	Reverse Current 2	$I_{R2}$	As per Table 2	As per Table 2	-	100	nA

**FIGURE 4 - TEST CIRCUIT FOR MINORITY CARRIER LIFETIME MEASUREMENT**



**NOTES**

1. The value of the reverse current  $I_R$  is -6.0mA and the forward current  $I_F$  is 10mA.
2. The lifetime ( $\tau_L$ ) is measured across the 50% points of the observed wave shape. The input pulse is provided by a pulse generator having a rise time of less than 5ns. The output pulse is amplified and observed on a sampling oscilloscope.
3. Pulse length: 1.0µs.
4. Duty cycle:  $\leq 33\%$ .

**TABLE 4 - PARAMETER DRIFT VALUES**

No.	Characteristics	Symbol	Spec. and/or Test Method	Test Conditions	Change Limits ( $\Delta$ )	Unit
2	Reverse Current 2	$I_{R2}$	As per Table 2	As per Table 2	$\pm 3$ (1)	nA
3	Forward Voltage	$V_F$	As per Table 2	As per Table 2	$\pm 20$ (1) or (2) $\pm 5$ (1)	mV %
5	Forward Resistance 1	$R_{F1}$	As per Table 2	As per Table 2	$\pm 15$ (1)	%

**NOTES**

1.  $\Delta 1 = \Delta 2 = \Delta 3$ .
2. Whichever is the greater, referred to the initial measurement.

**TABLE 5(a) - CONDITIONS FOR HIGH TEMPERATURE REVERSE BIAS BURN-IN**

No.	Characteristics	Symbol	Conditions	Unit
1	Ambient Temperature	$T_{amb}$	+150 (+0 -5)	°C
2	Reverse Voltage	$V_R$	-40	V

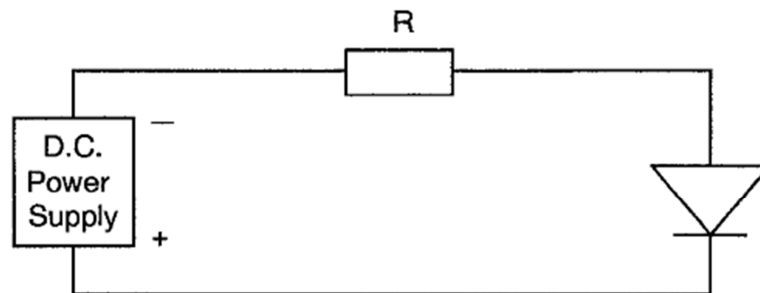
**NOTES**

- At the end of the H.T.R.B.,  $T_{amb}$  shall be decreased to room temperature and the reverse bias shall remain applied until  $T_{amb}$  is less than +35°C.

**TABLE 5(b) - CONDITIONS FOR POWER BURN-IN AND OPERATING LIFE TESTS**

No.	Characteristics	Symbol	Conditions	Unit
1	Junction Temperature	$T_j$	+175 (+0 -3)	°C
2	Forward Current	$I_F$	50	mA

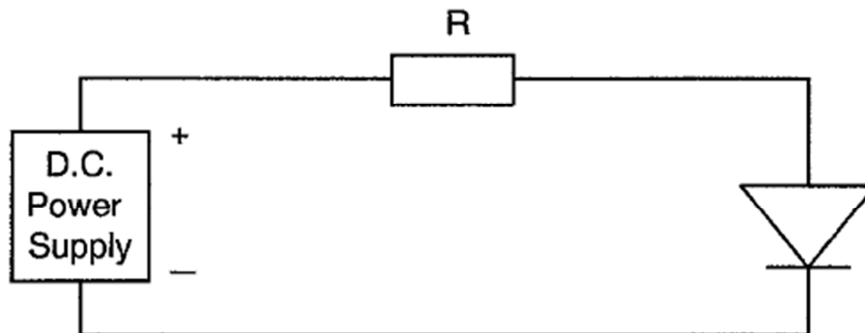
**FIGURE 5(a) - ELECTRICAL CIRCUIT FOR HIGH TEMPERATURE REVERSE BIAS BURN-IN**



**NOTES**

- $R = 22k\Omega$

**FIGURE 5(b) ELECTRICAL CIRCUIT FOR POWER BURN-IN AND OPERATING LIFE TESTS**



**NOTES**

- $R = 3.9\Omega$



#### 4.8 ENVIRONMENTAL AND ENDURANCE TESTS (CHARTS IV AND V OF ESCC GENERIC SPECIFICATION NO. 5010)

##### 4.8.1 Electrical Measurements on Completion of Environmental Tests

The parameters to be measured on completion of environmental tests are scheduled in Table 2. Unless otherwise stated, the measurements shall be performed at  $T_{amb} = +22 \pm 3^{\circ}\text{C}$ .

##### 4.8.2 Electrical Measurements at Intermediate Points and on Completion of Endurance Tests

The parameters to be measured at intermediate points and on completion of endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at  $T_{amb} = +22 \pm 3^{\circ}\text{C}$ .

##### 4.8.3 Conditions for Operating Life Tests (Part of Endurance Testing)

The requirements for operating life testing are specified in Section 9 of ESCC Generic Specification No. 5010. The conditions for operating life testing are specified in Table 5(b) of this specification.

##### 4.8.4 Electrical Circuits for Operating Life Tests

The circuit to be used for performance of the operating life test shall be the same as shown in Figure 5(b) for Power Burn-in.

#### 4.9 TOTAL DOSE IRRADIATION TESTING

##### 4.9.1 Application

If specified in Para. 4.2.1 of this specification, total dose irradiation testing shall be performed in accordance with the requirements of ESCC Basic Specification No. 22900.

##### 4.9.2 Bias Conditions

Continuous bias shall be applied during irradiation testing as shown in Figure 6 of this specification.

##### 4.9.3 Electrical Measurements

The parameters to be measured prior to irradiation exposure are scheduled in Table 2 of this specification. Only devices which meet the requirements of Table 2 shall be included in the test sample.

The parameters to be measured during and on completion of irradiation testing are scheduled in Table 7 of this specification.

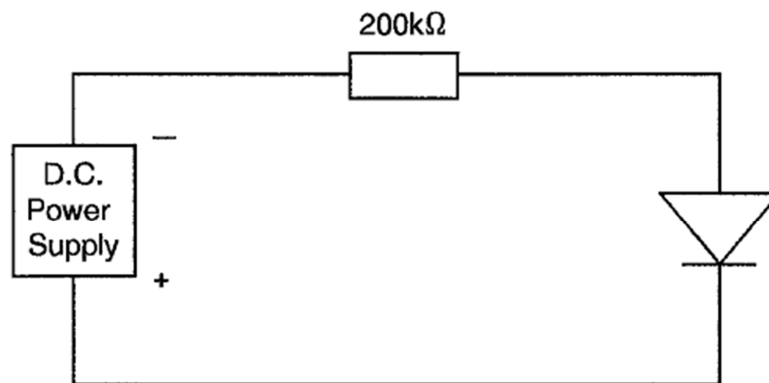
#### 4.10 SPECIAL TESTING

Not applicable.

**TABLE 6 - ELECTRICAL MEASUREMENTS AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING**

No.	Characteristics	Symbol	Spec. and/or Test Method	Test Conditions	Limits		Unit
					min.	max.	
1	Reverse Current 1	$I_{R1}$	As per Table 2	As per Table 2	-	10	$\mu\text{A}$
2	Reverse Current 2	$I_{R2}$	As per Table 2	As per Table 2	-	5	nA
3	Forward Voltage	$V_F$	As per Table 2	As per Table 2	-	1.1	V
4	Total Capacitance	$C_T$	As per Table 2	As per Table 2	-	0.24	pF
5	Forward Resistance 1	$R_{F1}$	As per Table 2	As per Table 2	-	3.5	$\Omega$
6	Forward Resistance 2	$R_{F2}$	As per Table 2	As per Table 2	-	2.5	$\Omega$

**FIGURE 6 - BIAS CONDITIONS FOR IRRADIATION TESTING**



**NOTES**

1. A reverse bias of  $V_R = -10\text{V}$  shall be applied.

**TABLE 7 - ELECTRICAL MEASUREMENTS DURING AND ON COMPLETION OF IRRADIATION TESTING**

No.	Characteristics	Symbol	Spec. and/or Test Method	Test Conditions	Change Limits ( $\Delta$ )	Unit
1	Reverse Current 1	$I_{R1}$	As per Table 2	As per Table 2	$\pm 1$	$\mu A$
5	Forward Resistance 1	$R_{F1}$	As per Table 2	As per Table 2	$\pm 35$	%

**NOTES**

- The graph given below shall be used to determine the maximum permitted change on  $R_{F1}$ .

