



**TRANSISTORS, FIELD-EFFECT, N-CHANNEL**

**BASED ON TYPE 2N4416 AND 2N4416A**

**ESCC Detail Specification No. 5205/004**

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## 1 GENERAL

### 1.1 SCOPE

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

### 1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. 5000
- (b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices

### 1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

### 1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

#### 1.4.1 The ESCC Component Number

The ESCC Component Number shall be constituted as follows:

Example: 520500401

- Detail Specification Reference: 5205004
- Component Type Variant Number: 01 (as required)

#### 1.4.2 Component Type Variants

The component type variants applicable to this specification are as follows:

Variant Number	Based on Type	Case	Lead/Terminal Material and/or Finish	Weight max g
01	2N4416	TO-72	D2	0.9
02	2N4416A	TO-72	D2	0.9
03	2N4416A	CCP	2	0.06
04	2N4416A	CCP	2	0.06
05	2N4416A	CCP	4	0.06
06	2N4416A	CCP	4	0.06

The lead/terminal material and/or finish shall be in accordance with the requirements of ESCC Basic Specification No. 23500.

1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

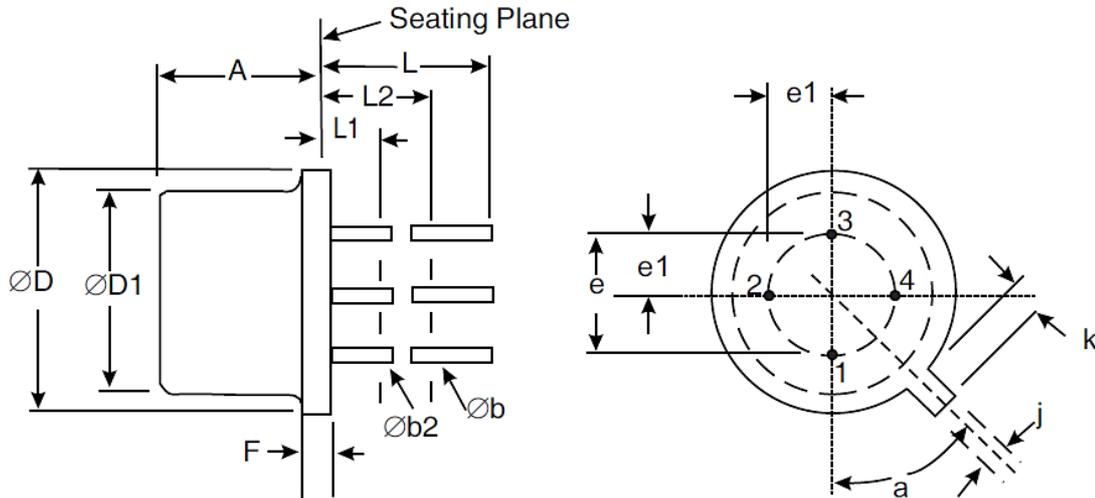
Characteristics	Symbols	Maximum Ratings	Unit	Remarks
Drain-Source Voltage	$V_{DS}$	30	V	Over entire operating temperature range
Gate-Source Voltage Variant 01 All other Variants	$V_{GS}$	-30 -35	V	
Gate-Drain Voltage Variant 01 All other Variants	$V_{GD}$	-30 -35	V	
Gate Current	$I_G$	10	mA	
Power Dissipation	$P_{tot}$	300	mW	At $T_{amb} \leq +25^{\circ}C$
Thermal Resistance, Junction-to-Ambient For TO-72 For CCP	$R_{th(j-a)}$	416.7 583	$^{\circ}C/W$	
Thermal Resistance, Junction-to-Solder Pad (Variants 03, 04, 05, 06 only)	$R_{th(j-sp)}$	110	$^{\circ}C/W$	
Operating Temperature Range For TO-72 For CCP	$T_{op}$	-55 to +150 -65 to +200	$^{\circ}C$	
Storage Temperature Range	$T_{stg}$	-65 to +200	$^{\circ}C$	
Soldering Temperature For TO-72 For CCP	$T_{sol}$	+235 +245	$^{\circ}C$	Note 1 Note 2

**NOTES:**

- Duration 10 seconds maximum at a distance of not less than 1.5mm from the device body and the same lead shall not be resoldered until 3 minutes have elapsed.
- Duration 5 seconds maximum and the same terminal shall not be resoldered until 3 minutes have elapsed.

1.6 PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION

1.6.1 Metal Can Package (TO-72) - 4 lead

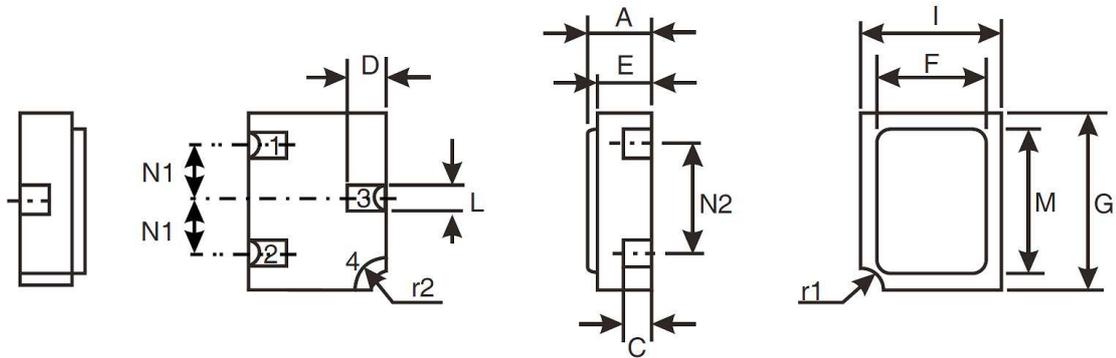


Symbols	Dimensions mm		Notes
	Min	Max	
A	4.32	5.33	
$\varnothing b$	0.406	0.533	2, 3
$\varnothing b2$	0.406	0.483	2, 3
$\varnothing D$	5.31	5.84	
$\varnothing D1$	4.52	4.95	
e	2.54 BSC		5
e1	1.27 BSC		5
F	-	0.762	
j	0.914	1.17	
k	0.711	1.22	4
L	12.7	-	2, 3
L1	-	1.27	2, 3
L2	6.35	-	2, 3
a	45° BSC		5, 6

**NOTES:**

- Terminal identification is specified by reference to the tab position where lead 1 = source, lead 2 = drain, lead 3 = gate and lead 4 = connected to the case.
- Applies to all leads.
- $\varnothing b2$  applies between  $L1$  and  $L2$ .  $\varnothing b$  applies between  $L2$  and  $12.7\text{mm}$  from the seating plane. Diameter is uncontrolled within  $L1$  and beyond  $12.7\text{mm}$  from the seating plane.
- Measured from the maximum diameter of the actual device. Leads having maximum diameter  $0.483\text{mm}$  measured in the gauging plane  $1.37 (+0.025, -0)$  mm below the seating plane of the device shall be within  $0.178\text{mm}$  of their true position relative to a maximum-width-tab.
- Measured from the tab centreline.

1.6.2 Chip Carrier Package (CCP) - 4 terminal



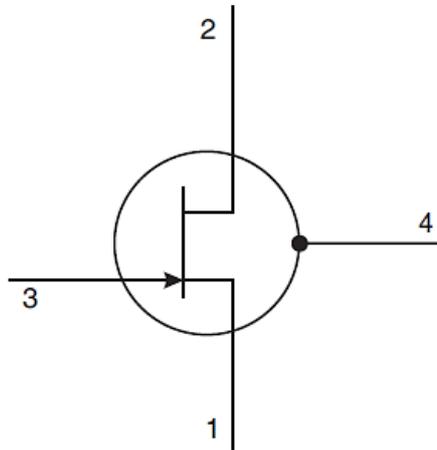
Symbols	Dimensions mm		Notes
	Min	Max	
A	1.15	1.5	
C	0.45	0.56	2
D	0.6	0.91	2
E	0.91	1.12	
F	1.9	2.15	
G	2.9	3.25	
I	2.4	2.85	
L	0.4	0.6	2
M	2.4	2.65	
N1	0.855	1.005	
N2	1.8	2	
r1	0.3 TYPICAL		1
r2	0.56 TYPICAL		1

**NOTES:**

- Terminal identification is specified by reference to the tab position where terminal 1 = source or drain depending on the Variant, terminal 2 = drain or source depending on the Variant, terminal 3 = gate and terminal 4 = shielding connected to the lid.
- Applies to terminals 1, 2, 3.

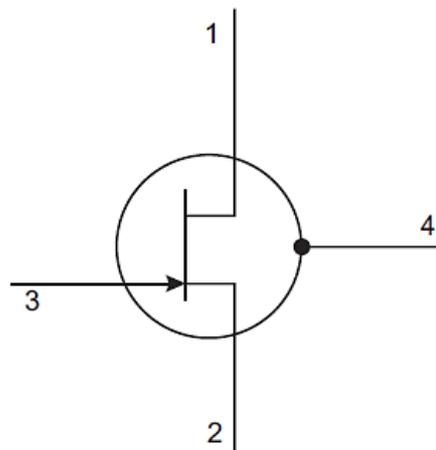
1.7 FUNCTIONAL DIAGRAM

Variants 01, 02, 03 and 05



- 1. Source.
- 2. Drain.
- 3. Gate.
- 4. Connected to case (Variants 01 and 02);  
Connected to lid (Variants 03 and 05)

Variants 04 and 06



- 1. Drain.
- 2. Source.
- 3. Gate.
- 4. Connected to lid.

1.8 MATERIALS AND FINISHES

Materials and finishes shall be as follows:

- (a) Case
  - For the metal can package the case shall be hermetically sealed and have a metal body with hard glass seals.
  - For the chip carrier package the case shall be hermetically sealed and have a ceramic body with a Kovar lid.
- (b) Leads/Terminals
  - As specified in Component Type Variants.

**2 REQUIREMENTS**

**2.1 GENERAL**

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

**2.1.1 Deviations from the Generic Specification**

**2.1.1.1 *Deviation from Screening Tests - Chart F3***

Power Burn-in and the subsequent Parameter Drift Values (Final Measurements) shall be omitted.

**2.2 WAFER LOT ACCEPTANCE**

SEM Inspection shall be performed.

**2.3 MARKING**

The marking shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and as follows.

The information to be marked on the component shall be:

- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- (b) The ESCC Component Number.
- (c) Traceability information.

**2.4 TERMINAL STRENGTH**

The test conditions for terminal strength, tested as specified in the ESCC Generic Specification, shall be as follows:

For TO-72, Test Condition: A, tension, with an applied force of 5N for duration 10s.

**2.5 ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES**

Electrical measurements shall be performed at room, high and low temperatures.

**2.5.1 Room Temperature Electrical Measurements**

The measurements shall be performed at  $T_{amb} = +22 \pm 3^{\circ}C$ .

Characteristics	Symbols	MIL-STD-750 Test Method	Test Conditions	Limits		Units
				Min	Max	
Gate Reverse Leakage Current	$I_{GSS}$	3411	$V_{DS} = 0V, V_{GS} = -20V$ Bias condition C	-	-100	pA

Characteristics	Symbols	MIL-STD-750 Test Method	Test Conditions	Limits		Units
				Min	Max	
Gate-Source Breakdown Voltage	$V_{(BR)GSS}$	3401	$V_{DS} = 0V, I_G = 1\mu A$ Bias condition C Variant 01 All other Variants	-30 -35	- -	V
Gate-Source Forward Voltage	$V_{GSF}$	3403	$V_{DS} = 0V, I_G = 1mA$	-	1	V
Gate-Source Cut-off Voltage	$V_{GS(off)}$	3403	$V_{DS} = 15V, I_D = 1nA$ Variant 01 All other Variants	- -2.5	-6 -6	V
Drain Current	$I_{DSS}$	3413	$V_{DS} = 15V$ Bias condition C Note 1	5	15	mA
Gate-Source Voltage	$V_{GS}$	3403	$V_{DS} = 15V, I_D = 500nA$	-1	-5.5	V
Small-Signal Common-Source Short-Circuit Input Capacitance	$C_{iss}$	3431	$V_{DS} = 15V, V_{GS} = 0V$ $f = 1MHz$ Note 2	-	4	pF
Small-Signal Common-Source Short-Circuit Reverse Transfer Capacitance	$C_{rss}$	3433	$V_{DS} = 15V, V_{GS} = 0V$ $f = 1MHz$ Note 2	-	1.2	pF
Common-Source Forward Transconductance	$g_{fs}$	-	$V_{DS} = 15V, V_{GS} = 0V$ $f = 1kHz$ Notes 1, 2	4.5	7.5	mS
Common-Source Output Transconductance	$g_{os}$	-	$V_{DS} = 15V, V_{GS} = 0V$ $f = 1kHz$ Notes 1, 2	-	50	$\mu S$
Small-Signal Common-Source Insertion Power Gain (Neutralised)	$G_P$	-	$V_{DS} = 15V, I_D = 5mA$ $R_G = 1k\Omega$ $f = 100MHz$ Note 3	18	-	dB
Noise Factor	NF	-	$V_{DS} = 15V, I_D = 5mA$ $R_G = 1k\Omega$ $f = 100MHz$ Note 3	-	2	dB

**NOTES:**

1. Pulsed measurement: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
2. For AC characteristics read and record measurements shall be performed on a sample of 32 components with 0 failures allowed. Alternatively a 100% inspection may be performed.
3. Small-signal common-source insertion power gain (neutralised) and noise factor shall be guaranteed but not tested.

2.5.2 High and Low Temperatures Electrical Measurements

Characteristics	Symbols	MIL-STD-750 Test Method	Test Conditions Note 1	Limits		Units
				Min	Max	
Gate Reverse Leakage Current	$I_{GSS}$	3411	$T_{amb} = +150 (+0 -5)^{\circ}C$ $V_{DS} = 0V, V_{GS} = -20V$ Bias condition C	-	-100	nA

**NOTES:**

1. Read and record measurements shall be performed on a sample of 5 components with 0 failures allowed. Alternatively a 100% inspection may be performed.

2.6 PARAMETER DRIFT VALUES

Unless otherwise specified, the measurements shall be performed at  $T_{amb} = +22 \pm 3^{\circ}C$ .

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The drift values ( $\Delta$ ) shall not be exceeded for each characteristic specified. The corresponding absolute limit values for each characteristic shall not be exceeded.

Characteristics	Symbols	Limits			Units
		Drift Value $\Delta$	Absolute		
			Min	Max	
Gate Reverse Leakage Current	$I_{GSS}$	$\pm 50$ or (1) $\pm 100\%$	-	-100	pA
Gate-Source Cut-off Voltage Variant 01 All other Variants	$V_{GS(off)}$	$\pm 10\%$	- -2.5	-6 -6	V
Drain Current	$I_{DSS}$	$\pm 15\%$	5	15	mA

**NOTES:**

1. Whichever is the greater referred to the initial value.

2.7 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

Unless otherwise specified, the measurements shall be performed at  $T_{amb} = +22 \pm 3^{\circ}C$ .

The test methods and test conditions shall be as per the corresponding test defined in Room Temperature Electrical Measurements.

The limit values for each characteristic shall not be exceeded.

Characteristics	Symbols	Limits		Units
		Min	Max	
Gate Reverse Leakage Current	$I_{GSS}$	-	-100	pA
Gate-Source Cut-off Voltage Variant 01 All other Variants	$V_{GS(off)}$	- -2.5	-6 -6	V
Drain Current	$I_{DSS}$	5	15	mA

2.8 HIGH TEMPERATURE REVERSE BIAS BURN-IN CONDITIONS

MIL-STD-750, Test Method 1039, Condition A

Characteristics	Symbols	Test Conditions	Units
Ambient Temperature	$T_{amb}$	+150 (+0 -5)	$^{\circ}C$
Drain-Source Voltage	$V_{DS}$	0	V
Gate-Source Voltage	$V_{GS}$	-21	V
Duration	t	168 Minimum	Hours

2.9 OPERATING LIFE CONDITIONS

The conditions shall be as specified for High Temperature Reverse Bias Burn-in except the duration shall be as specified in the ESCC Generic Specification.