



**TRANSISTORS, POWER, MOSFET, N-CHANNEL,
RAD-HARD**

BASED ON TYPE STRH100N10

ESCC Detail Specification No. 5205/021

| | |
|---------|---------------|
| Issue 9 | December 2020 |
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| DCR No. | CHANGE DESCRIPTION |
|----------------------|---|
| 1394 | Specification updated to incorporate changes per DCR. |

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1 GENERAL

1.1 SCOPE

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. [5000](#)
- (b) [MIL-STD-750](#), Test Methods and Procedures for Semiconductor Devices

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. [21300](#) shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 The ESCC Component Number

The ESCC Component Number shall be constituted as follows:

Example: 520502101F

- Detail Specification Reference: 5205021
- Component Type Variant Number: 01 (as required)
- Total Dose Radiation Level Letter: F (as required)

1.4.2 Component Type Variants

The component type variants applicable to this specification are as follows:

| Variant Number | Based on Type | Case | Lead Material and Finish | Weight max g | Total Dose Radiation Level Letter |
|----------------|---------------|----------|--------------------------|--------------|-----------------------------------|
| 01 | STRH100N10 | TO-254AA | S14 | 10 | F [50krad(Si)] |
| 02 | STRH100N10 | TO-254AA | S4 | 10 | F [50krad(Si)] |

The lead material and finish shall be in accordance with the requirements of ESCC Basic Specification No. [23500](#).

Total dose radiation level letters are defined in ESCC Basic Specification No. [22900](#). If an alternative radiation test level is specified in the Purchase Order the letter shall be changed accordingly.

1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

| Characteristics | Symbols | Maximum Ratings | Unit | Remarks |
|---|----------------------|-----------------|------|--|
| Drain-Source Voltage | V _{DS} | 100 | V | Over T _{op} , V _{GS} = 0V Note 2 |
| Gate-Source Voltage | V _{GS} | ±20 | V | Over T _{op} |
| Drain Current | I _{DS} | 48 | A | Continuous, At T _{case} ≤ +25°C Note 1 |
| | | 30 | A | Continuous, At T _{case} > +100°C Note 1 |
| Drain Current (Pulsed) | I _{DM} | 192 | A | Note 2 |
| Power Dissipation | P _{tot} | 170 | W | At T _{case} ≤ +25°C Note 1 |
| Avalanche Energy (Single Pulse) | E _{AS} | 954 280 | mJ | V _{DS} = 50V, I _A = 24A T _j = +25 ±3°C T _j = +110 (+0 -5)°C |
| Avalanche Energy (Repetitive Pulse) | E _{AR} | 24 7.7 | mJ | V _{DS} = 50V, I _A = 24A, f = 100kHz, Duty Cycle = 10% T _j = +25 ±3°C T _j = +110 (+0 -5)°C |
| Operating Temperature Range | T _{op} | -55 to +150 | °C | Note 3 |
| Junction Temperature | T _j | +150 | °C | |
| Storage Temperature Range | T _{stg} | -55 to +150 | °C | Note 3 |
| Soldering Temperature | T _{sol} | +260 | °C | Note 4 |
| Thermal Resistance, Junction-to-Heat Sink | R _{th(j-s)} | 0.73 | °C/W | Note 5 |
| Thermal Resistance, Junction-to-Ambient | R _{th(j-a)} | 48 | °C/W | Note 2 |

NOTES:

- I_{DS} and P_{tot} ratings are in accordance with R_{th(j-s)}. The maximum theoretical I_D limit at T_{case} > +25°C can be obtained by using the following formula (I_D is limited by the package and device construction):

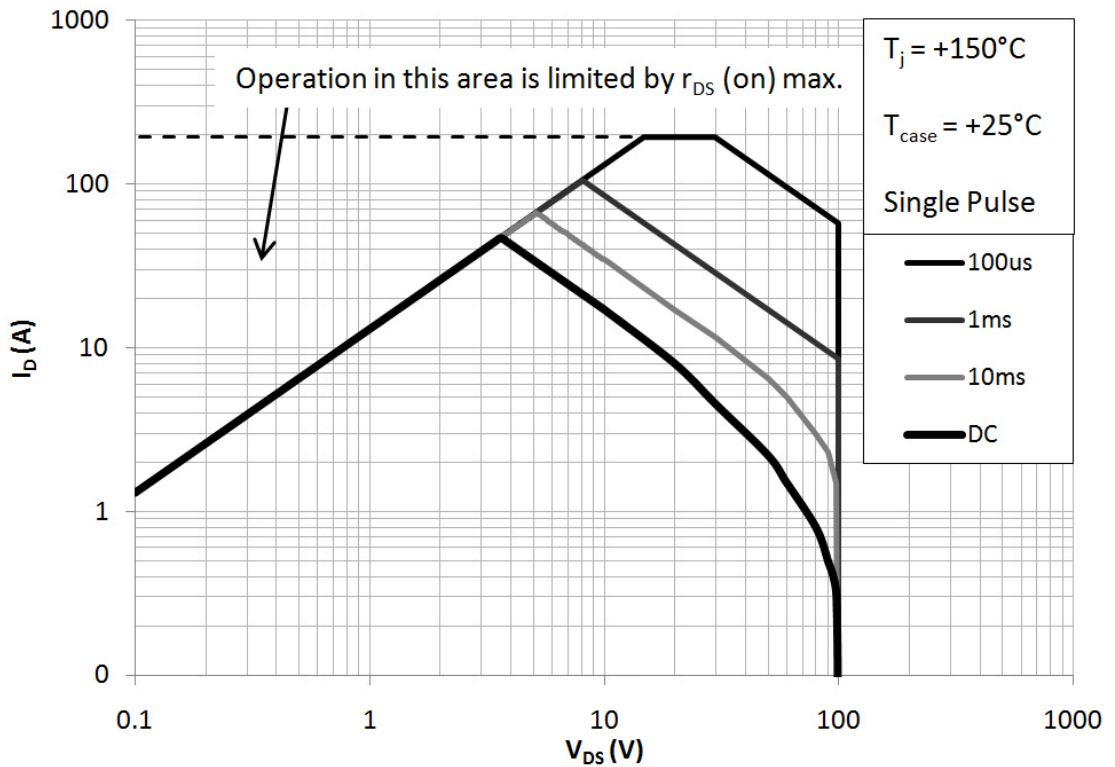
$$I_D = \sqrt{\frac{T_j(\text{max}) - T_{\text{case}}}{(R_{\text{th}(j-s)}) \times (r_{\text{DS(on)}} \text{ at } T_j(\text{max}))}}$$

Where (r_{DS(on)} at T_j(max)) = 75mΩ.

For T_{case} > +25°C, the power dissipation derates linearly to 0W at T_{case} = +150°C.

2. Safe Operating Area applies as follows:

Maximum Safe Operating Area



3. For Variants with hot solder dip lead finish, all testing and any handling performed at $T_{amb} > +125^\circ\text{C}$ shall be carried out in a 100% inert atmosphere.
4. Duration 10 seconds maximum at a distance of not less than 1.5mm from the device body and the same lead shall not be resoldered until 3 minutes have elapsed.
5. Package is mounted on an infinite heatsink.

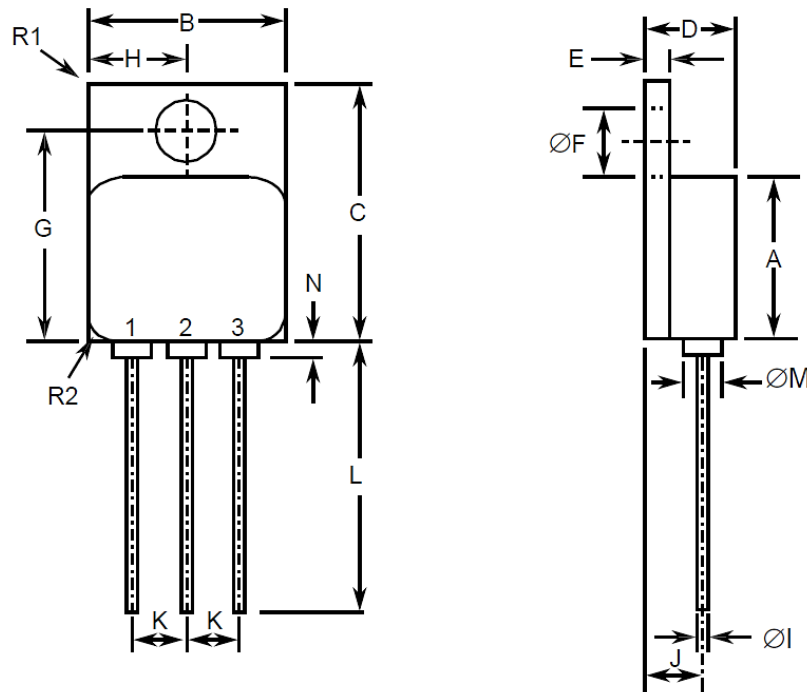
1.6 HANDLING PRECAUTIONS

These components are susceptible to damage by electrostatic discharge. Therefore, suitable precautions shall be employed for protection during all phases of manufacture, testing, shipment and any handling.

These components are categorised as Class 2 per ESCC Basic Specification No. [23800](#) with a Minimum Critical Path Failure Voltage of 3400 Volts.

The TO-254AA package contains Beryllium Oxide (BeO) and therefore it must not be ground, machined, sandblasted or subjected to any mechanical operation which will produce dust. The case must not be subjected to any chemical process (e.g. etching) which will produce fumes.

1.7 PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION
Metal Flange Mount Package (TO-254AA) – 3 lead



| Symbols | Dimensions mm | | Notes |
|-----------------|---------------|-------|-------|
| | Min | Max | |
| A | 13.59 | 13.84 | |
| B | 13.59 | 13.84 | |
| C | 20.07 | 20.32 | |
| D | 6.3 | 6.7 | |
| E | 1 | 1.35 | |
| $\varnothing F$ | 3.5 | 3.9 | |
| G | 16.89 | 17.4 | |
| H | 6.86 BSC | | |
| $\varnothing I$ | 0.89 | 1.14 | 2 |
| J | 3.81 BSC | | |
| K | 3.81 BSC | | |
| L | 12.95 | 14.5 | |
| $\varnothing M$ | 3.05 Typical | | 2 |
| N | - | 0.71 | 2 |
| R1 | - | 1 | 3 |
| R2 | 1.65 Typical | | 4 |

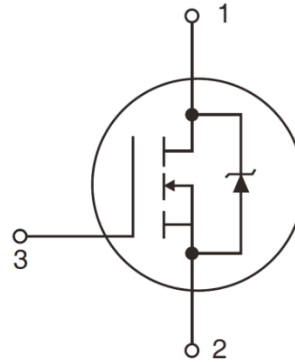
NOTES:

1. The terminal identification is specified by the components geometry. See Para. 1.8 Functional Diagram for the terminal connections.

2. 3 places.
3. Radius of heatsink flange corner, 4 places.
4. Radius of body corner, 4 places.

1.8 FUNCTIONAL DIAGRAM

Terminal 1: Drain
Terminal 2: Source
Terminal 3: Gate



NOTES:

1. The case is not connected to any lead.

1.9 MATERIALS AND FINISHES

Materials and finishes shall be as follows:

- (a) Case
The case shall be hermetically sealed and have a metal body. The leads pass through ceramic eyelets brazed into the frame and the lid shall be welded.
- (b) Leads
As specified in Para. 1.4.2.

2 REQUIREMENTS

2.1 GENERAL

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

2.1.1 Deviations from the Generic Specification

2.1.1.1 *Deviations from Screening Tests - Chart F3*

- (a) Verification of Safe Operating Area
The Safe Operating Area shall be verified by performing the ΔV_{SD} test specified in Para. 2.6.1 Room Temperature Electrical Measurements (Thermal Resistance, Junction-to-Heat Sink).
- (b) A High Temperature Forward Bias test shall be performed instead of Power Burn-in.

2.2 WAFER LOT ACCEPTANCE

A SEM inspection shall be performed as specified in the ESCC Generic Specification.

2.3 MARKING

The marking shall be in accordance with the requirements of ESCC Basic Specification No. 21700 and as follows.

The information to be marked on the component shall be:

- (a) The ESCC Qualified Component symbol (for ESCC qualified components only).
- (b) The ESCC Component Number (see Para. 1.4.1).
- (c) Traceability information.
- (d) Warning sign for Beryllium Oxide.

2.4 CASE ISOLATION

Case Isolation shall be performed as specified in the ESCC Generic Specification and as follows:

- Test Conditions:
 - Test voltage: 500Vdc
 - Duration of application of test voltage: 1s
 - Points of application of test voltage: between case metal tab and all terminals connected together.
 - Maximum leakage current: 10nA

2.5 TERMINAL STRENGTH

The test conditions for terminal strength, tested as specified in the ESCC Generic Specification, shall be as follows:

Test Condition: A, tension, with an applied force of 10N for a duration of 10s.

2.6 ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES

Electrical measurements shall be performed at room, high and low temperatures. Consolidated notes are given after the tables in Para. 2.6.3.

2.6.1 Room Temperature Electrical Measurements

Unless otherwise specified, the measurements shall be performed at $T_{amb} = +22 \pm 3^{\circ}C$.

| Characteristics | Symbols | MIL-STD-750 Test Method | Test Conditions | Limits | | Units |
|-----------------------------------|---------------|----------------------------|--|--------|-----|---------|
| | | | | Min | Max | |
| Drain-to-Source Breakdown Voltage | $V_{(BR)DSS}$ | 3407 | $V_{GS} = 0V, I_D = 1mA$ Bias condition C | 100 | - | V |
| Gate-to-Source Leakage Current 1 | I_{GSS1} | 3411 | $V_{GS} = 20V, V_{DS} = 0V$ Bias condition C | - | 100 | nA |
| Gate-to-Source Leakage Current 2 | I_{GSS2} | 3411 | $V_{GS} = -20V, V_{DS} = 0V$ Bias condition C | -100 | - | nA |
| Drain Current | I_{DSS} | 3413 | $V_{DS} = 80V, V_{GS} = 0V$ Bias condition C | - | 10 | μA |
| Gate-to-Source Threshold Voltage | $V_{GS(th)}$ | 3403 | $V_{DS} \geq V_{GS}$ $I_D = 1mA$ | 2 | 4.5 | V |

| Characteristics | Symbols | MIL-STD-750 Test Method | Test Conditions | Limits | | Units |
|---|---------------|----------------------------|--|--------|------|---------------|
| | | | | Min | Max | |
| Static Drain-to-Source On Resistance | $r_{DS(on)}$ | 3421 | $V_{GS} = 12V, I_D = 24A$ Note 1 | - | 35 | m Ω |
| Source-to-Drain Diode Forward Voltage | V_{SD} | 4011 | $V_{GS} = 0V, I_{SD} = 48A$ Note 1 | - | 1.5 | V |
| Thermal Resistance, Junction-to-Heat Sink | $R_{th(j-s)}$ | 3161 | Note 2 | - | 0.73 | $^{\circ}C/W$ |
| Input Capacitance | C_{iss} | 3431 | $V_{GS} = 0V, V_{DS} = 25V$ $f = 1MHz$ | 3940 | 5910 | pF |
| Output Capacitance | C_{oss} | 3453 | | 543 | 814 | pF |
| Reverse Transfer Capacitance | C_{rss} | 3433 | | 190 | 284 | pF |
| Total Gate Charge | Q_g | 3471 | $V_{GS} = 12V, V_{DS} = 50V$ $I_D = 48A$ | 108 | 162 | nC |
| Gate-to-Source Charge | Q_{gs} | | | 21.6 | 32.4 | nC |
| Gate-to-Drain Charge | Q_{gd} | | | 36 | 54 | nC |
| Turn-on Delay Time | $t_{d(on)}$ | 3472 | $V_{GS} = 12V, V_{DS} = 50V$ $I_D = 24A, R_G = 4.7\Omega$ | 23.6 | 35.4 | ns |
| Rise Time | t_r | | | 34.4 | 51.6 | ns |
| Turn-off Delay Time | $t_{d(off)}$ | | | 79 | 119 | ns |
| Fall Time | t_f | | | 33.6 | 50.4 | ns |
| Reverse Recovery Time | t_{rr} | 3473 | $V_{DS} = 50V, I_{SD} = 48A$ $di/dt = 100A/\mu s$ $T_j = +25 \pm 3^{\circ}C$ | 332 | 498 | ns |

2.6.2 High and Low Temperatures Electrical Measurements

| Characteristics | Symbols | MIL-STD-750 Test Method | Test Conditions Note 3 | Limits | | Units |
|--------------------------------------|--------------|----------------------------|---|--------|-----|------------|
| | | | | Min | Max | |
| Gate-to-Source Leakage Current 1 | I_{GSS1} | 3411 | $V_{GS} = 20V, V_{DS} = 0V$ Bias condition C $T_{case} = +125(+0-5)^{\circ}C$ | - | 200 | nA |
| Gate-to-Source Leakage Current 2 | I_{GSS2} | 3411 | $V_{GS} = -20V,$ $V_{DS} = 0V$ Bias condition C $T_{case} = +125(+0-5)^{\circ}C$ | -200 | - | nA |
| Drain Current | I_{DSS} | 3413 | $V_{DS} = 80V,$ $V_{GS} = 0V$ Bias condition C $T_{case} = +125(+0-5)^{\circ}C$ | - | 100 | μA |
| Gate-to-Source Threshold Voltage | $V_{GS(th)}$ | 3403 | $V_{DS} \geq V_{GS}$ $I_D = 1mA$ $T_{case} = +125(+0-5)^{\circ}C$ | 1.5 | 3.7 | V |
| | | | $V_{DS} \geq V_{GS}$ $I_D = 1mA$ $T_{case} = -55(+5-0)^{\circ}C$ | 2.1 | 5.5 | V |
| Static Drain-to-Source On Resistance | $r_{DS(on)}$ | 3421 | $V_{GS} = 12V, I_D = 24A$ $T_{case} = +125(+0-5)^{\circ}C$ Note 1 | - | 63 | m Ω |

| Characteristics | Symbols | MIL-STD-750 Test Method | Test Conditions Note 3 | Limits | | Units |
|---------------------------------------|-----------------|----------------------------|---|--------|-------|-------|
| | | | | Min | Max | |
| Source-to-Drain Diode Forward Voltage | V _{SD} | 4011 | V _{GS} = 0V, I _{SD} = 48A T _{case} = +125(+0-5)°C Note 1 | - | 1.275 | V |

2.6.3 Notes to Room, High and Low Temperature Electrical Measurements

- Pulsed measurement: Pulse Width ≤ 680μs, Duty Cycle ≤ 2%.
- The R_{th(j-s)} limit is guaranteed by performing a ΔV_{SD} (go-no-go) test. The following test conditions and limits shall apply:
 - V_{DS} = 7V
 - I_D = 22.91A
 - I_{cal} = 29mA
 - t_{pulse} = 20ms
 - t_{cal} = 100μs
 - V_{SD} = 60mV minimum, 120mV maximum
- Read and record measurements shall be performed on a sample of 5 components with 0 failures allowed. Alternatively a 100% inspection may be performed.

2.7 PARAMETER DRIFT VALUES

Unless otherwise specified, the measurements shall be performed at T_{amb} = +22 ±3 °C.

The test methods and test conditions shall be as per the corresponding test defined in Para. 2.6.1 Room Temperature Electrical Measurements.

The drift values (Δ) shall not be exceeded for each characteristic specified. The corresponding absolute limit values for each characteristic shall not be exceeded.

| Characteristics | Symbols | Limits | | | Units |
|--------------------------------------|---------------------|------------------------|----------|-----|-------|
| | | Drift Value Δ | Absolute | | |
| | | | Min | Max | |
| Gate-to-Source Leakage Current 1 | I _{GSS1} | ±50 or (1) ±100% | - | 100 | nA |
| Gate-to-Source Leakage Current 2 | I _{GSS2} | ±50 or (1) ±100% | -100 | - | nA |
| Drain Current | I _{DSS} | ±4 or (1) ±100% | - | 10 | μA |
| Gate-to-Source Threshold Voltage | V _{GS(th)} | ±5% | 2 | 4.5 | V |
| Static Drain-to-Source On Resistance | r _{DS(on)} | ±10% | - | 5 | mΩ |

NOTES:

- Whichever is the greater referred to the initial value.

2.8 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

Unless otherwise specified, the measurements shall be performed at $T_{amb} = +22 \pm 3^{\circ}C$.

The test methods and test conditions shall be as per the corresponding test defined in Para. 2.6.1 Room Temperature Electrical Measurements.

The limit values for each characteristic shall not be exceeded.

| Characteristics | Symbols | Limits | | Units |
|--------------------------------------|--------------|--------|-----|-----------|
| | | Min | Max | |
| Drain Current | I_{DSS} | - | 10 | μA |
| Gate-to-Source Threshold Voltage | $V_{GS(th)}$ | 2 | 4.5 | V |
| Static Drain-to-Source On Resistance | $r_{DS(on)}$ | - | 35 | $m\Omega$ |

2.9 HIGH TEMPERATURE REVERSE BIAS BURN-IN CONDITIONS

HTRB Burn-in shall be performed in accordance with [MIL-STD-750, Test Method 1042](#), Test Condition A with the following conditions:

| Characteristics | Symbols | Test Conditions | Units |
|-------------------------|-----------|-----------------|-------------|
| Ambient Temperature | T_{amb} | +150 (+0 -5) | $^{\circ}C$ |
| Drain-to-Source Voltage | V_{DS} | 80 | V |
| Gate-to-Source Voltage | V_{GS} | 0 | V |
| Duration | t | 240 minimum | Hours |

2.10 HIGH TEMPERATURE FORWARD BIAS BURN-IN CONDITIONS

HTFB Burn-in shall be performed in accordance with [MIL-STD-750, Test Method 1042](#), Test Condition B with the following conditions:

| Characteristics | Symbols | Test Conditions | Units |
|-------------------------|-----------|-----------------|-------------|
| Ambient Temperature | T_{amb} | +150 (+0 -5) | $^{\circ}C$ |
| Drain-to-Source Voltage | V_{DS} | 0 | V |
| Gate-to-Source Voltage | V_{GS} | 16 | V |
| Duration | t | 48 minimum | Hours |

2.11 OPERATING LIFE CONDITIONS

Operating Life shall consist of High Temperature Reverse Bias in accordance with [MIL-STD-750, Test Method 1042](#), Test Condition A, followed by High Temperature Forward Bias in accordance with [MIL-STD-750, Test Method 1042](#), Test Condition B. The test conditions are as follows:

High Temperature Reverse Bias Conditions

| Characteristics | Symbols | Test Conditions | Units |
|-------------------------|------------------|-----------------|-------|
| Ambient Temperature | T _{amb} | +150 (+0 -5) | °C |
| Drain-to-Source Voltage | V _{DS} | 80 | V |
| Gate-to-Source Voltage | V _{GS} | 0 | V |
| Duration | t | 1000 minimum | Hours |

High Temperature Forward Bias Conditions

| Characteristics | Symbols | Test Conditions | Units |
|-------------------------|------------------|-----------------|-------|
| Ambient Temperature | T _{amb} | +150 (+0 -5) | °C |
| Drain-to-Source Voltage | V _{DS} | 0 | V |
| Gate-to-Source Voltage | V _{GS} | 16 | V |
| Duration | t | 1000 minimum | Hours |

2.12 TOTAL DOSE RADIATION TESTING

All lots shall be irradiated in accordance with ESCC Basic Specification No. [22900](#), standard dose rate (window 1: 3.6krad to 36krad per hour).

2.12.1 Bias Conditions and Total Dose Level for Total Dose Radiation Testing

The following bias condition (worst-case) shall be used for Total Dose Radiation Testing at T_{amb} = +22 ±3°C:

With V_{GS} bias = +15V and V_{DS} = 0V during irradiation.

The total dose level applied shall be as specified in Para. 1.4.2 or in the Purchase Order.

2.12.2 Electrical Measurements for Total Dose Radiation Testing

Prior to irradiation testing the devices shall have successfully met Room Temperature Electrical Measurements specified herein (Para. 2.6.1).

Unless otherwise stated the measurements shall be performed at $T_{amb} = +22 \pm 3^{\circ}C$.

Unless otherwise specified the test methods and test conditions shall be as per the corresponding test defined in Para. 2.6.1 Room Temperature Electrical Measurements.

The parameters to be measured during irradiation testing, on completion of irradiation testing, after 24 hours anneal at Room Temperature and after 168 hours anneal at $+100 \pm 3^{\circ}C$ are shown below.

| Characteristics | Symbols | Limits | | | Units |
|---------------------------------------|--------------|---------------------------|----------|-----|-----------|
| | | Drift Values (Δ) | Absolute | | |
| | | | Min | Max | |
| Drain-to-Source Voltage Note 1 | V_{DSS} | -25% Note 2 | N/A | | V |
| Gate-to-Source Leakage Current 1 | I_{GSS1} | +20 | - | 100 | nA |
| Gate-to-Source Leakage Current 2 | I_{GSS2} | -20 | -100 | - | nA |
| Drain Current | I_{DSS} | - | - | 10 | μA |
| Gate-to-Source Threshold Voltage | $V_{GS(th)}$ | -50% / +10% | 2 | 4.5 | V |
| Static Drain-to-Source On Resistance | $r_{DS(on)}$ | $\pm 10\%$ | - | 35 | $m\Omega$ |
| Source-to-Drain Diode Forward Voltage | V_{SD} | $\pm 10\%$ | - | 1.5 | V |

NOTES:

1. Drain-to-Source Voltage measurements shall be made in accordance with [MIL-STD-750, Test Method 3405](#), with $V_{GS} = 0V$ and $I_D = 1mA$.
2. Referred to an initial Drain-to-Source Voltage measurement made prior to the commencement of Total Dose Radiation Testing.

APPENDIX A
AGREED DEVIATIONS FOR STMICROELECTRONICS (F)

| ITEMS AFFECTED | DESCRIPTION OF DEVIATIONS |
|---|---|
| Para. 2.1.1, Deviations from the Generic Specification: Para. 8, Test Methods and Procedures | For qualification and qualification maintenance, or procurement of qualified or unqualified components, the following replacement test method specifications shall be used instead of the following ESCC Basic Specifications: <ul style="list-style-type: none"> • No. 20500, External Visual Inspection: replaced by MIL-STD-750 Test Method 2071. • No. 20900, Radiographic Inspection of Electronic Components: replaced by MIL-STD-750 Test Method 2076. • No. 21400, Scanning Electron Microscope Inspection of Semiconductor Dice: replaced by MIL-STD-750 Test Method 2077. |
| Para. 2.1.1.1, Deviations from the Generic Specification: Deviations from Screening Tests - Chart F3 | Solderability is not applicable unless specifically stipulated in the Purchase Order. |
| Para. 2.6.1, Room Temperature Electrical Measurements | The AC characteristics C_{iss} , C_{oss} , C_{rss} , Q_g , Q_{gs} , Q_{gd} , $t_{d(on)}$, t_r , $t_{d(off)}$, t_f and t_{rr} may be considered guaranteed but not tested if successful pilot lot testing has been performed on the wafer lot in accordance with STMicroelectronics "Acceptation wafers" internal procedure as specified in the PID which includes AC (C_{iss} , C_{oss} , C_{rss} , Q_g , Q_{gs} , Q_{gd} , $t_{d(on)}$, t_r , $t_{d(off)}$, t_f and t_{rr}) characteristic measurements per the Detail Specification. A summary of the pilot lot testing shall be provided if required by the Purchase Order. |

ADDITIONAL DATA - STMICROELECTRONICS (F)

- (a) Derating for Space Application
 These components are susceptible to Single Event Gate Rupture if operated in a space environment unless the following derating is applied:

$$V_{DS} \leq 100V \text{ when } V_{GS} = 0V$$

$$V_{DS} \leq 80V \text{ when } V_{GS} = -2V$$

$$V_{DS} \leq 60V \text{ when } V_{GS} = -5V$$

$$V_{DS} \leq 30V \text{ when } V_{GS} = -10V$$

$$V_{DS} \leq 10V \text{ when } V_{GS} = -20V$$

The derating for space applications was originally obtained on STRH100N10 devices under the following test conditions. The testing was performed in a vacuum at UCL (Louvain-la-Neuve, Belgium):

$$\text{Ion used} = Kr$$

$$\text{LET} = 32\text{MeV}/(\text{mg}/\text{cm}^2)$$

$$\text{Energy} = 768\text{MeV}$$

$$\text{Range} = 94\mu\text{m}$$

Single Event Effect Safe Operating Area