

Page 1 of 17

# RESISTOR ARRAY, FIXED, SURFACE MOUNT, THIN FILM

# **BASED ON TYPE PRA HR**

# ESCC Detail Specification No. 4001/025

Issue 9	June 2025



Document Custodian: European Space Agency - see https://escies.org



PAGE 2

#### LEGAL DISCLAIMER AND COPYRIGHT

European Space Agency, Copyright © 2025. All rights reserved.

The European Space Agency disclaims any liability or responsibility, to any person or entity, with respect to any loss or damage caused, or alleged to be caused, directly or indirectly by the use and application of this ESCC publication.

This publication, without the prior permission of the European Space Agency and provided that it is not used for a commercial purpose, may be:

- copied in whole, in any medium, without alteration or modification.
- copied in part, in any medium, provided that the ESCC document identification, comprising the ESCC symbol, document number and document issue, is removed.



PAGE 3

No. 4001/025

**ISSUE 9** 

# **DOCUMENTATION CHANGE NOTICE**

(Refer to https://escies.org for ESCC DCR content)

DCR No.	CHANGE DESCRIPTION
1731	Specification upissued to incorporate changes per DCR.



# TABLE OF CONTENTS

1	GENERAL	5
1.1	SCOPE	5
1.2	APPLICABLE DOCUMENTS	5
1.3	TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS	5
1.4	THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS	5
1.4.1	The ESCC Component Number	5
1.4.1.1	Characteristics and/or Ratings Codes	6
1.4.2	Component Type Variants and Range of Components	7
1.5	MAXIMUM RATINGS	9
1.6	HANDLING PRECAUTIONS	9
1.7	PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION	10
1.8	FUNCTIONAL DIAGRAM	11
1.9	MATERIALS AND FINISHES	12
1.9.1	Body	12
1.9.2	Terminations	12
2	REQUIREMENTS	12
2.1	GENERAL	12
2.1.1	Deviations from the Generic Specification	12
2.1.1.1	Deviations from Screening Tests - Chart F3	12
2.1.1.2	Deviations from Qualification and Periodic Tests - Chart F4	12
2.2	MARKING	12
2.3	OVERLOAD	13
2.4	ROBUSTNESS OF TERMINATIONS - SUBSTRATE BENDING TEST	13
2.5	RESISTANCE TO SOLDERING HEAT	13
2.6	ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES	13
2.6.1	Room Temperature Electrical Measurements	13
2.6.2	High and Low Temperatures Electrical Measurements	14
2.7	INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS	15
2.8	BURN-IN CONDITIONS	16
2.9	OPERATING LIFE CONDITIONS	16
APPENDIX	( A	17



PAGE 5

# 1 <u>GENERAL</u>

## 1.1 <u>SCOPE</u>

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

#### 1.2 <u>APPLICABLE DOCUMENTS</u>

The following documents form part of this specification and shall be read in conjunction with it:

(a) ESCC Generic Specification No. 4001.

# 1.3 <u>TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS</u>

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

## 1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

## 1.4.1 <u>The ESCC Component Number</u>

The ESCC Component Number shall be constituted as follows:

(a) Resistor arrays where all resistors have the same ohmic value (Variants 01 to 21):

Example: 4001025011001FW1

- Detail Specification Reference: 4001025
- Component Type Variant Number: 01 (as required)
- Characteristic code: Rated Resistance (1000Ω): 1001 (as required)
- Characteristic code: Absolute Resistance Tolerance (±1%): F (as required)
- Characteristic code: Relative Resistance Tolerance (±0.05%): W (as required)
- Characteristic code: Absolute Temperature Coefficient (±10 x 10-6/°C): 1
- (b) Resistor arrays where 2 or more resistors have different ohmic values (Variants 22 to 42): Example: 4001025221229FW1
  - Detail Specification Reference: 4001025
  - Component Type Variant Number: 22 (as required)
  - Characteristic code: Array Reference: 1229 (as required)
  - Characteristic code: Absolute Resistance Tolerance (±1%): F (as required)
  - Characteristic code: Relative Resistance Tolerance (±0.05%): W (as required)
  - Characteristic code: Absolute Temperature Coefficient (±10 x 10-6/°C): 1

**ESCC** Detail Specification



ISSUE 9

PAGE 6

#### 1.4.1.1 Characteristics and/or Ratings Codes

Characteristics and/or ratings to be codified as part of the ESCC Component Number shall be as follows:

(a) Rated Resistance

For resistor arrays where all resistors have the same ohmic value (type S; Variants 01 to 21), the rated resistance,  $R_n$ , shall be expressed by means of the following codes in accordance with ESCC Basic Specification No. 21700. The unit quantity shall be ohm ( $\Omega$ ):

Rated Resistance $R_n(\Omega)$	Code
XXX	XXX0
XXX 10 <sup>1</sup>	XXX1
XXX 10 <sup>2</sup>	XXX2
XXX 10 <sup>3</sup>	XXX3
XXX 10 <sup>4</sup>	XXX4

(b) Array Reference

Resistor arrays where 2 or more resistors have different ohmic values (type D; Variants 22 to 42) shall each be assigned with a unique 4-digit Array Reference by the Manufacturer to represent the specific array configuration. Each Array Reference shall be held by the Manufacturer under configuration control and shall contain the following information:

- The 4-digit Array Reference.
- The Rated Resistance, Rn, and location for each resistor in the array.
- (c) Absolute and Relative Resistance Tolerance expressed by the following codes in accordance with ESCC Basic Specification No. 21700:

Tolerance (± %)	Code Letter
0.05	W
0.1	В
0.5	D
1	F

(d) Absolute Temperature Coefficient expressed by the following code in accordance with ESCC Basic Specification No. 21700:

Temperature Coefficient (± 10 <sup>-6</sup> /°C)	Code
10	1



**ISSUE 9** 

# 1.4.2 <u>Component Type Variants and Range of Components</u>

# The component type variants and range of components applicable to this specification are as follows:

Variant Number				rance %)	Tempe Coeffic (± 10		Terminal Material and	Weight max (g)			
						Absolute	Relative	Absolute	Relative (Note 5)	Finish	
01	PRA100	S	2	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.006
02	PRA100	S	3	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.009
03	PRA100	S	4	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.012
04	PRA100	S	5	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.01
05	PRA100	S	6	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.018
06	PRA100	S	7	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
07	PRA100	S	8	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.024
08	PRA135	S	2	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.008
09	PRA135	S	3	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.012
10	PRA135	S	4	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.016
11	PRA135	S	5	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
12	PRA135	S	6	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
13	PRA135	S	7	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
14	PRA135	S	8	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.03
15	PRA182	S	2	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
16	PRA182	S	3	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.03
17	PRA182	S	4	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.04
18	PRA182	S	5	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.05
19	PRA182	S	6	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.06
20	PRA182	S	7	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.07
21	PRA182	S	8	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.08
22	PRA100	D	2	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.00
23	PRA100	D	3	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.00
24	PRA100	D	4	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.01
25	PRA100	D	5	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.01
26	PRA100	D	6	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.01
27	PRA100	D	7	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
28	PRA100	D	8	100	0.2	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
29	PRA135	D	2	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.00
30	PRA135	D	3	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.01

PAGE 7



#### **ISSUE 9**

Variant Number	Style (Note 1)	Same (S) or Different (D) Ohmic	Number of Resistors per Array	sistors Range R <sub>n</sub> (± %)			Tempe Coeffic (± 10		Terminal Material and	Weight max (g)	
		Values (Note 2)	Values (Note 2)		Max (MΩ)	Absolute	Relative	Absolute	Relative (Note 5)	Finish	
31	PRA135	D	4	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.016
32	PRA135	D	5	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
33	PRA135	D	6	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.024
34	PRA135	D	7	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.028
35	PRA135	D	8	100	0.25	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.032
36	PRA182	D	2	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.02
37	PRA182	D	3	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.03
38	PRA182	D	4	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.04
39	PRA182	D	5	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.05
40	PRA182	D	6	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.06
41	PRA182	D	7	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.07
42	PRA182	D	8	100	1	0.1, 0.5, 1	0.05, 0.1	10	5, 3	E4	0.08

# NOTES:

1. See Para. 1.7.

2. For S types (Variants 01 to 21), all individual resistors in the resistor array have the same ohmic value.

For D types (Variants 22 to 42) the resistor array has individual resistors with 2 or more different ohmic values.

3. The critical resistance for each style is as follows:

Variant Number	Style	Critical Resistance
		(kΩ)
01 to 07, 22 to 28	PRA100	12.25
08 to 14, 29 to 35	PRA135	56.25
15 to 21, 36 to 42	PRA182	100

- 4. Any value(s) in the resistance range to 3 significant figures can be selected.
- 5.  $\pm 5 \times 10^{-6}$ /°C: if one or more resistors of the array is in the range:  $100\Omega \le R_n \le 250\Omega$ .  $\pm 3 \times 10^{-6}$ /°C: if all resistors of the array are:  $R_n > 250\Omega$ .



PAGE 9

No. 4001/025

**ISSUE 9** 

## 1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Variant Number	Style	Symbols	Limits	Units	Remarks
Rated Dissipation	All	All	Pn	100	mW/resistor	Note 1
Limiting Element Voltage	01 to 07, 22 to 28: 08 to 14, 29 to 35: 15 to 21, 36 to 42:	PRA100 PRA135 PRA182	UL	35 75 100	V	Per resistor
Rated Voltage	All	All	U <sub>R</sub>	√(Pn x Rn)	V	Per resistor Note 2
Isolation Voltage	01 to 07, 22 to 28: 08 to 14, 29 to 35: 15 to 21, 36 to 42:	PRA100 PRA135 PRA182	Ui	70 150 200	Vrms	Per resistor
Operating Temperature Range	All	All	T <sub>op</sub>	-55 to +155	°C	T <sub>amb</sub>
Storage Temperature Range	All	All	Tstg	-55 to +155	°C	-
Soldering Temperature	All	All	T <sub>sol</sub>	+260	°C	Note 3

# NOTES:

- 1. At  $T_{amb} \le +70^{\circ}$ C. For  $T_{amb} > +70^{\circ}$ C, derate linearly to 0W at  $T_{amb} = +155^{\circ}$ C.
- 2. Shall never exceed Limiting Element Voltage. Rn = Rated Resistance.
- 3. Duration 10 seconds maximum.

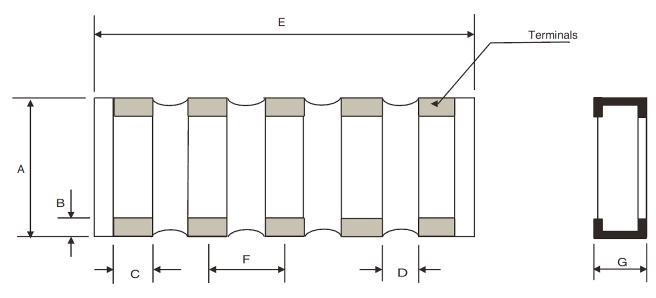
#### 1.6 HANDLING PRECAUTIONS

These devices are susceptible to damage by electrostatic discharge. Therefore, suitable precautions shall be employed for protection during all phases of manufacture, testing, packaging, shipment and any handling.

These components are categorised as Class 2 per ESCC Basic Specification No. 23800 with a Minimum Critical Path Failure Voltage of 2500 Volts.



# 1.7 PHYSICAL DIMENSIONS AND TERMINAL IDENTIFICATION



Variant Style Dimensions (mm)														
Number		А		В		С		D		G		F		E
		Min	Max											
01 to 07, 22 to 28	PRA100	1.5	1.9	0.17	0.51	0.5	0.8	0.25	0.35	0.38	0.58	0.95	1.05	Note 1
08 to 14, 29 to 35	PRA135	1.75	2.05	0.17	0.51	0.9	1.2	0.25	0.35	0.38	0.58	1.3	1.4	Note 1
15 to 21, 36 to 42	PRA182	2.9	3.2	0.17	0.51	1.15	1.65	0.25	0.55	0.38	0.58	1.77	1.87	Note 1

# NOTES:

1.  $E = (N \times F) \pm 0.3$ mm, where N = number of resistors per array (N = 2 to 8).

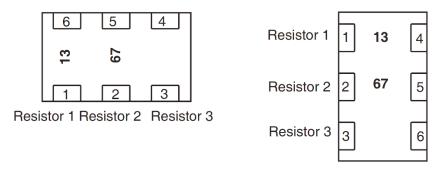


PAGE 11 ISSUE 9

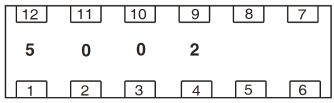
2. Terminal identification shall be by means of the location of the marking. Due to the size of the component, only the 4-digit Rated Resistance code or Array Reference code, as applicable (see Para. 1.4.1), shall be marked on the component.

For symmetrical marking using reversible figures such as 0, 6 or 9, the first resistor of the array, R1, shall be identified by a dot.

For arrays with 2 or 3 resistors, the marking shall be located adjacent to the first 2 resistors of the array. The first resistor, R1, of the array is the upper resistor whilst reading the marking, as follows:

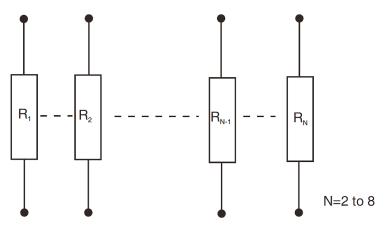


For arrays with 4 to 8 resistors, the marking shall be located adjacent to the first 4 resistors of the array. The first resistor,  $R_1$ , of the array is on the left hand end of the array whilst reading the marking, as follows:



Resistor 1 Resistor 2 Resistor 3 Resistor 4 Resistor 5 Resistor 6

#### 1.8 <u>FUNCTIONAL DIAGRAM</u>





PAGE 12

**ISSUE 9** 

## 1.9 MATERIALS AND FINISHES

## 1.9.1 <u>Body</u> Each resistive element deposited on the alumina substrate shall be covered with a suitable coating.

#### 1.9.2 <u>Terminations</u>

The terminal material and finish shall be as specified in Para. 1.4.2 in accordance with the requirements of ESCC Basic Specification No. 23500.

#### 2 <u>REQUIREMENTS</u>

#### 2.1 <u>GENERAL</u>

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

#### 2.1.1 <u>Deviations from the Generic Specification</u>

#### 2.1.1.1 Deviations from Screening Tests - Chart F3

- (a) Para. 8.2, Non-Linearity: Voltage on each resistor of the array =  $\sqrt{(P_n \times R_{min})}$  or  $2U_L$ , whichever is less ( $R_{min}$  is the lowest ohmic value of the array).
- 2.1.1.2 Deviations from Qualification and Periodic Tests Chart F4
  - (a) Para. 8.9, Vibration: shall not be performed
  - (b) Para. 8.10, Climatic Sequence: Voltage applied during the Low Air Pressure test: as per Para. 2.8 Burn-in herein.

#### 2.2 MARKING

The marking of all components delivered to this specification shall be in accordance with the requirements of ESCC Basic Specification No. 21700. When the component is too small to accommodate all of the marking specified, as much as space permits shall be marked and the marking information, in full, shall accompany each component in its primary package.

The information to be marked and the order of precedence, shall be as follows:

- (a) For Variants 01 to 21: Rated Resistance (see Para. 1.4.1.1(a))
  For Variants 22 to 42: Terminal identification and Array Reference (see Para. 1.7 Note 2 and Para. 1.4.1.1(b))
- (b) The ESCC qualified components symbol (for ESCC qualified components only).
- (c) The ESCC Component Number (see Para. 1.4.1).
- (d) Traceability information.



PAGE 13

ISSUE 9

### 2.3 <u>OVERLOAD</u>

The test conditions for Overload, tested as specified in the ESCC Generic Specification, shall be as follows:

•	Voltage on each	:	$\sqrt{(6.25 P_n \: x \: R_{min})}$ or $2 U_L,$ whichever is less (R_{min} is the
	resistor of the array		lowest ohmic value of the array).

• Duration : 2s minimum.

#### 2.4 ROBUSTNESS OF TERMINATIONS - SUBSTRATE BENDING TEST

The test conditions for the Substrate Bending Test, tested as specified in the ESCC Generic Specification, shall be as follows:

- Number of bends : 10.
- Deflection : 2mm (all Variants)
- Duration : 5±1s
- Orientation : Same orientation as for a single resistor

## 2.5 RESISTANCE TO SOLDERING HEAT

The test conditions for Resistance to Soldering Heat, tested as specified in the ESCC Generic Specification, shall be as follows:

- Temperature : 260°C
- Duration : 10 (+0 -1) s

#### 2.6 ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES

#### 2.6.1 <u>Room Temperature Electrical Measurements</u>

The measurements shall be performed at  $T_{amb}$  = +22 ±3°C.

Characteristics Symbols	ESCC No. 4001 Test Method	Tolerance (± %)	Limits		Units	
		and Conditions	(± 70)	Min	Max	
Resistance	RA	Para. 8.3.1.1 Note 1	0.1	0.999 R <sub>n</sub>	1.001 R <sub>n</sub>	Ω
		Note 1	0.5	0.995 R <sub>n</sub>	1.005 R <sub>n</sub>	
			1	0.99 R <sub>n</sub>	1.01 R <sub>n</sub>	
Relative Tolerance	$\Delta T_R$	Para. 8.3.1.1 Note 2	0.05 0.1	-0.05 -0.1	+0.05 +0.1	%
Insulation Resistance	Rı	Para. 8.3.1.2 V = 100V Note 3	All	1000	-	MΩ

# NOTES:

1. The measurements shall be performed on all the individual resistors of the array.

2. 
$$\Delta T_{R} = Max \left[ \frac{R_{A} - R_{n}}{R_{n}} \right] - Min \left[ \frac{R_{A} - R_{n}}{R_{n}} \right]$$

The calculations shall be performed between all the resistor pairs of the array.

3. Guaranteed but not tested during Chart F3 Screening Tests.



ISSUE 9

# 2.6.2 <u>High and Low Temperatures Electrical Measurements</u>

Characteristics	Symbols ESCC No. 4001 Test Method and Conditions			Limits		Unit
				Min	Max	
Resistance Change between -55 (+3 -0)°C and +22 ±3°C	$\Delta R_A/R_A$	Para. 8.3.1.1	Notes 1 and 2	-0.08	+0.08	%
Resistance Change between +155 (+0 -3)°C and +22 ±3°C	∆Ra/Ra	Para. 8.3.1.1	Notes 1 and 2	-0.136	+0.136	%
Relative Temperature Coefficient	$\Delta TC_R$	Para. 8.3.1.1	Notes 2 and 3	-3	+3	10 <sup>-6</sup> /°C
-55 (+3 -0)°C		Para. 8.3.1.1	Notes 2 and 3	-5	+5	
Relative Temperature Coefficient	$\Delta TC_R$	Para. 8.3.1.1	Notes 2 and 3	-3	+3	10 <sup>-6</sup> /°C
-155 (+3 -0)°C		Para. 8.3.1.1	Notes 2 and 3	-5	+5	

## NOTES:

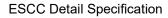
- 1. The measurements shall be performed on all the individual resistors of the array.
- The measurements shall be performed on a sample of 5 components selected from the total production lot. The components shall be mounted as specified in the ESCC Generic Specification.
- 3. See Component Type Variants and Range of Component, Para. 1.4.2 Note 5.

$$\Delta \mathsf{TC}_{\mathsf{R}} = \mathsf{Max} \left[ \frac{\mathsf{R}_{\mathsf{A}(\mathsf{T}^\circ)} - \mathsf{R}_{\mathsf{A}(\mathsf{T})}}{\mathsf{R}_{\mathsf{A}(\mathsf{T}^\circ)}} \cdot \frac{1}{(\mathsf{T}^\circ - \mathsf{T})} \right] - \mathsf{Min} \left[ \frac{\mathsf{R}_{\mathsf{A}(\mathsf{T}^\circ)} - \mathsf{R}_{\mathsf{A}(\mathsf{T})}}{\mathsf{R}_{\mathsf{A}(\mathsf{T}^\circ)}} \cdot \frac{1}{(\mathsf{T}^\circ - \mathsf{T})} \right]$$

The calculations are done between all the resistor pairs of the array.

(T°) is the reference temperature of the test method (+22  $\pm$ 3°C).

(T) is the high (+155 (+0 -3)°C) or the low (-55 (+3 -0)°C) testing temperature.



PAGE 15

ISSUE 9

# 2.7 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

The components shall be mounted as specified in the ESCC Generic Specification.

Unless otherwise specified, the measurements shall be performed at  $T_{amb}$  = +22 ±3°C.

Unless otherwise specified the test methods and test conditions shall be as per the corresponding test defined in Para. 2.6.1 Room Temperature Electrical Measurements.

Test Reference per	Characteristics	Symbols	Limits		Units
ESCC No. 4001			Min	Max	-
Rapid Change of Temperature					
Initial Measurement	Resistance	R <sub>A</sub>	Record Values		
	Relative Tolerance	$\Delta T_R$	See Para. 2.6.1		
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	± (0.05 +0.05Ω x 100 / R <sub>n</sub> )		%
	Relative Tolerance	$\Delta T_R$	See Par	a. 2.6.1	
Robustness of Terminations					
Initial Measurement	Resistance	RA	Record	Values	
	Relative Tolerance	$\Delta T_R$	See Par	a. 2.6.1	
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	± (0.05 +0.05	Ω x 100 / R <sub>n</sub> )	%
	Relative Tolerance	$\Delta T_R$	See Par	a. 2.6.1	
Resistance to Soldering Heat					
Initial Measurement	Resistance	RA	Record Values		
	Relative Tolerance	$\Delta T_R$	See Par	a. 2.6.1	
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	,		%
	Relative Tolerance	$\Delta T_R$			
Solderability					
Initial Measurement	Resistance	RA	Record Values		
	Relative Tolerance	$\Delta T_R$	See Para. 2.6.1		
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	A ± (0.05 +0.05Ω x 100 / R <sub>n</sub> )		%
	Relative Tolerance	$\Delta T_{R}$	See Para. 2.6.1		
Climatic Sequence					
Initial Measurements (Procedure 1)	Resistance (after drying)	RA	Record Values		
	Relative Tolerance	$\Delta T_R$	See Para. 2.6.1		
Final Measurements	Relative Tolerance	$\Delta T_{R}$			
	Change in Resistance	$\Delta R_A/R_A$			%
	Insulation Resistance (V⊤ = 100V)	Rı	1000	-	MΩ





**ISSUE 9** 

Test Reference per	Characteristics	Symbols	Limits		Units
ESCC No. 4001			Min	Max	
Operating Life					
Initial Measurement (0 hour)	Resistance	RA	Record Values		
	Relative Tolerance	$\Delta T_{R}$	See Para. 2.6.1		
Intermediate Measurements	Relative Tolerance	$\Delta T_{R}$	See Para. 2.6.1		
(1000 hours)	Change in Resistance	$\Delta R_A/R_A$	± (0.1 +0.059	Ω x 100 / R <sub>n</sub> )	%
Final Measurements	Relative Tolerance	$\Delta T_R$	See Para. 2.6.1		
(2000 hours)	Change in Resistance	$\Delta R_A/R_A$	± (0.15 +0.05	Ω x 100 / R <sub>n</sub> )	%
	Insulation Resistance (V <sub>T</sub> = 100V)	Rı	1000	-	MΩ

## 2.8 BURN-IN CONDITIONS

Characteristics	Symbols	Test Conditions	Units
Ambient Temperature	T <sub>amb</sub>	+70 ±5	°C
Test Voltage	VT		V
Variants 01 to 21		$\sqrt{(P_n \ x \ R_n)}$ or $U_L$ , whichever is less	
Variants 22 to 42		√(Pn x Rmin) or U∟, whichever is less	

# **NOTES**

- 1.  $V_T$  is applied to each individual resistor.  $R_{min}$  is the lowest ohmic value of the array.
- 2. After Burn-in, the components shall be removed from the chamber and allowed to cool under normal atmospheric conditions for a minimum of 4 hours.

#### 2.9 OPERATING LIFE CONDITIONS

The conditions shall be as specified in Para.2.8 Burn-in.

ESCC Detail Specification



PAGE 17

No. 4001/025

**ISSUE 9** 

# APPENDIX A

# AGREED DEVIATIONS FOR VISHAY S.A. DIVISION SFERNICE (F)

Items Affected	Description of Deviations		
Para. 2.1.1.1, Deviations from Screening Tests - Chart F3	Non-Linearity: shall not be performed.		
Para. 2.1.1.2 Deviations from Qualification and Periodic Tests - Chart F4	Climatic Sequence: shall not be performed		
Para. 2.1.1.2 Deviations from Qualification and Periodic Tests - Chart F4	Robustness Of Terminations - Substrate Bending Test: shall not be performed.		
Para. 2.4 Robustness of Terminations - Substrate Bending			
Para. 2.6.2, High and Low Temperatures Electrical Measurements	All tests at high and low temperatures are guaranteed but not tested based on temperature coefficient and relative temperature coefficient measurements performed on each wafer at +25°C and +75°C in accordance with VISHAY specification CM-SF-00210.		