

Page i

DIODES, MICROWAVE, GALLIUM ARSENIDE,

SCHOTTKY, MIXER,

BASED ON TYPES ML40461 TO ML40464

ESCC Detail Specification No. 5513/021

ISSUE 1 October 2002



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Pages 1 to 23

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SCHOTTKY, MIXER,

BASED ON TYPES ML40461 TO ML40464

ESA/SCC Detail Specification No. 5513/021

space components coordination group

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	See	ESA/SCC Detail Specification No. 5513/021		PAGE ISSUE	3 1
		TABLE OF CONTENTS	<u> </u>		
1.	GENERAL			<u>P</u>	2 <u>age</u> 5
1.1	Scope				5
1.2	Type Variants				5
1.2	Maximum Ratings				5
1.4	Parameter Derating Info	ormation			5
1.5	Physical Dimensions				5
1.6	Functional Diagram				5
1.7	Handling Precautions				5
2.		IENTS			5
3.		S, ABBREVIATIONS, SYMBOLS AND U			5
3. 4.	REQUIREMENTS	S, ABBREVIATIONS, STMBOLS AND C			13
4.1	General	- Cussification			13
4.2	Deviations from Generic				13
4.2.1	Deviations from Special				13
4.2.2	Deviations from Final P				13
4.2.3		and Electrical Measurements			13
4.2.4	Deviations from Qualific				13
4.2.5	Deviations from Lot Acc				13
4.3	Mechanical and Enviror	nmental Requirements			13
4.3.1	Dimension Check				13
4.3.2	Weight				14
4.3.3	Terminal Strength				14
4.3.4	Bond Strength				15
4.3.5	Die Shear				15
4.4	Materials and Finishes				15 15
4.4.1	Case	ch			15 15
4.4.2	Lead Materials and Fini	50			15 15
4.5 4.5.1	Marking				15 15
	General Cathoda Identification				15 15
4.5.2	Cathode Identification	lumber			15
4.5.3	The SCC Component N				16
4.5.4	Traceability Information				16
4.6 4.6.1	Electrical Measurement				16 16
4.6.1		ts at Room Temperature			16
4.6.2	Circuits for Electrical M	s at High and Low Temperatures			16
4.0.3	Burn-in Tests	easurements			16
4.7	Parameter Drift Values				16
4.7.1		nperature Reverse Bias Burn-in			16
4.7.2	Conditions for Power B				16
4.7.4		igh Temperature Reverse Bias and Power	Rum-in		16
4.8	Environmental and End		Durran		20
4.8.1		ts on Completion of Environmental Tests			20
4.8.2		ts at Intermediate Points and on Completion	on of Endurance Te	ete	20
4.8.2	Conditions for Operatin	-			20
4.8.4	Electrical Circuits for O	-			20
4.0.4	Total Dose Irradiation T	• –			20
4.9.1	Application				20
4.9.1	Bias Conditions				20
4.9.2	Electrical Measurement	ts.			20
4.10	Special Testing				20

••

••

Sec	ESA/SCC Detail Specification No. 5513/021		PAGE ISSUE	4	
-----	--	--	---------------	---	--

TABLES

Page

1(a)	Type Variants	6
1(b)	Maximum Ratings	7
2	Electrical Measurements at Room Temperature - D.C. Parameters	17
	Electrical Measurements at Room Temperature - A.C. Parameters	17
3	Electrical Measurements at High and Low Temperatures	18
4	Parameter Drift Values	18
5(a)	Conditions for High Temperature Reverse Bias Burn-in	19
5(b)	Conditions for Power Burn-in	19
5(c)	Conditions for Operating Life Tests	19
6	Electrical Measurements at Intermediate Points and on Completion of Endurance Testing	21
7	Electrical Measurements During and on Completion of Irradiation Testing	22

FIGURES

1	Parameter Derating Information	7
2	Physical Dimensions	8
3	Functional Diagram	12
4	Circuits for Electrical Measurements	N/A
5(a)	Electrical Circuit for High Temperature Reverse Bias Burn-in	19
5(b)	Electrical Circuit for Power Burn-in and Operating Life Tests	19
6	Bias Conditions for Irradiation Testing	21
	-	

APPENDICES	(Applicable to specific Manufacturers	s only)

'A' Agreed deviations for M/A-Com Ltd. (G.B.)

23



1. <u>GENERAL</u>

1.1 <u>SCOPE</u>

This specification details the ratings, physical and electrical characteristics, test and inspection data for a Diode, Microwave, Gallium Arsenide, Schottky, Mixer, based on Types ML40461 to ML40464. It shall be read in conjunction with ESA/SCC Generic Specification No. 5010, the requirements of which are supplemented herein.

1.2 <u>TYPE VARIANTS</u>

Variants of the basic diodes specified herein, which are also covered by this specification are given in Table 1(a).

1.3 MAXIMUM RATINGS

The maximum ratings, which shall not be exceeded at any time during use or storage, applicable to the diodes specified herein, are as scheduled in Table 1(b).

1.4 PARAMETER DERATING INFORMATION

The derating information applicable to the diodes specified herein is shown in Figure 1.

1.5 PHYSICAL DIMENSIONS

The physical dimensions of the diodes specified herein are shown in Figure 2.

1.6 FUNCTIONAL DIAGRAM

The functional diagram, showing lead identification, of the diodes specified herein, is shown in Figure 3.

1.7 HANDLING PRECAUTIONS

These devices are susceptible to damage by electrostatic discharge. Therefore, suitable precautions shall be employed for protection during all phases of manufacture, testing, packaging, shipment and any handling.

These components are Categorised as Class 1 with a Minimum Critical Path Failure Voltage of 500V.

2. APPLICABLE DOCUMENTS

The following documents form part of this specification and shall be read in conjunction with it:-

- (a) ESA/SCC Generic Specification No. 5010 for Discrete Microwave Semiconductor Components.
- (b) MIL-STD-750, Test Methods and Procedures for Semiconductor Devices.

3. TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESA/SCC Basic Specification No. 21300 shall apply. In addition, the following symbol is used:-

 $E_B = Burn-out Energy.$



PAGE 6

ISSUE 1

TABLE 1(a) - TYPE VARIANTS

(1) VARIANT	(2) BASED ON TYPE	(3) FIGURE	(4) L.O. TEST FREQUENCY LO _f (GHz)	(5) TOTAL CAPACITANCE C _T (pF) (MAX.)	(6) NOISE FIGURE NF (dB) (MAX.)	(7) BODY-LID AND LEAD MATERIAL AND FINISH
01	ML40461 - 118	2(a)	9.375	0.34	6.5	A7
02	ML40461 - 119	2(b)	9.375	0.31	6.5	A7-D2
03	ML40461 - 120	2(c)	9.375	0.26	6.5	D2
04	ML40461 - 148	2(d)	9.375	0.36	6.5	A7
05	ML40461 - 186	2(e)	9.375	0.30	6.5	D2
06	ML40461 - 255	2(f)	9.375	0.46	6.5	A7
07	ML40461 - 275	2(g)	9.375	0.52	6.5	A7
08	ML40461 - 276	2(h)	9.375	0.26	6.5	D2
09	ML40462 - 118	2(a)	16	0.34	7.0	A7
10	ML40462 - 119	2(b)	16	0.31	7.0	A7-D2
11	ML40462 - 120	2(c)	16	0.26	7.0	D2
12	ML40462 - 148	2(d)	16	0.36	7.0	A7
13	ML40462 - 186	2(e)	16	0.30	7.0	D2
14	ML40462 - 255	2(f)	16	0.46	7.0	A7
15	ML40462 - 275	2(g)	16	0.52	7.0	A7
16	ML40462 - 276	2(h)	16	0.26	7.0	D2
17	ML40463 - 118	2(a)	24	0.32	7.5	A7
18	ML40463 - 119	2(b)	24	0.29	7.5	A7-D2
19	ML40463 - 120	2(c)	24	0.24	7.5	D2
20	ML40463 - 148	2(d)	24	0.34	7.5	A7
21	ML40463 - 186	2(e)	24	0.28	7.5	D2
22	ML40463 - 255	2(f)	24	0.44	7.5	A7
23	ML40463 - 275	2(g)	24	0.50	7.5	A7
24	ML40463 - 276	2(h)	24	0.24	7.5	D2
25	ML40464 - 118	2(a)	36	0.30	7.5	A7
26	ML40464 - 119	2(b)	36	0.28	7.5	A7-D2
27	ML40464 - 120	2(c)	36	0.23	7.5	D2
28	ML40464 - 148	2(d)	36	0.32	7.5	A7
29	ML40464 - 186	2(e)	36	0.27	7.5	D2
30	ML40464 - 255	2(f)	36	0.42	7.5	A7
31	ML40464 - 275	2(g)	36	0.48	7.5	A7
32	ML40464 - 276	2(h)	36	0.23	7.5	D2



ISSUE 1

TABLE 1(b) - MAXIMUM RATINGS

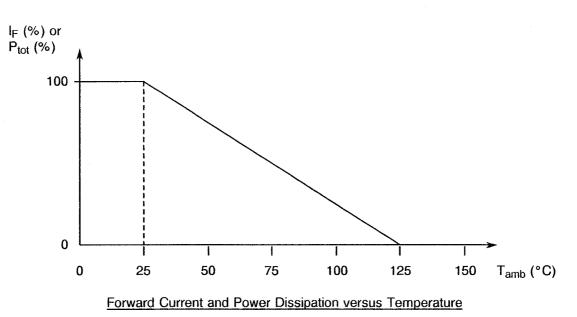
No.	CHARACTERISTIC	SYMBOL	MAXIMUM RATINGS	UNIT	REMARKS
1	D.C. Reverse Voltage	V _R	- 5.0	V	
2	D.C. Forward Current	l _F	10	mA	Note 1
3	D.C. Power Dissipation	P _{tot}	12.5	mW	Note 1
4	Burn-out Energy	E _B	4.0	Erg	Note 2
5	Operating Temperature Range	Т _{ор}	-65 to +125	°C	T _{amb}
6	Storage Temperature Range	T _{stg}	-65 to +125	°C	
7	Soldering Temperature	T _{soł}	+ 230	°C	Note 3

NOTES

1. At T_{amb} = +25°C. For derating at T_{amb} > +25°C, see Figure 1.

- 2. Quoted for a single discharge of Torry line during the first 2.4ns current flow in the forward direction. General criterion for burn-out energy is a 3.0dB increase in Noise Figure.
- 3. Duration 5 seconds maximum (at a distance of not less than 1.5mm from the body for Variants 05, 08, 13, 16, 21, 24, 29, 32) and the same termination shall not be resoldered until 3 minutes have elapsed.

FIGURE 1 - PARAMETER DERATING INFORMATION





≪Β⇒

Α

→ Y1

Ζ2

Q

X1

Cathode

Ød ØD

end

-B2

FIGURE 2 - PHYSICAL DIMENSIONS

FIGURE 2(a) - VARIANTS 01, 09, 17, 25

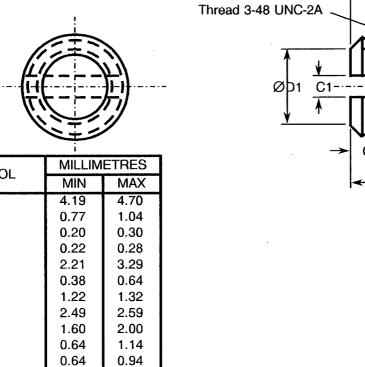
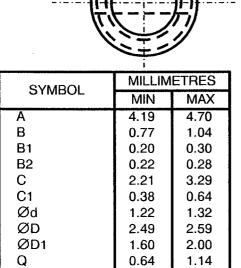
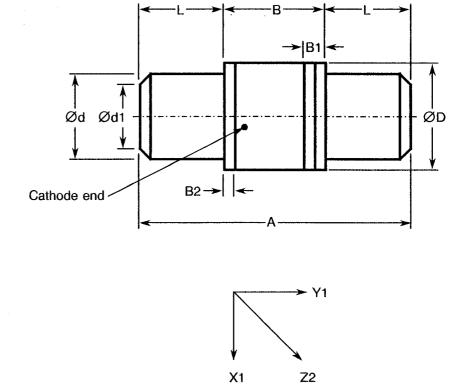
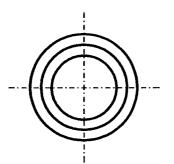


FIGURE 2(b) - VARIANTS 02, 10, 18, 26



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SYMBOL	MILLIM	ETRES
STWIDUL	MIN	MAX
А	4.83	5.33
В	1.68	2.08
B1	0.41	0.61
B2	0.15	0.25
Ød	1.52	1.63
Ød1	1.20	1.50
ØD	1.95	2.11
L	1.52	1.63

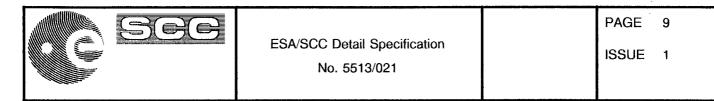
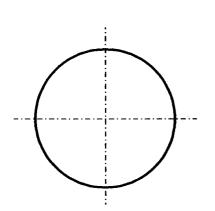
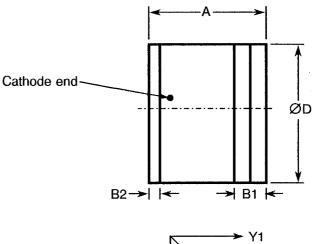
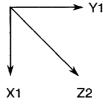


FIGURE 2 - PHYSICAL DIMENSIONS (CONTINUED)

FIGURE 2(c) - VARIANTS 03, 11, 19, 27

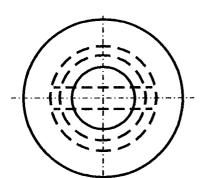




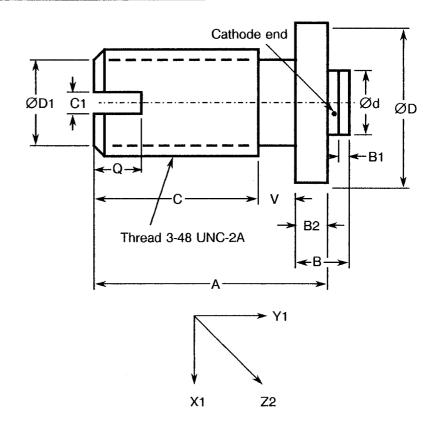


SYMBOL	MILLIMETRES		
STMBUL	MIN	MAX	
А	1.02	1.27	
B1	0.23	0.33	
B2	0.10	0.15	
ØD	1.29	1.40	

FIGURE 2(d) - VARIANTS 04, 12, 20, 28



SYMBOL	MILLIMETRES		
STINDUL	MIN	MAX	
A	4.24	4.75	
В	1.02	1.32	
B1	0.20	0.30	
B2	0.46	0.56	
С	2.74	3.65	
C1	0.38	0.64	
Ød	1.22	1.32	
ØD	2.87	3.00	
ØD1	1.60	2.00	
Q	0.89	1.14	
V	0.64	0.89	



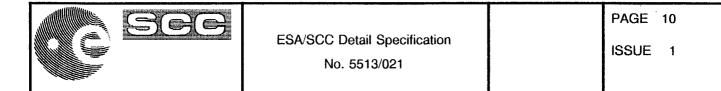
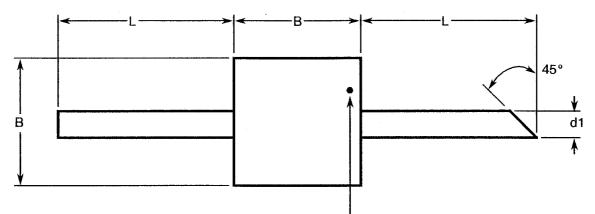
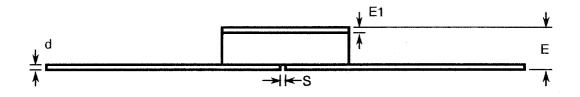


FIGURE 2 - PHYSICAL DIMENSIONS (CONTINUED)

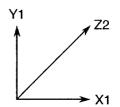
FIGURE 2(e) - VARIANTS 05, 13, 21, 29



Cathode end



SYMBOL	MILLIM	ETRES
STWDUL	MIN	MAX
В	2.39	2.62
d	0.07	0.15
d1	0.48	0.56
E	0.79	1.12
E1	0.10	0.18
L	3.30	5.84
S	0.10	-



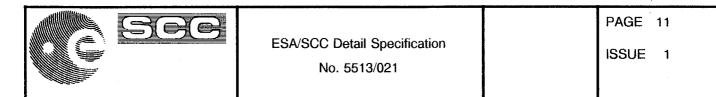
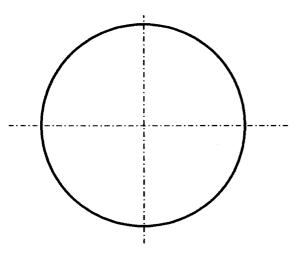


FIGURE 2 - PHYSICAL DIMENSIONS (CONTINUED)

FIGURE 2(f) - VARIANTS 06, 14, 22, 30



MILLIMETRES

MAX

1.40

0.33

0.20

2.16

MIN

1.14

0.23

0.15

1.90

SYMBOL

A

B1

B2

ØD

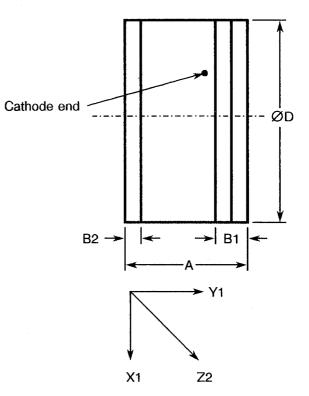
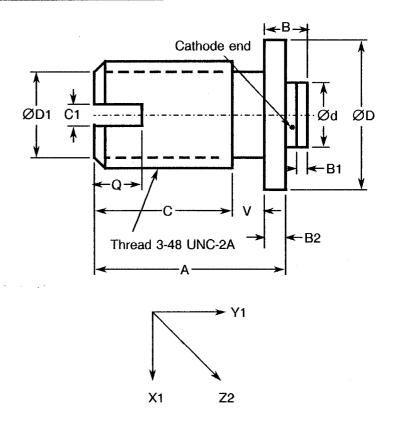
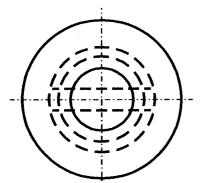


FIGURE 2(g) - VARIANTS 07, 15, 23, 31



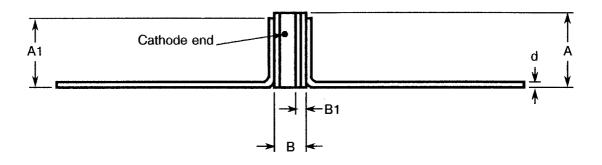


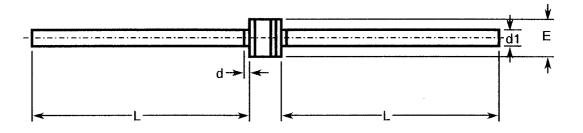
SYMBOL	MILLIM	ETRES
STWDUL	MIN	MAX
А	4.04	4.55
В	0.84	1.12
B1	0.20	0.30
B2	0.46	0.56
С	2.54	3.45
C1	0.38	0.64
Ød	1.22	1.32
ØD	2.87	3.00
ØD1	1.60	2.00
Q	0.89	1.14
V	0.64	0.94



FIGURE 2 - PHYSICAL DIMENSIONS (CONTINUED)

FIGURE 2(h) - VARIANTS 08, 16, 24, 32





SYMBOL	MILLIM	IETRES
STMDUL	MIN	MAX
A	1.35	2.54
A1	-	2.46
В	1.02	1.27
B1	_	0.38
d	0.07	0.15
d1	0.38	0.64
E	1.29	1.40
L	6.10	12.40

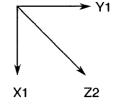


FIGURE 3 - FUNCTIONAL DIAGRAM



1. Anode

2. Cathode

NOTES

1. The cathode end shall be marked with a black dot or band. The marking will not be on the cathode connection but adjacent to it.



4. **REQUIREMENTS**

4.1 GENERAL

The complete requirements for procurement of the diodes specified herein shall be as stated in this specification and ESA/SCC Generic Specification No. 5010 for Discrete Microwave Semiconductor Components. Deviations from the Generic Specification applicable to this specification only, are listed in Para. 4.2.

Deviations from the applicable Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESA/SCC requirements and do not affect the components' reliability, are listed in the appendices attached to this specification.

4.2 DEVIATIONS FROM GENERIC SPECIFICATION

- 4.2.1 Deviations from Special In-process Controls
 - (a) Para. 5.2.2, Total Dose Irradiation Testing: Shall be performed during qualification and extension of qualification.
 - (b) Para. 5.2.2, Total Dose Irradiation Testing: Shall be performed during procurement on a lot acceptance basis at the total dose irradiation level specified in the purchase order.
 - (c) Para. 5.3, Wafer Lot Acceptance: Shall be performed as an S.E.M. Inspection only.

4.2.2 Deviations from Final Production Tests (Chart II)

(a) Para. 9.14, Vibration, Variable Frequency: Shall not be performed.

- 4.2.3 <u>Deviations from Burn-in and Electrical Measurements (Chart III)</u>
 (a) Para. 7.1.1(a), High Temperature Reverse Bias Burn-in: Shall be performed at 60% of rated V_B.
- 4.2.4 <u>Deviations from Qualification Tests (Chart IV)</u>(a) Para. 9.23, Special Testing: Shall not be performed.
- 4.2.5 <u>Deviations from Lot Acceptance Tests (Chart V)</u>(a) Para. 9.23, Special Testing: Shall not be performed.
- 4.3 MECHANICAL AND ENVIRONMENTAL REQUIREMENTS
- 4.3.1 Dimension Check

The dimensions of the diodes specified herein shall be checked. They shall conform to those shown in Figure 2.



4.3.2 Weight

The maximum weight of the diodes specified herein shall be:

Variant No.	Weight (g)
01, 09, 17, 25	0.14
02, 10, 18, 26	0.12
03, 11, 19, 27	0.014
04, 12, 20, 28	0.15

Variant No.	Weight (g)
05, 13, 21, 29	0.06
06, 14, 22, 30	0.08
07, 15, 23, 31	0.15
08, 16, 24, 32	0.025

4.3.3 Terminal Strength

The requirements for terminal strength testing are specified in Section 9 of ESA/SCC Generic Specification No. 5010. The test conditions shall be as follows:-

(a) Condition: 'A' (Tension)

Variants 05, 13, 21 and 29:

- Force: 5.1N.
- Duration: 5 seconds.

Variants 08, 16, 24, 32:

- Force: 1.22N.
- Duration: 5 seconds.

(b) Condition: 'D2' (Stud Torque)

Variants 01, 04, 07, 09, 12, 15, 17, 20, 23, 25, 28 and 31:

- Torque: 42mNm.
- Duration: 5 seconds.

(c) Condition: Compression

Variants 02, 06, 10, 14, 18, 22, 26 and 30:

- Force: 50N.
- Duration: 5 seconds.

Variants 01, 03, 04, 07, 09, 11, 12, 15, 17, 19, 20, 23, 25, 27, 28 and 31:

- Force: 10N.
- Duration: 5 seconds.

The compression test shall be performed by applying the specified force to the end-cap by means of a suitable weight applied for the specified time. On completion of the test, a visual examination shall be performed to check for damage to the end-cap or the ceramic body.



4.3.4 Bond Strength

The requirements for bond strength are specified in Section 9 of ESA/SCC Generic Specification No. 5010. The minimum separation force shall be:-

- (a) For Chart II : 0.015N.
- (b) For Chart IV : 0.075N.

4.3.5 Die Shear

The requirements for die shear are specified in Section 9 of ESA/SCC Generic Specification No. 5010. The minimum die shear strength shall be 0.45N.

4.4 MATERIALS AND FINISHES

The materials and finishes shall be as specified herein. Where a definite material is not specified, a material which will enable the diodes specified herein to meet the performance requirements of this specification shall be used. Acceptance or approval of any constituent material shall not guarantee acceptance of the finished product.

4.4.1 <u>Case</u>

The case shall be hermetically sealed and have a ceramic body. The lid shall be welded or preform soldered.

4.4.2 Lead Materials and Finish

- (a) For Variants 02, 10, 18 and 26, the body material shall be Type 'A' with Type '7' finish and the lid material shall be Type 'D' with Type '2' finish, in accordance with the requirements of ESA/SCC Basic Specification No. 23500.
- (b) For Variants 01, 04, 06, 07, 09, 12, 14, 15, 17, 20, 22, 23, 25, 28, 30 and 31, the lead material shall be Type 'A' with Type '7' finish in accordance with the requirements of ESA/SCC Basic Specification No. 23500.
- (c) For Variants 03, 05, 08, 11, 13, 16, 19, 21, 24, 27, 29 and 32, the lead material shall be Type 'D' with Type '2' finish in accordance with the requirements of ESA/SCC Basic Specification No. 23500.

4.5 <u>MARKING</u>

4.5.1 <u>General</u>

The marking of components delivered to this specification shall be in accordance with the requirements of ESA/SCC Basic Specification No. 21700 and the following paragraphs. When the component is too small to accommodate all of the marking specified, as much as space permits shall be marked and the marking information, in full, shall accompany the component in its primary package.

The information to be marked and the order of precedence, shall be as follows:-

- (a) Cathode Identification.
- (b) The SCC Component Number.
- (c) Traceability Information.

4.5.2 Cathode Identification

Cathode identification shall be as shown in Figures 2 and 3 of this specification.



4.5.3 The SCC Component Number

Each component shall bear the SCC Component Number which shall be constituted and marked as follows:

	55130210	1BF	:
Detail Specification Number			
Type Variant (see Table 1(a))			
Testing Level (B or C, as applicable)		┛╽	

Total Dose Irradiation Level (if applicable)

The Total Dose Irradiation Level designation shall be added for those devices for which a sample has been successfully tested to the level in question. For these devices, a code letter shall be added in accordance with the requirements of ESA/SCC Basic Specification No. 22900.

4.5.4 Traceability Information

Each component shall be marked in respect of traceability information as defined in ESA/SCC Basic Specification No. 21700.

4.6 ELECTRICAL MEASUREMENTS

4.6.1 Electrical Measurements at Room Temperature

The parameters to be measured at room temperature are scheduled in Table 2. Unless otherwise specified, the measurements shall be performed at T_{amb} = +22 ± 3 °C.

4.6.2 Electrical Measurements at High and Low Temperatures

The parameters to be measured at high and low temperatures are scheduled in Table 3. Unless otherwise specified, the measurements shall be performed at $T_{amb} = +150(+0-3)$ °C.

4.6.3 Circuits for Electrical Measurements

Not applicable

4.7 BURN-IN TESTS

Burn-in shall be Category 3 of Chart III(a).

4.7.1 Parameter Drift Values

The parameter drift values applicable to burn-in are specified in Table 4 of this specification. Unless otherwise stated, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C. The parameter drift values (Δ) applicable to the scheduled parameters shall not be exceeded. In addition to these drift value requirements for a given parameter, the appropriate limit value specified in Table 2 shall not be exceeded.

4.7.2 Conditions for High Temperature Reverse Bias Burn-in

The requirements for the high temperature reverse bias burn-in are specified in Section 7 of ESA/SCC Generic Specification No. 5010. The conditions for high temperature reverse bias burn-in shall be as specified in Table 5(a) of this specification.

4.7.3 Conditions for Power Burn-in

The requirements for power burn-in are specified in Section 7 of ESA/SCC Generic Specification No. 5010. The conditions for power burn-in shall be as specified in Table 5(b) of this specification.

4.7.4 Electrical Circuits for High Temperature Reverse Bias and Power Burn-in

Circuits for use in performing the H.T.R.B and power burn-in tests are shown in Figures 5(a) and 5(b) of this specification.



TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - D.C. PARAMETERS

No	No. CHARACTERISTICS		MIL-STD-750	TEST CONDITIONS	LIMITS		UNIT
	UNANAUTENIS 103	SYMBOL	TEST METHOD	THOD		MAX.	
1	Breakdown Voltage	V _(BR)	4021	l _R = 10μA	5.0	-	V
2	Reverse Current	l _R	4016	V _R = -1.0V	-	25	nA
3	Forward Voltage 1	V _{F1}	4011	l _F = 0.1mA	-	0.75	V
4	Forward Voltage 2	V _{F2}	4011	I _F = 1.0mA	-	0.82	V
5	Series Resistance	R _S	Note 1	l _F = 10mA Note 2	2.5	8.5	Ω

NOTES

1. Circuit as per Items 3 and 4.

2.
$$R_S = R_d - 2.6$$
 where $R_d = \frac{dV}{dl}$ at $I_F = 10$ mA.

TABLE 2 - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - A.C. PARAMETERS

No.	CHARACTERISTICS	SYMBOL	MIL-STD-750	TEST CONDITIONS	LIM	ITS	UNIT
110.	CHARACTERISTICS	STIMBUL	TEST METHOD		MIN.	MAX.	UNIT
6	Total Capacitance	CT	4001	V _R = 0V f = 1.0MHz	Not	e 1	pF
7	Noise Figure	NF	4126	I.F. = 30MHz L.O. Power = 6dbm LO _f = Note 3	Not	e 2	dB

NOTES

1. See Column 5 of Table 1(a).

2. See Column 6 of Table 1(a).

3. See Column 4 of Table 1(a).



TABLE 3 - ELECTRICAL MEASUREMENTS AT HIGH AND LOW TEMPERATURES

No.	CHARACTERISTICS	SVMBOL	SPEC AND/OR	TEST	LIM	IITS	
NO.	NO. CHARACTERISTICS	STIVIDUL	TEST METHOD	CONDITIONS	MIN.	MAX.	UNIT
2	Reverse Current	l _R	As per Table 2	As per Table 2	-	1.0	μА

TABLE 4 - PARAMETER DRIFT VALUES

No.	CHARACTERISTICS	SYMBOL	SPEC.AND/OR TEST METHOD	TEST CONDITIONS	CHANGE LIMITS (Δ)	UNIT
2	Reverse Current	I _R	As per Table 2	As per Table 2	±5.0 (1) or (2) ±100 (1)	nA %
3	Forward Voltage 1	V _{F1}	As per Table 2	As per Table 2	± 10 (1)	%
4	Forward Voltage 2	V _{F2}	As per Table 2	As per Table 2	± 10 (1)	%
6	Total Capacitance	CT	As per Table 2	As per Table 2	± 10 (1)	%

<u>NOTES</u>

1. $\Delta 1 = \Delta 2 = \Delta 3$.

2. Whichever is the greater, referred to the initial measurement.

FIGURE 4 - CIRCUITS FOR ELECTRICAL MEASUREMENTS

Not applicable.



TABLE 5(a) - CONDITIONS FOR HIGH TEMPERATURE REVERSE BIAS BURN-IN

No.	CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT
1	Ambient Temperature	T _{amb}	+ 125(+ 0 - 3)	°C
2	Reverse Voltage	V _R	- 3.0	V

TABLE 5(b) - CONDITIONS FOR POWER BURN-IN

No.	CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT
1	Ambient Temperature	T _{amb}	+ 100(+ 0 - 3)	°C
2	Forward Current	١F	2.5	mA

TABLE 5(c) - CONDITIONS FOR OPERATING LIFE TESTS

No.	CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT
1	Ambient Temperature 1	T _{amb1}	+ 90(+ 0 - 3)	°C
2	Forward Current 1	I _{F1}	3.0	mA
3	Ambient Temperature 2	T _{amb2}	+ 100(+ 0 - 3)	°C
4	Forward Current 2	I _{F2}	2.5	mA

FIGURE 5(a) - ELECTRICAL CIRCUIT FOR HIGH TEMPERATURE REVERSE BIAS BURN-IN

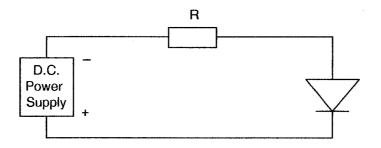
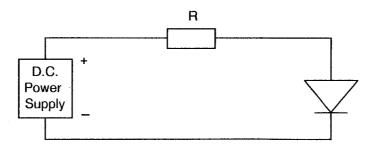


FIGURE 5(b) - ELECTRICAL CIRCUIT FOR POWER BURN-IN AND OPERATING LIFE TESTS





4.8 <u>ENVIRONMENTAL AND ENDURANCE TESTS (CHARTS IV AND V OF ESA/SCC GENERIC</u> SPECIFICATION NO. 5010)

4.8.1 Electrical Measurements on Completion of Environmental Tests

The parameters to be measured on completion of environmental tests are scheduled in Table 2. Unless otherwise stated, the measurements shall be performed at T_{amb} = +22 ± 3 °C.

4.8.2 Electrical Measurements at Intermediate Points and on Completion of Endurance Tests

The parameters to be measured at intermediate points and on completion of endurance tests are scheduled in Table 6. Unless otherwise stated, the measurements shall be performed at $T_{amb} = +22 \pm 3$ °C.

4.8.3 Conditions for Operating Life Tests (Part of Endurance Testing)

The requirements for operating life testing are specified in Section 9 of ESA/SCC Generic Specification No. 5010. The conditions for operating life testing are specified in Table 5(c) of this specification.

4.8.4 Electrical Circuits for Operating Life Tests

The circuit to be used for performance of the operating life test shall be the same as shown in Figure 5(b) for Power Burn-in.

4.9 TOTAL DOSE IRRADIATION TESTING

4.9.1 Application

If specified in Para. 4.2.1 of this specification, total dose irradiation testing shall be performed in accordance with the requirements of ESA/SCC Basic Specification No. 22900.

4.9.2 Bias Conditions

Continuous bias shall be applied during irradiation testing as shown in Figure 6 of this specification.

4.9.3 Electrical Measurements

The parameters to be measured prior to irradiation exposure are scheduled in Table 2 of this specification. Only devices which meet the requirements of Table 2 shall be included in the test sample.

The parameters to be measured during and on completion of irradiation testing are scheduled in Table 7 of this specification.

4.10 SPECIAL TESTING

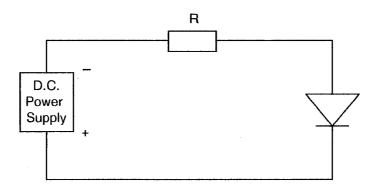
Not applicable.



TABLE 6 - ELECTRICAL MEASUREMENTS AT INTERMEDIATE POINTS AND ON COMPLETION OF ENDURANCE TESTING

No.	CHARACTERISTICS	SYMBOL	SPEC. AND/OR TEST METHOD	TEST CONDITIONS	LIMITS MIN. MAX.	UNIT
1	Breakdown Voltage	V _(BR)	As per Table 2	As per Table 2	As per Table 2	V
2	Reverse Current	I _R	As per Table 2	As per Table 2	As per Table 2	nA
3	Forward Voltage 1	V _{F1}	As per Table 2	As per Table 2	As per Table 2	V
4	Forward Voltage 2	V _{F2}	As per Table 2	As per Table 2	As per Table 2	V
7	Noise Figure	NF	As per Table 2	As per Table 2	As per Table 2	dB

FIGURE 6 - BIAS CONDITIONS FOR IRRADIATION TESTING



<u>NOTES</u>

1. A reverse bias of $V_{\rm R}$ = -1.0 shall be applied.

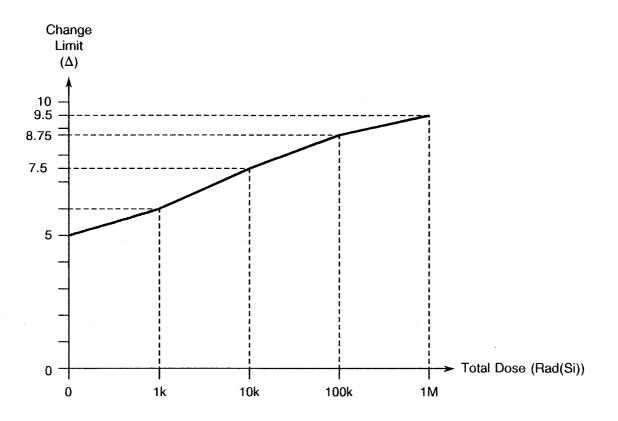


TABLE 7 - ELECTRICAL MEASUREMENTS DURING AND ON COMPLETION OF IRRADIATION TESTING

No.	CHARACTERISTICS	SYMBOL	SPEC. AND/OR TEST METHOD	TEST CONDITIONS	CHANGE LIMITS (Δ)	UNIT
2	Reverse Current	I _R	As per Table 2	As per Table 2	Note 1	nA

NOTES

1. The graph given below shall be used to determine the maximum permitted change.





APPENDIX 'A'

Page 1 of 1

AGREED DEVIATIONS FOR M/A-Com LTD. (G.B.)

ITEMS AFFECTED	DESCRIPTION OF DEVIATIONS
Para. 4.2.2	Para. 9.4, "High Temperature Stabilisation Bake": May be performed at +150(+0-3) °C.