Total Dose Test Octal Bus Transceiver 74 ACT 245 Manufactured by Harris Semiconductor

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1 Tested Device

- Octal Bus Transceiver, $74\,\mathrm{ACT}\,245$ from Harris Semiconductor
- Temperature range: $-40^{\circ}C...+85^{\circ}C$
- Package: 20-lead plastic SOIC
- $\bullet\,$ Package marks: ACT 245 , H9845 BFHX
- $\bullet\,$ vH&S order 004437/COSIMA-We
00, 13 January 2000

1.1 Device Marking

One device irradiated, second device for reference.

Mark	Total Dose
22krad	$22 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$
ref	non irradiated

2 Radiation Facility

Radiation Facility at ESTEC, Noordwijk, The Netherlands.

Date	2 March 2000	3 March 2000	2 March 2000
Total Dose	$5 \mathrm{krad} \left(\mathrm{H}_2\mathrm{O}\right)$	$10 \mathrm{krad} \mathrm{(H_2O)}$	$22 \mathrm{kRad} \mathrm{(H_2O)}$
Log File	d:\data\vh-s5k.txt	d:\data\vh-s10k.txt	d:\data\vh-s15k.txt
Device		unbiased	
Project	vH&S		
Dosemeter	Farmer 2670		
Chamber	NE 0.6cc air ionisation type 2571 serial no. 2915		
Dose Rate	$24 \mathrm{rad}/\mathrm{min} \mathrm{(H_2O)}$		
Test Eng.	Bob Nickson, QCA, ESTEC, Noordwijk		

All pins of all tested devices shorted during irradiation. No annealing was performed after irradiation.

3 Measured Device Parameters

- Current $I_{\rm CC}$ see section 4.
- Threshold voltages $V_{\rm IL}$, $V_{\rm IH}$ at A1 input see section 5.
- Output Source Resistance $R_{\rm i}$ at Q1 output see section ~6

4 $I_{\rm CC}$ Test

Measurements done on 10th of April 2000, vH&S.

4.1 Test Setup

Pins 2, 3, 4, 5, 6, 7, 8, 9, 10, 19 connected to GND Pins 11, 12, 13, 14, 15, 16, 17, 18 not connected Pin 1, 20 connected to $V_{\rm CC} = 5~V$

DC-Current into Pin 20 measured with digital multimeter.

4.2 Test Results

Device	Total Dose	$I_{\rm CC}$
22krad	$22 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$	110 nA
ref	$0 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$	$\approx 2 \text{ nA}$

(current of reference device below sensitivity range)

5 Threshold Voltages Test

Measurements done on 10th of April 2000, vH&S.

vH&S

5.1 Test Setup

Pins 3, 4, 5, 6, 7, 8, 9, 10, 19 connected to GND Pins 11, 12, 13, 14, 15, 16, 17 not connected Pin 1, 20 connected to $V_{\rm CC} = 5 V$ Pin 2 (A1) connected to variable voltage source 0...5 V Pin 18 (Q1) connected to a) 10 M Ω load (multimeter), b) 1 k Ω load Threshold voltage on pin 2 (A1) measured as voltage level on pin 18 (Q1) reaches final Highor Low-condition.

5.2 Test Results

Device	Total Dose	Load Resistance	$V_{\mathrm{IL}_{\mathrm{max}}}$	$V_{\mathrm{IH}_{\min}}$
22krad	$22 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$	$10 \ \mathrm{M}\Omega$	$0.891 { m V}$	1.386 V
ref	$0 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$	$10 \ \mathrm{M}\Omega$	$1.007~\mathrm{V}$	$1.318~\mathrm{V}$
22krad	$22 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$	$1 \ \mathrm{k}\Omega$	$0.920~\mathrm{V}$	$1.314~\mathrm{V}$
ref	$0 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$	$1 \ \mathrm{k}\Omega$	$1.014~\mathrm{V}$	$1.355~\mathrm{V}$

6 Output Source Resistance Test

Measurements done on 10th of April 2000, vH&S.

6.1 Test Setup

Pins 3, 4, 5, 6, 7, 8, 9, 10, 19 connected to GND Pins 11, 12, 13, 14, 15, 16, 17 not connected Pin 1, 20 connected to $V_{\rm CC} = 5,002 V$ Pin 2 (A1) connected to +5 V Pin 18 (Q1) connected to 1 kΩ load

Voltage over load on pin 18 measured, source resistance calculated

6.2 Test Results

Device	Total Dose	$R_{ m i}$
ref	$0 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$	10.02 Ω
22krad	$22 \mathrm{kRad} \mathrm{H}_2\mathrm{O}$	10.62 Ω