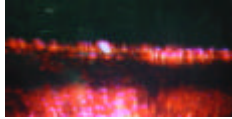


Microphotonics Devices for Space Applications

Emile Haddad, Roman V. Kruzelecky and Wes Jamroz

MPB Communications Inc.,
Igor Zayer and Iain McKenzie
European Space Agency, ESTEC
And Wanping Zheng
Canadian Space Agency



MPB

Outline

- Project Motivation and Objectives
- Advantages of Micro-optics
- Material Systems for Micro Photonic Integrated Circuits (PIC)
- Micro-PIC Technologies
- MPB Technology Demonstrator
- Conclusions

MPB

Motivation

- High penalty mass-cost during the launch of payloads (€200,000 /kg)
- Miniaturization advantages
 - higher functional densities.
 - higher frequency of missions,
 - More scientific or commercial benefit per mission.
- Optical systems in particular:
 - High data transmission and processing rates (GHz);
 - Immunity to EM interference;
 - Significant mass savings for signal harnesses (1/20 electrical)
 - Simplified signal routing and protection from ESD damage
 - Low high-frequency signal attenuation

MPB

Specific Space Applications for Microphotonics

- Optical processor for multi-channel optical sensor systems
- Optical high capacity downlinks (hyper-spectral missions)
- Optical WDM inter-satellite links
- Multi-channel configurable optical RF signal distribution and processing (programmable true-time delay, filtering)
- Satellite optical attitude control (optical gyroscope, sun sensor, earth sensor)

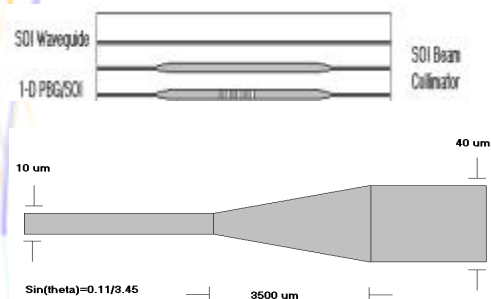
MPB

Advantages of SOI System

- Two material systems for micro PICs:
Silicon on Insulator (SOI) and InGaAsP (InP)
- SOI Advantages:
 - Large-area, low-cost substrates (> 20 cm O.D.)
 - Compatible with CMOS integrated electronics
 - Well-established wafer processing and micro machining due to Si I.C. technologies
 - Good thermal stability
 - Low-cost native thermal SiO₂
 - High-index Si waveguides or low-index SION waveguides
 - Possibility of Er or Yb-doped active silica waveguides for light amplification and waveguide tunable lasers

MPB

Basic parts: tapered waveguide and collimator



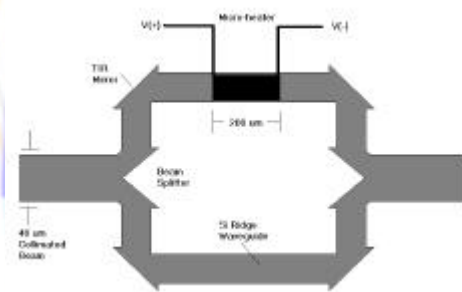
MPB

Tuning/Switching Mechanisms for SOI Platform

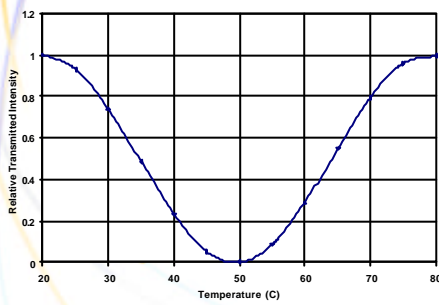
- Thermo-optic ($n = 3.38(1 + 3.9 \cdot 10^{-5}T)$, T in K)
- MEMS micro-actuators
- pressure/strain
- Photonic Bandgap structure
- VOn smart materials (Metal-Insulator transition)
 - Thermochromic (ms to μ s)
 - Electrochromic (μ s to ps)



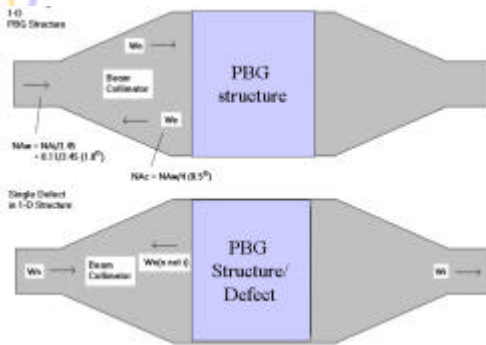
Device 1: TIR Mach Zehnder Interferometer



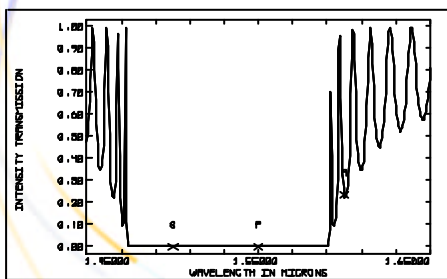
Transmitted intensity at 1550 nm versus micro-heater temperature:



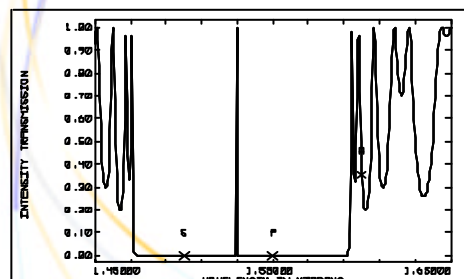
Device 2: WDM PBG Structure



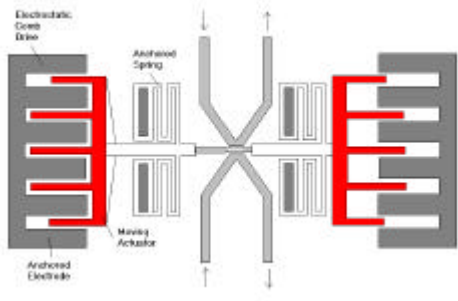
PBG 1-D Structure Transmittance



Reflected narrow line due to Single Defect in 1-D PBG Structure

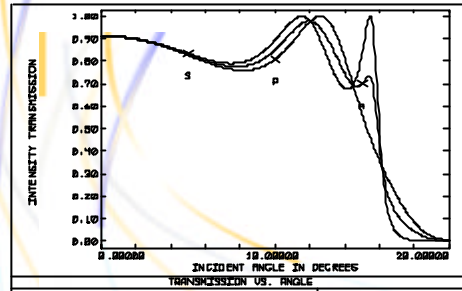


Device 3: Preliminary layout of PBG/MEMS 2x2 switch.



MPA

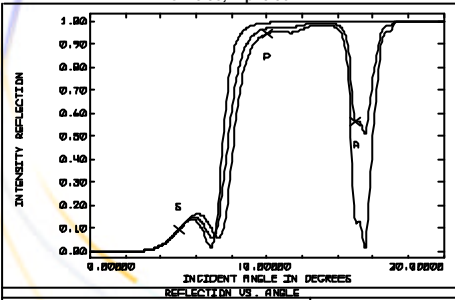
Cross-State: Tolerance: +/- 0.1μ air gap
Optimal incidence angle : 12°. $T_s = T_p = 0.96$.



MPA
 DATE: 21/03/07
 FOR THE STUDY ON SURFACE I
 INCIDENT MEDIA: SILICON
 SUBSTRATE: SILICON
 WAVELENGTH: 1.5500
 CONFIGURATION: 1 OF 1

MPA

Bar State
 $R_s = 0.98, R_p = 0.96$



MPA
 DATE: 21/03/07
 FOR THE STUDY ON SURFACE I
 INCIDENT MEDIA: SILICON
 SUBSTRATE: SILICON
 WAVELENGTH: 1.5500

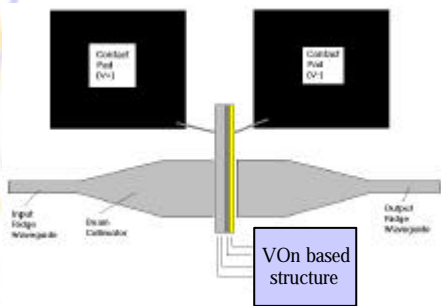
MPA

PBG MEMS advantages

- The optical switch insensitive to the polarization state
- Less than 0.5 dB loss at junction
- The "digital" switch control (cross/bar) makes the performance less sensitive to fabrication variations=> potential fabrication of arrays.
- The optical waveguide and switch are fabricated on the a single SOI wafer
- Minimal area per switching node (2mm x3mm)

MPA

Device 3: Hybrid SOI/VOn Optical Switch

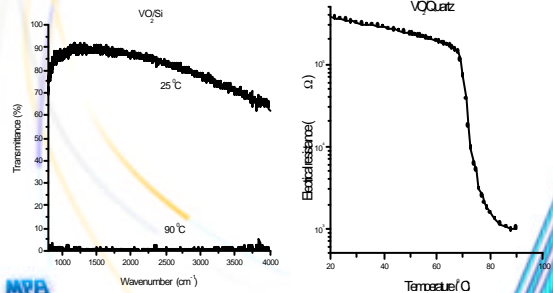


MPA

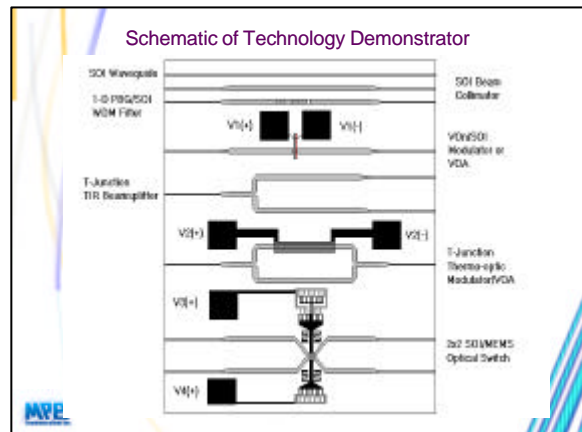
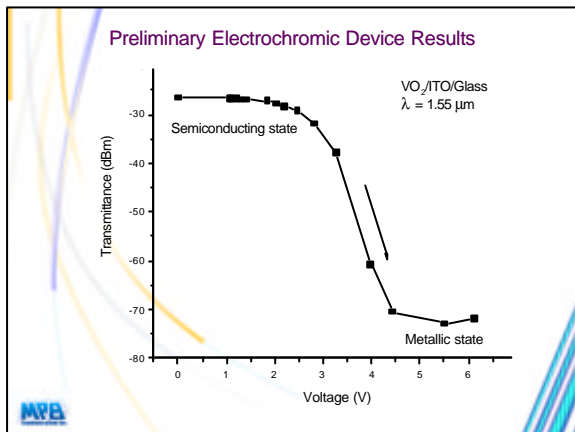
Optical and electrical characteristics of MPBC VO₂

Transmittance measurements (FT-IR) of the VO₂ thin film on silicon in a semiconductor state (25°C) and a metallic state (90°C).

The variation of the resistance of the VO₂ on quartz with temperature as measured using the four-point probe technique.



MPA



- ### Micro-PIC Technology Demonstrator
- SOI platform (5-6 μm Si/1 μm SiO₂/ Si)
 - Integration of several different active and passive components on a single substrate
 - Employs several different miniaturization technologies on a single substrate: TIR waveguides, 1-D PBG concepts, thin-film electrochromic active optics, MEMS
 - Novel 1-D PBG wavelength selector for WDM systems
 - Novel T-junction beamsplitter - 160 x 200 μm
 - Novel VO₂/SOI optical switch
 - Novel polarization-independent SOI/MEMS optical switch

Summary of different technologies for NxM integrated-optic switches

Switch Technology	Substrate	Switching Speed	Channel Capacity	Size (cm ²)	Functional Density (channels/cm ²)
Electro-optic	LiNbO ₃	<nsec	6 x 6	350	0.03
Amplification Switch	III-V	nsec	4 x 4	0.04	400
Thermo-optic	Silica, SOI	2 msec	16 x 16	110	2.4
Total Internal Reflection	Silica, SOI	5 to 10 msec	32 x 32	6	171
MPB 2x2 MEMS	SOI	ms to μs		2 x 3 mm	120 (limited by MEMS actuator size)
MPB VO ₂	SOI	μs to ps		0.2 x 0.2 mm	> 2000

Comparison to Traditional Integrated-Optic Devices

Traditional SOI Device	Device area (cm ²)	New MPB Device	Device area (cm ²)
2x2 Thermo-optic Switch	0.4	2x2 MEMS/PBG Device	0.06-junction + MEMS, 0.02 (junction only)
1x2 silica splitter	0.3	TIR Beam splitter	0.0002
LiNbO ₃ high speed device	2	Von SOI high speed switch	<0.05
WDM linear Bragg grating	>10 cm long for 1 μm resolution	PBG linear Defect	< 50 μm long

- ### Conclusions
- Novel passive and active components have been formulated for the SOI platform
 - Proposed methodology employs TIR, PBG, MEMS and smart-coating concepts to improve device performance and significantly reduce device substrate area.
 - Concepts for novel PBG structures for WDM filtering have been developed that offer significantly greater feasibility for fabrication.
 - Proposed technology demonstrator incorporates new, advanced-technology concepts for miniaturization of optical devices on SOI platform

ANNEXES

- Examples comparison RF/Microphotonics
- MEMS and ANSYS simulations

MVA

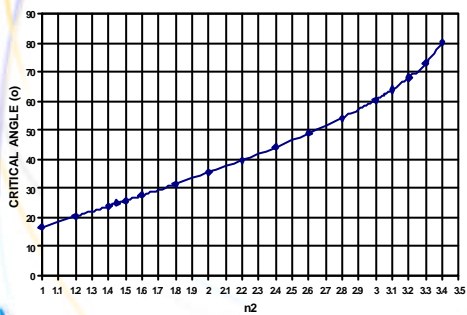
Miniaturization Technologies for SOI micro-PICS

- Total-Internal Reflection (TIR)
- Tunable 50 to 100 nm-thick smart materials (VO_n)
- Photonic band-gap concepts (1-D, 2-D)
- 3-D structures using silica on SOI and vertical couplers
- MEMS actuators (1 x 1 mm)

MVA

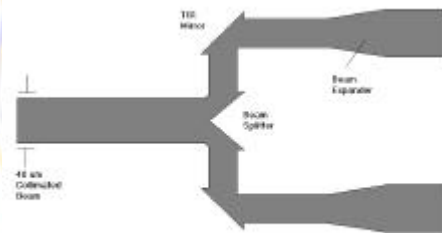
Critical angle for Total Internal Reflection

$n_1=3.45$



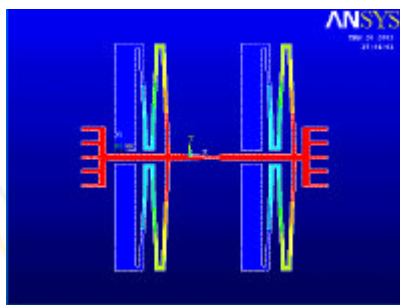
MVA

TIR Y-junction



MVA

MEMS Flexure Structure

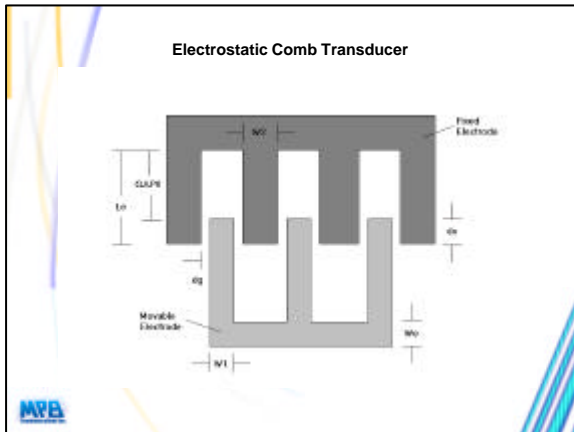


MVA

Ansys mechanical simulation of MEMS actuator.

Spring Length (μm)	Number of Spring Elements	Restoring Force ($\mu\text{N}/\mu\text{m}$)	Max Stress (50 μm displacement) ($\mu\text{N}/\mu\text{m}$)	Maximum Force (50 μm displacement) (μN)
300	3	34.7		1562
500	3	7.7	132	384
700	3	2.8	68	141
750	3	2.3	59	115
500	4	5.78	101	289
700	4	2.4	58	120
750	4	1.95	51	97
1000	4	0.825	29	41

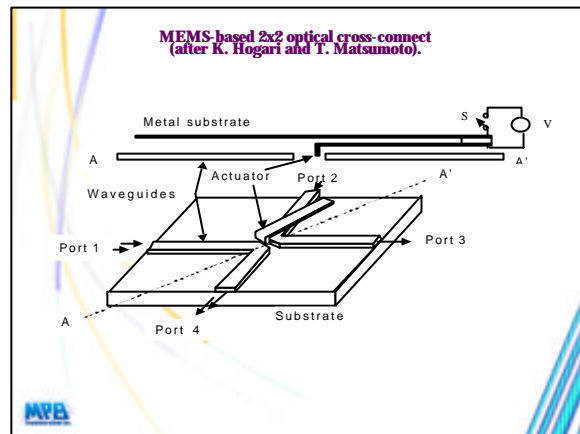
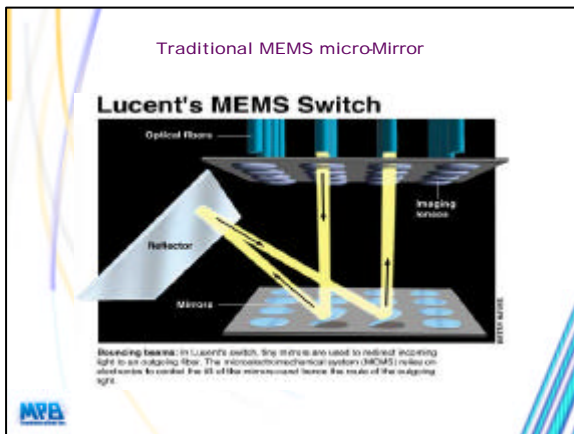
MVA



Dependence of Comb force on dg and V as simulated using Ansys.

$L_s = 750 \mu\text{m}$

Comb Drive Gap (dg) (mm)	V (V)	F (mN)	Nc
1	100	0.89	130
1	150	1.33	87
1.5	100	0.6	192
1.5	150	0.89	130
2	100	0.30	383
2	150	0.67	172



Specifications of optical switching characteristics for different applications in Space.

Application	Switch Type	Switching Speed
Optical Bus Network	$N \times M$	msec
Transponders, OADM	2x2 cross-bar	msec
Optical Beam-forming (Beam Steering)	2x2 cross-bar arrays	msec to μsec
OTDM	1xN	ns to ps (bit interleaving) μs to ps (packet switching)
Optical D/A	1xN	ns to ps (signal interleaving)
Multi-channel Sensor Systems	1xN	ms
Optical integrated gyroscope.	1x2 crossbar	ns

Comparison of electrical and optical box-to-box links.

Parameter	Electrical harness	Optical Ring/Bus
Typical weight	10kg	<02kg
EMI concerns	Yes requires special routing and shielding	No
Ground loop problems	Yes	No
System integration	Time-consuming and costly	Safe, considerably simplified system integration
Data rates	MHz	> 1GHz
Scalability	No, requires change in architecture	WDM - can add extra capacity using multiple wavelengths on single fiber.
Signal integrity	Low, requires significant shielding and data coding	High, enables high data transfers

Comparison of RF and Optical Link Characteristics.

Parameter	RF Link	WDM Optical Link
Information transfer capacity	150 Mbps	25 to 10 Gbps
Beam Divergence (θ in dB), dB is the antenna diameter	$(3 \cdot 10^{-4})/\theta$	$(1.5 \cdot \theta)/\theta$
Power loss ($1/\theta^2$)	1	10^8
Scalability for future needs	Poor	Yes (N wavelengths ($N > 40$))
Power requirements	BER dependent	< 12 W
Weight	> 100 kg	< 5 kg
Aperture size	Typically m ²	Typically 4" OD
Single to many point	Best	Poor
Point to point	Poor, need large aperture	Best, most power efficient
Redundance for high quality of service	Poor, single event upset can stop communications	Good, can reroute signals to different points and have several optical links on one satellite bus

MPL

Trade-off analysis of beam-forming techniques:

Parameter	Microwave ⁵	Fiber Optics	TOM
Weight	about 3 kg/m ²	< 1 kg/m ²	< 0.2 kg/m ²
Bandwidth	20 to 100 MHz	GHz	GHz
Tunable Delay	600 ps, 8 bit, 0.6° ripple (Paratek Microwave Inc)	1100 ps per grating, 10 ps ripple.	115 ps/cm, $< \pm 0.025$ ps accuracy, 12 bit resolution (SOI waveguides).
Method	Phase delay	True time delay	True time delay
EMI sensitivity	High	Low	Low
RF Frequency Sensitivity	High squint error	Frequency independent	Frequency independent
RF Matching	Impedance Matching Stubs		None, direct conversion to RF
Shielding	Machined Al enclosures for all critical RF components and matching stubs.		Minimal

MPL