n order to get rid of the externally powered

o the housing of the DSS could decrease mas

f DSS is integrated further, the mass and ine

ara meters:

ize, Mass, Power consu mption.

equi red developments:

Integrate FPGA and APS thus reducing power

(<250 m W requi red to make DSS2 have the s

Develop sol ar array on housing concept incl

Change to low power data interface (LVDS

Mi ni ature connectors necessary

Disadvantages

• No Sun no si gnal

(data interface stops)