RIR IN:- 79964

RIR OUT:- 79964

PARTS HISTORY LOG

Radiation Testing

PROGRAMME:- XMM

PART TYPE:- UC1842J

RADIATION REPORT:- RD 255

IGG TASK NUMBER:- 1500

SUMMARY OF TEST RESULTS

No parameter showed any significant drift with irradiation and no failures were recorded.

Radiation Report Number:- RD 255

Project:- XMM

Part Type:- UC1842J Date Code:- 9146

Manufacturer:- Unitrode IGG Task No:- 1500

Project Approval of Lot Traveller:-

Signed..... Date.27-1-98.

Position..Component Engineer

Serial Number Range:-

21 through 26 (not inclusive)

I certify that the subject component has been tested in accordance with the following radiation specifications:-

Test Method - ESA/SCC22900 ISSUE- 4 DATE- Jan '95

Irradiation Test Plan- XM-PL-IGG-0056 ISSUE- 2 DATE- Jan '97

Closed/Approved NCR No:- N N/A

Approved Waiver No:- WAR N/A

Signed... Date.26/1/98

Upscreening Engineer

Signed... Date.26/1/98.

Upscreening Manager

RADIATION REPORT NUMBER:- RD 255 DATE:- 22.1.98

PROJECT:- XMM RIR IN:- 79964

PART NUMBER:- UC1842J MANUFACTURER:- Unitrode

PROCUREMENT LEVEL:- SIC01-009-02B DATE CODE:- 9146

TEST METHOD:- ESA/SCC22900 ISSUE- 4 DATE- Jan '95

TEST PLAN:- XM-PL-IGG-0056 ISSUE- 2 DATE- Jan '97

START QUANTITY:- 5

No.	Test (Sample Size)	XM-PL-IGG-0056 Test Method and Conditions	Date in	Qty in	Date out	Qty out	SIGNED Op/QA
1	Serialisation and Selection of Control Sample (100%)	Control Sample= SN 21	7/1/98	5	7/1/98	4 + CONTROL SAMPLE	IGG 16 CT
2	Initial Electrical Measurements (100% read and record)	Table A Testing at IGG	7/1/98	4	7/1/98	4	IGG 16 CT
3	Initial Electrical Measurements (100% read and record)	Table A Testing at ERA	8/1/98	4	8/1/98	4	IGG 16 CT
4	Set-up and apply Bias per Figure 1	Verify Bias Circuit and conditions (in-situ) for all 4 test samples	8/1/98	4	8/1/98	4	IGG 16 CT
5	Irradiation 1 (4 samples)	Dose= 10kRAD(Si) Rate= 10RAD(Si) per second Time= 1000secs	8/1/98	4	8/1/98	4	IGG 16 CT
6	Interim 1 Electrical Measurements (100% read and record)	Table A. Bias to be maintained until testing is performed. Tdwell=10mins maximum	8/1/98	4	8/1/98	4	IGG 16 CT

Report No: RD 255	Part Type: UC1842J	Date: 22.1.98

No.	Test (Sample Size)	XM-PL-IGG-0056 Test Method and Conditions	Date in	Qty in	Date out	Qty out	SIGNED Op/QA
7	Irradiation 2 (4 samples)	As Test 5	8/1/98	4	8/1/98	4	IGG 16 CT
8	Interim 2 Electrical Measurements (100% read and record)	As Test 6	8/1/98	4	8/1/98	4	IGG 16 CT
9	Irradiation 3 (4 samples)	As Test 5	8/1/98	4	8/1/98	4	IGG 16 CT
10	Interim 3 Electrical Measurements (100% read and record)	As Test 6	8/1/98	4	8/1/98	4	IGG 16 CT
11	Irradiation 4 (4 samples)	Dose= 20kRAD(Si) Rate= 10RAD(Si) per second Time=2000secs	8/1/98	4	8/1/98	4	IGG 16 CT
12	Interim 4 Electrical Measurements (100% read and record)	As Test 6	8/1/98	4	8/1/98	4	IGG 16 CT
13	Irradiation 5 (4 samples)	Dose= 25kRAD(Si) Rate= 10RAD(Si) per second Time=2500secs	8/1/98	4	8/1/98	4	IGG 16 CT
14	Interim 5 Electrical Measurements (100% read and record)	As Test 6	8/1/98	4	8/1/98	4	IGG 16 CT
15	Irradiation 6 (4 samples)	As Test 13	8/1/98	4	8/1/98	4	IGG 16 CT

Report No: RD 255	Part Type: UC1842J	Date: 22.1.98

No.	Test (Sample Size)	XM-PL-IGG-0056 Test Method and Conditions	Date in	Qty in	Date out	Qty out	SIGNED Op/QA
16	Final Electrical Measurements (100% read and record)	As Test 6 At ERA	8/1/98	4	8/1/98	4	IGG 16 CT
17	Annealing Test (4 samples)	Bias for 24hrs min at +25°C (record exact time)	8/1/98	4	9/1/98	4	IGG 16 CT
18	Post Annealing Electrical Measurements (100% read and record)	Table A	9/1/98	4	9/1/98	4	IGG 16 CT
19	Accelerated Aging under bias (4 samples)	168 hours bias at +100±5°C	9/1/98	4	16/1/98	4	IGG 16 CT
20	Post Aging Electrical Measurements (100% read and record)	Table A	16/1/98	4	16/1/98	4	IGG 16 CT
21	Test Report Collation	[redacted]	[redacted]	[redacted]	26/1/98	[redacted]	IGG 2 CT
22	Test Report Approval	[redacted]	[redacted]	[redacted]	26/1/98	[redacted]	IGG 2 CT
23	NOTES:-						

Report No: RD 255 Part Type: UC1842J Date: 22.1.98

FAILURE LIST AND APPLICABLE NCR

Test No.	Serial Number(s)	Failed Parameter and Failure Mode	Applicable NCR

Report No: RD 255 Part Type: UC1842J Date: 22.1.98

TEST EQUIPMENT LIST

Item, Type Number and Serial Number	CT Inventory Number	Function	Calibration Due
Item: Radiation Source Type No: Cobalt 60 Serial No: N/A	ERA's Facility	Irradiation	1 / 1 /1999
Item: SZ Type No: M3000 Serial No: 884769	CT339	ATE	28 / 3 /1998
Item: THURLBY Type No: PL320 Serial No: 2GP0246	CT296	PSU	30 / 3 /1998
Item: Type No: Serial No:			/ /199
Item: Type No: Serial No:			/ /199
Item: Type No: Serial No:			/ /199
Item: Type No: Serial No:			/ /199
Item: Type No: Serial No:			/ /199
Item: Type No: Serial No:			/ /199
Item: Type No: Serial No:			/ /199
Item: Type No: Serial No:			/ /199
Item: Type No: Serial No:			/ /199

XMM RD255 RIR 79964	IRRADIATION TEST PLAN NO. XM-PL-IGG-0056	Issue No. 2 Date: January 1997 Page: 1/5

Component No. SIC01-009-02B	Component Designation: IC, Modulator Controller Type UC1842J	Irradiation Spec No. N/A Iss. Rev.

Specification	Acceptance	Electrical Meas.	Project/Programme
Generic: ESA/SCC 9000 Iss. 9 Detail: SIC01-009 Iss. 3	Evaluation ___ Element Diffusion ___ Lot X	In-situ ___ Remote X	XMM

Manufacturer: Unitrode Address: Merrimack NH, USA	Test Facility: ERA Address: Leatherhead Surrey ENGLAND	Originator: IGG CT Name: J. Arnold

Radiation Source:	Sample Size: 4	Exposure:	Annealing Test:	Radiation Level:
COBALT 60	Control Devices: 1	Single ___ Multiple X	YES X NO ___	10kRAD(Si), 50kRAD(Si) 20kRAD(Si), 75kRAD(Si) 30kRAD(Si), 100kRAD(Si)

Single Exposure:
Dose [kRAD(Si)]
Dose Rate [RAD(Si)/s]
Exposure Time

Not Applicable

Multiple Exposure:

Irradiation Steps	1	2	3	4	5	6
Dose [kRAD(Si)]	10	10	10	20	25	25
Maximum Dose Rate [RAD(Si)/s]	10	10	10	10	10	10
Minimum Exposure Time[s]	1000	1000	1000	2000	2500	2500

Bias Requirements: During and after Exposure (for remote electrical measurements): YES

Bias Conditions:
Test Circuits: The Electrical Bias circuit is given in Figure 1 herein.

Shielding: Shielding is required to minimize dose enhancement effects caused by low energy, scattered radiation. The test specimens shall be enclosed in a Pb/Al container of Pb 1.5mm minimum, surrounding an inner shield of Al 0.7 to 1.0mm.

Irradiation Test Sequence

Test Step	Description	Requirements
1	Irradiation Test Samples	Quantity 5 devices shall be selected from the lot delivered to IGG.
2	Serialisation	Serialisation - (if the devices are not serialised). Test units shall be serialised 1 to 4 and the control unit shall be 5.
3	Initial Electrical Measurements (at IGG)	Per Table A herein - (Read and Record) - on all 5 parts at IGG. (See Remarks 1 and 2).
4	Initial Electrical Measurements (at ERA)	Per Table A herein - (Read and Record) - on all 5 parts at ERA. (See Remarks 1 and 2).

Irradiation Test Sequence (Cont.)

Test Step	Description	Requirements
5	Set-up Test	Verify Bias Circuit and Voltages (In-situ) for 4 test units. (See Remark 3).
6	Irradiation Exposure	Verify radiation dose rate and position in the chamber to achieve required dose. Verify and witness duration of exposure to achieve required dose. (See Remark 4).
7	Intermediate Electrical Measurement (at ERA)	Bias to be maintained until test is performed. Test per Table A herein - (Read and Record) - on all 5 parts. Test to be performed immediately upon removal from chamber (less than 10 mins interval). Upon completion of test 10 test units shall be replaced in bias circuit and returned to chamber. Maximum interval between two consecutive exposures to be 30 mins. (See Remark 2).
8 to 22	Repeat Set-up/Exposure/Test sequence up to a Final Total Dose of 100kRAD(Si)	Repeat Steps 5, 6, 7 for a total of 6 cycles as per multiple exposure in Box No. 19. (See Remark 5).
23	Annealing	Bias shall be maintained during Annealing for 4 test units. Annealing shall be at room temperature for 24 hours. (See Remark 3).
24	Post Annealing Electrical Measurements (at IGG)	Per Table A herein - (Read and Record) - on all 5 parts at IGG. (See Remark 2).
25	Accelerated Aging under Bias	Bias shall be maintained during Aging for 4 test units. Aging shall be at T_{amb} = +100±5°C for 168 hours. (See Remark 3).
26	Final Electrical Measurements (at IGG)	Per Table A herein - (Read and Record) - on all 5 parts at IGG (See Remark 2).
27	Total Dose Irradiation Test Report	ESA/SCC No. 22900.

Remarks

1. The initial electrical measurements performed at IGG (Test Step 3) shall be performed within 24 hours of the initial electrical measurements at ERA (Test Step 4).
2. All electrical testing shall be performed on the same set of equipment in order to achieve correlation of results both at IGG and ERA.
3. The control unit shall not be biased during testing.
4. The dose rates and exposure times given above, may be adjusted during irradiation testing to achieve convenient test points but shall not exceed the limits specified in Box No. 19. The dose rates and exposure times used during the testing shall be recorded for each test step.
5. The set up/exposure/test sequence shall be stopped for any device that exhibits repeated functional failure.

TABLE A - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - T_{amb} = +25 ±5°C BEFORE, AT INTERMEDIATE POINTS AND ON COMPLETION OF IRRADIATION

NO.	CHARACTERISTICS	SYMBOL	CONDITIONS (See note 1)	LIMITS MIN.	LIMITS MAX.	UNIT
REFERENCE SECTION						
1	Reference output voltage	V_{REF}	I_O = 1.0mA	4.95	5.05	V
2	Line regulation	V_{RLINE}	12V ≤ V_{IN} ≤ 25V	-	20	mV
3	Load regulation	V_{RLOAD}	1.0mA ≤ I_O ≤ 20mA	-	25	mV
4	Output short-circuit current	I_{OS}	-	-30	-180	mA
OSCILLATOR SECTION						
5	Initial Accuracy	f_o	-	47	57	KHz
6	Voltage stability	ΔV_O	12V ≤ V_{CC} ≤ 25V	-	1.0	%
ERROR AMP SECTION						
7	Input Voltage	V_{IN}	$V_{PIN\ 1}$ = 2.5V	2.45	2.55	V
8	Input bias current	I_{IB}		-	-1.0	μA
9	Open loop voltage gain	A_{VOL}	2.0V ≤ V_O ≤ 4.0V	65	-	dB
10	Power supply rejection ratio	PSRR	12V ≤ V_{CC} ≤ 25V	60	-	dB
11	Output sink current	I_{SINK}	$V_{PIN\ 2}$ = 2.7V $V_{PIN\ 1}$ = 1.1V	2.0	-	mA
12	Output source current	I_{SOURCE}	$V_{PIN\ 2}$ = 2.3V $V_{PIN\ 1}$ = 5.0V	-0.5	-	mA
13	V_{OUT} high	V_{OH}	$V_{PIN\ 2}$ = 2.3V R_L = 15kΩ to ground	5.0	-	V
14	V_{OUT} low	V_{OL}	$V_{PIN\ 2}$ = 2.7V R_L = 15kΩ to pin 8	-	1.1	V
CURRENT SENSE SECTION						
15	Gain	A_{CS}	(See notes 2 & 3)	2.85	3.15	V/V
16	Maximum input signal	V_{CSMAX}	$V_{PIN\ 1}$ = 5.0V (See note 2)	0.9	1.1	V
17	Input bias current	I_{IB}	-	-	-10	μA
OUTPUT SECTION						
18	Output low voltage 1	V_{OL1}	I_{SINK} = 20mA	-	0.4	V
19	Output low voltage 2	V_{OL2}	I_{SINK} = 200mA	-	2.2	V
20	Output high voltage 1	V_{OH1}	I_{SOURCE} = 20mA	13	-	V
21	Output high voltage 2	V_{OH2}	I_{SOURCE} = 200mA	12	-	V

NOTES:- See Page 4

TABLE A - ELECTRICAL MEASUREMENTS AT ROOM TEMPERATURE - T_{amb} = +25 ±5°C BEFORE, AT INTERMEDIATE POINTS AND ON COMPLETION OF IRRADIATION

NO.	CHARACTERISTICS	SYMBOL	CONDITIONS (See note 1)	LIMITS		UNIT
				MIN.	MAX.	
UNDER-VOLTAGE LOCKOUT SECTION						
22	Start threshold	V_{TH}	-	15	17	V
23	Minimum operating voltage	V_{MIN}	After turn-on	9.0	11	V
POWER CONSUMPTION						
24	Maximum duty cycle	DC_{MAX}	-	95	100	%
25	Start-up current	I_{START}	-	-	1.0	mA
26	Operating supply current	I_{CC}	$V_{PIN2} = V_{PIN3} = 0V$	-	17	mA

NOTES:

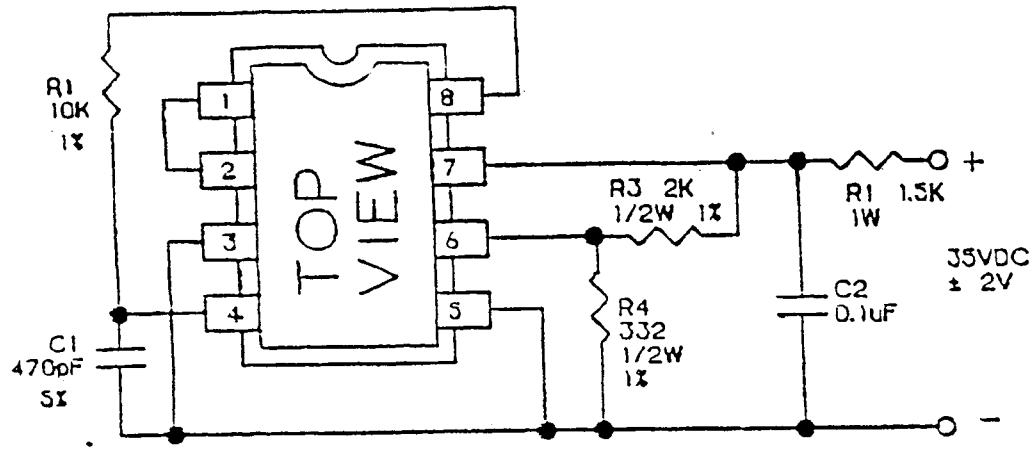
1. Unless otherwise specified Characteristics apply at V_{CC} = 15V (adjust V_{CC} above the start threshold before setting at 15V), R_T = 10KΩ and C_T = 3.3nF.

2. Parameters measured at trip point of latch with V_{PIN2} = 0V.

3. Gain is defined as:

$$A = \frac{\Delta V_{PIN1}}{\Delta V_{PIN3}} \quad , 0V \leq V_{PIN3} \leq 0.8V$$

FIGURE 1 - ELECTRICAL BIAS CIRCUIT FOR IRRADIATION TESTING



NOTES: (UNLESS OTHERWISE SHOWN)

1. All resistors are 1% tolerance, 1/8W.
2. All capacitors are 10% tolerance, rated 50V.

SZ-TESTSYSTEME Statistics QC Vers. 2.15 For TP :
RD2SS_UC1842J_INIT_EMS_@_IGG / 1.00 / 05.01.98 / SMR

Results file : RD2SS_UC1842J_INIT_EMS_@_IGG from: 07.01.98 / 10:01:15
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD2SS
Order number : D/C 9146
Vendor : UNITRODE
: CONTROL 21 ; RAD 22,23,25,26
: INITIAL EMS @ IGG
: UC 1842 J / 1.00 / 05.01.98 SMR

Test steps

1. Start threshold	15.00	...	17.00	V
2. Min.operating volt.	9.00	...	11.00	V
3. Start up current	0.00	...	1.00	mA
4. Oper.supply current	0.00	...	17.00	mA
5. Ref.output voltage	4.95	...	5.05	V
6. Line regulation	0.0	...	20.0	mV
7. Load regulation	0.0	...	25.0	mV
8. Output short circuit	-180.0	...	-30.0	mA
9. Initial accuracy	47.0	...	57.0	kHz
10. Amplitude Vpin 4	0.60	...	2.60	V
11. Input voltage	2.45	...	2.55	V
12. Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13. Output sink current	2.00	...	40.00	mA
14. Outp. source current	-4.00	...	-0.50	mA
15. Vout high (Pin 1)	5.00	...	15.00	V
16. Vout low (Pin 1)	0.00	...	1.10	V
17. Max.cur.sense signal	0.90	...	1.10	V
18. Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19. Outp.low (Is=20mA)	0.00	...	0.40	V
20. Outp.low (Is=200mA)	0.00	...	2.20	V
21. Outp.high (Is=20mA)	13.00	...	15.00	V
22. Outp.high(Is=200mA)	12.00	...	15.00	V

	21	22	23	25	26
1.1 [V]	16.30	16.10	16.30	16.30	16.50
2.1 [V]	9.80	9.90	9.70	9.90	9.70
3.1 [mA]	0.80	0.83	0.83	0.81	0.80
4.1 [mA]	12.33	12.14	12.54	12.39	12.12
5.1 [V]	4.99	4.97	4.99	4.98	5.01
6.1 [mV]	5.8	6.0	5.9	6.7	6.3
7.1 [mV]	10.5	10.0	10.5	10.5	10.5
8.1 [mA]	-99.9	-101.6	-104.9	-101.7	-100.6
9.1 [kHz]	50.3	51.5	52.4	51.8	50.8
10.1 [V]	1.79	1.69	1.71	1.68	1.76
11.1 [V]	2.50	2.49	2.53	2.51	2.52
12.1 [uA]	-0.25	-0.23	-0.25	-0.24	-0.23
13.1 [mA]	8.87	8.48	8.80	8.95	8.96
14.1 [mA]	-0.75	-0.74	-0.85	-0.75	-0.76
15.1 [V]	5.66	5.63	5.67	5.65	5.67
16.1 [V]	0.81	0.82	0.81	0.81	0.80
17.1 [V]	1.01	1.00	1.02	1.01	1.01
18.1 [uA]	-4.48	-4.63	-4.94	-4.52	-4.62
19.1 [V]	0.09	0.10	0.09	0.09	0.09
20.1 [V]	1.59	1.62	1.62	1.60	1.59
21.1 [V]	13.56	13.55	13.55	13.56	13.57
22.1 [V]	13.34	13.33	13.33	13.34	13.35

```
EI-TESTSYSTEME  Statistics 00    Vers. 2.15  for TA11
RD255 UC1842J INIT EMS @ ERA      / 1.00 / 05.01.98 / SMR
```

Results file : RD255.UC1842. INI EMS @ ERA from: 08.01.98 / 10:09:31
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD255
Order number : D/C 9145
Vendor : UNITRODE
: CONTROL 21 ; RAD 22,23,25,26
: INITIAL EMS @ ERA
: UC 1842 J / 1.00 / 05.01.98 / SMR

Test steps

1. Start threshold	15.00	...	17.00	V
2. Min.operating volt.	9.00	...	11.00	V
3. Start up current	0.00	...	1.00	mA
4. Oper.supply current	0.00	...	17.00	mA
5. Ref.output voltage	4.95	...	5.05	V
6. Line regulation	0.0	...	20.0	mV
7. Load regulation	0.0	...	25.0	mV
8. Output short circuit	-180.0	...	-30.0	mA
9. Initial accuracy	47.0	...	57.0	kHz
10. Amplitude Vpin 4	0.60	...	2.60	V
11. Input voltage	2.45	...	2.55	V
12. Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13. Output sink current	2.00	...	40.00	mA
14. Outp. source current	-4.00	...	-0.50	mA
15. Vout high (Pin 1)	5.00	...	15.00	V
16. Vout low (Pin 1)	0.00	...	1.10	V
17. Max.cur.sense signal	0.90	...	1.10	V
18. Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19. Outp.low (Is=20mA)	0.00	...	0.40	V
20. Outp.low (Is=200mA)	0.00	...	2.20	V
21. Outp.high (Is=20mA)	13.00	...	15.00	V
22. Outp.high(Is=200mA)	12.00	...	15.00	V

	21	22	23	25	26
1.1 [V]	16.30	16.10	15.90	16.30	15.50
2.1 [V]	9.90	9.90	9.70	9.90	9.70
3.1 [mA]	0.81	0.84	0.83	0.83	0.79
4.1 [mA]	12.39	12.11	12.53	12.33	12.14
5.1 [V]	4.99	4.97	4.99	4.98	5.01
6.1 [mV]	6.0	5.7	5.9	6.9	5.9
7.1 [mV]	10.2	9.9	10.3	10.5	11.0
8.1 [mA]	-102.8	-104.2	-107.7	-104.7	-104.1
9.1 [kHz]	50.8	51.5	52.4	51.8	51.0
10.1 [V]	1.77	1.68	1.71	1.68	1.67
11.1 [V]	2.50	2.49	2.53	2.51	2.52
12.1 [uA]	-0.25	-0.23	-0.24	-0.24	-0.23
13.1 [mA]	8.70	8.33	8.44	8.79	8.78
14.1 [mA]	-0.75	-0.73	-0.85	-0.74	-0.76
15.1 [V]	5.67	5.64	5.68	5.66	5.69
16.1 [V]	0.82	0.83	0.82	0.82	0.82
17.1 [V]	1.01	1.00	1.02	1.01	1.01
18.1 [uA]	-4.49	-4.62	-4.92	-4.51	-4.61
19.1 [V]	0.09	0.10	0.09	0.09	0.09
20.1 [V]	1.61	1.64	1.64	1.62	1.62
21.1 [V]	13.54	13.53	13.53	13.54	13.54
22.1 [V]	13.33	13.32	13.31	13.32	13.32

SZ-TESTSYSTEME Statistics 03 vers. 2.15 for TA11
RD255 UC1842J SMS @ 10 KRAD / 1.00 / 05.01.98 / SMR

Results file : RD255 UC1842J SMS @ 10 KRAD from: 08.01.98 / 10:16:07
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD255
Order number : D/C 9145
Vendor : UNITRODE
: CONTROL 21 ; RAD 22,23,25,26
: EMS @ 10 KRAD
: UC 1842 J / 1.00 / 05.01.98 / SMR

Test steps

1. Start threshold	15.00	...	17.00	V
2. Min.operating volt.	9.00	...	11.00	V
3. Start up current	0.00	...	1.00	mA
4. Oper.supply current	0.00	...	17.00	mA
5. Ref.output voltage	4.95	...	5.05	V
6. Line regulation	0.0	...	20.0	mV
7. Load regulation	0.0	...	25.0	mV
8. Output short circuit	-180.0	...	-30.0	mA
9. Initial accuracy	47.0	...	57.0	kHz
10. Amplitude Vpin 4	0.60	...	2.60	V
11. Input voltage	2.45	...	2.55	V
12. Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13. Output sink current	2.00	...	40.00	mA
14. Outp. source current	-4.00	...	-0.50	mA
15. Vout high (Pin 1)	5.00	...	15.00	V
16. Vout low (Pin 1)	0.00	...	1.10	V
17. Max.cur.sense signal	0.90	...	1.10	V
18. Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19. Outp.low (Is=20mA)	0.00	...	0.40	V
20. Outp.low (Is=200mA)	0.00	...	2.20	V
21. Outp.high (Is=20mA)	13.00	...	15.00	V
22. Outp.high(Is=200mA)	12.00	...	15.00	V

	21	22	23	25	26
1.1 [V]	19.30	19.10	19.30	19.30	19.60
2.1 [V]	9.80	9.90	9.70	9.90	9.70
3.1 [mA]	0.84	0.83	0.83	0.79	0.81
4.1 [mA]	12.41	12.12	12.49	12.33	12.11
5.1 [V]	4.99	4.96	4.99	4.98	5.01
6.1 [mV]	6.1	5.9	5.7	6.2	5.8
7.1 [mV]	10.3	9.9	10.0	10.5	10.3
8.1 [mA]	-103.0	-101.6	-105.4	-103.5	-102.2
9.1 [kHz]	51.0	51.8	52.4	51.8	51.0
10.1 [V]	1.77	1.68	1.70	1.68	1.75
11.1 [V]	2.50	2.49	2.53	2.51	2.52
12.1 [uA]	-0.25	-0.24	-0.25	-0.25	-0.24
13.1 [mA]	8.69	8.39	8.55	8.82	8.85
14.1 [mA]	-0.75	-0.74	-0.85	-0.74	-0.75
15.1 [V]	5.67	5.64	5.67	5.66	5.68
16.1 [V]	0.82	0.82	0.82	0.81	0.81
17.1 [V]	1.01	1.00	1.02	1.01	1.01
18.1 [uA]	-4.48	-4.78	-5.03	-4.72	-4.78
19.1 [V]	0.09	0.10	0.09	0.09	0.09
20.1 [V]	1.61	1.63	1.63	1.61	1.61
21.1 [V]	13.54	13.54	13.55	13.55	13.55
22.1 [V]	13.33	13.33	13.32	13.33	13.33

SI-TESTSYSTEME Statistics 03 Vers. 2.15 for TA11
RD255_UC1842J_EMS_@ 20.KRAD / 1.00 / 05.01.98 / SMR

Results file : RD255_UC1842J_EMS_@_20_KRAD from: 08.01.98 / 10:45:03
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD255
Order number : D/C 9146
Vendor : UNITRODE
: CONTROL 21 ; RAD 22,23,25,26
: EMS @ 20 KRAD
: UC 1842 J / 1.00 / 05.01.98 / SMR

Test steps

1. Start threshold	15.00	...	17.00	V
2. Min.operating volt.	9.00	...	11.00	V
3. Start up current	0.00	...	1.00	mA
4. Oper.supply current	0.00	...	17.00	mA
5. Ref.output voltage	4.95	...	5.05	V
6. Line regulation	0.0	...	20.0	mV
7. Load regulation	0.0	...	25.0	mV
8. Output short circuit	-180.0	...	-30.0	mA
9. Initial accuracy	47.0	...	57.0	kHz
10. Amplitude Vpin 4	0.60	...	2.60	V
11. Input voltage	2.45	...	2.55	V
12. Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13. Output sink current	2.00	...	40.00	mA
14. Outp. source current	-4.00	...	-0.50	mA
15. Vout high (Pin 1)	5.00	...	15.00	V
16. Vout low (Pin 1)	0.00	...	1.10	V
17. Max.cur.sense signal	0.90	...	1.10	V
18. Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19. Outp.low (Is=20mA)	0.00	...	0.40	V
20. Outp.low (Is=200mA)	0.00	...	2.20	V
21. Outp.high (Is=20mA)	13.00	...	15.00	V
22. Outp.high(Is=200mA)	12.00	...	15.00	V

	21	22	23	25	26
1.1 [V]	16.30	16.10	15.80	16.30	15.50
2.1 [V]	9.90	9.90	9.90	9.90	9.70
3.1 [mA]	0.83	0.83	0.82	0.81	0.80
4.1 [mA]	12.44	12.09	12.48	12.35	12.09
5.1 [V]	4.99	4.96	4.99	4.98	5.00
6.1 [mV]	5.6	6.1	5.9	6.6	6.2
7.1 [mV]	10.2	10.0	10.4	10.3	10.1
8.1 [mA]	-102.7	-102.4	-105.3	-103.7	-102.5
9.1 [kHz]	50.8	51.8	52.6	51.9	51.0
10.1 [V]	1.77	1.69	1.71	1.68	1.76
11.1 [V]	2.50	2.49	2.53	2.51	2.52
12.1 [uA]	-0.25	-0.24	-0.26	-0.26	-0.25
13.1 [mA]	8.70	8.38	8.55	8.79	8.81
14.1 [mA]	-0.75	-0.74	-0.85	-0.74	-0.76
15.1 [V]	5.67	5.64	5.67	5.66	5.68
16.1 [V]	0.82	0.82	0.82	0.81	0.81
17.1 [V]	1.01	1.00	1.02	1.01	1.01
18.1 [uA]	-4.48	-4.93	-5.14	-4.89	-4.94
19.1 [V]	0.09	0.10	0.09	0.09	0.09
20.1 [V]	1.61	1.63	1.63	1.61	1.61
21.1 [V]	13.54	13.55	13.55	13.55	13.55
22.1 [V]	13.33	13.33	13.32	13.33	13.33

SZ-TESTSYSTEME Statistics 03 Vers. 2.19 for TA11
RD255 UC1842J_EMS_@_30_KRAD / 1.00 / 05.01.98 / SMR

Results file : RD255_UC1842J_EMS_@_30_KRAD from: 08.01.98 / 11:01:08
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD255
Order number : D/C 9146
Vendor : UNITRODE
: CONTROL 21 ; RAD 22,23,25,26
: EMS @ 30 KRAD
: UC1842 J / 1.00 / 05.01.98 / SMR

Test steps

1.	Start threshold	15.00	...	17.00	V
2.	Min.operating volt.	9.00	...	11.00	V
3.	Start up current	0.00	...	1.00	mA
4.	Oper.supply current	0.00	...	17.00	mA
5.	Ref.output voltage	4.95	...	5.05	V
6.	Line regulation	0.0	...	20.0	mV
7.	Load regulation	0.0	...	25.0	mV
8.	Output short circuit	-180.0	...	-30.0	mA
9.	Initial accuracy	47.0	...	57.0	kHz
10.	Amplitude Vpin 4	0.60	...	2.60	V
11.	Input voltage	2.45	...	2.55	V
12.	Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13.	Output sink current	2.00	...	40.00	mA
14.	Outp. source current	-4.00	...	-0.50	mA
15.	Vout high (Pin 1)	5.00	...	15.00	V
16.	Vout low (Pin 1)	0.00	...	1.10	V
17.	Max.cur.sense signal	0.90	...	1.10	V
18.	Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19.	Outp.low (Is=20mA)	0.00	...	0.40	V
20.	Outp.low (Is=200mA)	0.00	...	2.20	V
21.	Outp.high (Is=20mA)	13.00	...	15.00	V
22.	Outp.high(Is=200mA)	12.00	...	15.00	V

1.1	[V]	15.20	15.13	15.30	15.32	15.50
2.1	[V]	9.90	9.90	9.90	9.90	9.70
3.1	[mA]	0.81	0.81	0.85	0.81	0.79
4.1	[mA]	12.48	12.09	12.50	12.33	12.10
5.1	[V]	4.99	4.96	4.98	4.98	5.00
6.1	[mV]	6.1	5.9	5.9	5.8	6.0
7.1	[mV]	10.1	9.9	10.3	10.3	10.4
8.1	[mA]	-102.4	-102.1	-105.4	-103.3	-102.0
9.1	[kHz]	51.0	51.8	52.4	52.1	50.8
10.1	[V]	1.78	1.69	1.71	1.68	1.68
11.1	[V]	2.50	2.49	2.53	2.51	2.52
12.1	[uA]	-0.25	-0.25	-0.26	-0.27	-0.26
13.1	[mA]	8.73	8.39	8.53	8.80	8.82
14.1	[mA]	-0.75	-0.73	-0.85	-0.74	-0.75
15.1	[V]	5.67	5.63	5.67	5.65	5.68
16.1	[V]	0.82	0.82	0.82	0.81	0.81
17.1	[V]	1.01	1.00	1.02	1.01	1.01
18.1	[uA]	-4.49	-5.03	-5.22	-5.02	-5.05
19.1	[V]	0.09	0.10	0.09	0.09	0.09
20.1	[V]	1.61	1.63	1.63	1.61	1.61
21.1	[V]	13.55	13.55	13.55	13.55	13.55
22.1	[V]	13.33	13.33	13.32	13.33	13.34

SZ-TESTSYSTEME Statistics 03 Vers. 2.15 for TA11
RD255_UC1842J_EMS_@_50 KRAD / 1.00 / 05.01.98 / SMR

Results file : RD255_UC1842J_EMS_@_50_KRAD from: 08.01.98 / 11:21:55
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD255
Order number : D/C 9145
Vendor : UNITRODE
: CONTROL 21 ; RAD 22.23.25.26
: EMS @ 50 KRAD
: UC 1842 J / 1.00 / 05.01.98 / SMR

Test steps

1. Start threshold	15.00	...	17.00	V
2. Min.operating volt.	9.00	...	11.00	V
3. Start up current	0.00	...	1.00	mA
4. Oper.supply current	0.00	...	17.00	mA
5. Ref.output voltage	4.95	...	5.05	V
6. Line regulation	0.0	...	20.0	mV
7. Load regulation	0.0	...	25.0	mV
8. Output short circuit	-180.0	...	-30.0	mA
9. Initial accuracy	47.0	...	57.0	kHz
10. Amplitude Vpin 4	0.60	...	2.60	V
11. Input voltage	2.45	...	2.55	V
12. Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13. Output sink current	2.00	...	40.00	mA
14. Outp. source current	-4.00	...	-0.50	mA
15. Vout high (Pin 1)	5.00	...	15.00	V
16. Vout low (Pin 1)	0.00	...	1.10	V
17. Max.cur.sense signal	0.90	...	1.10	V
18. Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19. Outp.low (Is=20mA)	0.00	...	0.40	V
20. Outp.low (Is=200mA)	0.00	...	2.20	V
21. Outp.high (Is=20mA)	13.00	...	15.00	V
22. Outp.high(Is=200mA)	12.00	...	15.00	V

	21	22	23	25	26
1.1 [V]	16.30	16.10	16.90	16.30	15.50
2.1 [V]	9.00	9.00	9.00	9.00	9.70
3.1 [mA]	0.03	0.04	0.03	0.01	0.00
4.1 [mA]	12.44	12.06	12.45	12.25	12.06
5.1 [V]	4.98	4.96	4.38	4.98	5.00
6.1 [mV]	6.0	5.6	5.7	6.0	6.1
7.1 [mV]	9.7	9.9	10.4	10.7	10.3
8.1 [mA]	-101.0	-101.0	-104.4	-102.6	-101.7
9.1 [kΩ]	51.0	51.0	51.4	51.0	51.0
10.1 [V]	1.70	1.69	1.71	1.68	1.67
11.1 [V]	2.50	2.49	2.52	2.51	2.52
12.1 [uA]	-0.25	-0.26	-0.27	-0.28	-0.27
13.1 [mA]	0.76	0.38	0.56	0.00	0.02
14.1 [mA]	-0.75	-0.73	-0.66	-0.74	-0.75
15.1 [V]	5.67	5.63	5.66	5.65	5.69
16.1 [V]	0.01	0.02	0.01	0.01	0.01
17.1 [V]	1.01	1.00	1.02	1.01	1.01
18.1 [uA]	-4.48	-5.23	-5.38	-5.26	-5.26
19.1 [V]	0.09	0.10	0.09	0.09	0.09
20.1 [V]	1.60	1.63	1.63	1.61	1.61
21.1 [V]	13.55	13.55	13.55	13.55	13.55
22.1 [V]	13.33	13.33	13.33	13.33	13.34

SZ-TESTSYSTEME Statistics 03 vers. 2.15 for TA11
RD255 UC1842J_EMS_@_75_KRAD / 1.00 / 05.01.98 / SMR

Results file : RD255_UC1842J_EMS_@_75_KRAD from: 08.01.98 / 12:31:13
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD255
Order number : D/C 9148
Vendor : UNITRODE
: CONTROL 21 ; RAD 22,23,25,26
: EMS @ 75 KRAD
: UC 1842 J / 1.00 / 05.01.98 / SMR

Test steps

1.	Start threshold	15.00	... 17.00	V
2.	Min.operating volt.	9.00	... 11.00	V
3.	Start up current	0.00	... 1.00	mA
4.	Oper.supply current	0.00	... 17.00	mA
5.	Ref.output voltage	4.95	... 5.05	V
6.	Line regulation	0.0	... 20.0	mV
7.	Load regulation	0.0	... 25.0	mV
8.	Output short circuit	-180.0	... -30.0	mA
9.	Initial accuracy	47.0	... 57.0	kHz
10.	Amplitude Vpin 4	0.60	... 2.60	V
11.	Input voltage	2.45	... 2.55	V
12.	Inp.bias curr.(Pin2)	-1.00	... 1.00	uA
13.	Output sink current	2.00	... 40.00	mA
14.	Outp. source current	-4.00	... -0.50	mA
15.	Vout high (Pin 1)	5.00	... 15.00	V
16.	Vout low (Pin 1)	0.00	... 1.10	V
17.	Max.cur.sense signal	0.90	... 1.10	V
18.	Inp.bias curr.(Pin3)	-10.00	... 10.00	uA
19.	Outp.low (Is=20mA)	0.00	... 0.40	V
20.	Outp.low (Is=200mA)	0.00	... 2.20	V
21.	Outp.high (Is=20mA)	13.00	... 15.00	V
22.	Outp.high(Is=200mA)	12.00	... 15.00	V

	21	22	23	25	26
2.1 [V]	9.90	9.90	9.90	9.90	9.70
3.1 [mA]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
4.1 [mA]	12.46	12.04	12.44	12.28	12.06
5.1 [V]	4.96	4.96	4.90	4.96	5.00
6.1 [mV]	6.7	6.5	6.6	6.4	6.0
7.1 [mV]	10.2	10.7	10.1	10.9	11.1
8.1 [mA]	-102.0	-102.4	-104.0	-103.3	-102.0
9.1 [kHz]	50.8	51.0	52.6	52.1	51.0
10.1 [V]	1.70	1.60	1.71	1.67	1.60
11.1 [V]	2.50	2.49	2.52	2.51	2.51
12.1 [uA]	-0.25	-0.27	-0.20	-0.30	-0.20
13.1 [mA]	0.75	0.32	0.52	0.74	0.77
14.1 [mA]	-0.75	-0.73	-0.85	-0.74	-0.75
15.1 [V]	5.67	5.93	5.66	5.65	5.60
16.1 [V]	0.81	0.82	0.82	0.81	0.81
17.1 [V]	1.01	0.99	1.02	1.01	1.01
18.1 [uA]	-4.46	-5.43	-5.53	-5.50	-5.46
19.1 [V]	0.00	0.10	0.10	0.00	0.00
20.1 [V]	1.61	1.63	1.63	1.61	1.61
21.1 [V]	13.55	13.54	13.55	13.55	13.55
22.1 [V]	13.33	13.33	13.33	13.33	13.34

SI-TESTSYSTEME Statistics 03 Vers. 2.15 for TA11
RO255_UC1842J_EMS_@_100_KRAD / 1.00 / 05.01.98 / SMR

Results file : RO255_UC1842J_EMS_@_100_KRAD from: 09.01.98 / 12:41:55
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RO255
Order number : D/C 9148
Vendor : UNITRODE
: CONTROL 21 ; RAD 22.23.25.26
: EMS @ 100 KRAD
: UC 1842 J / 1.00 / 05.01.98 / SMR

Test steps

1. Start threshold	15.00		17.00	V
2. Min.operating volt.	9.00	...	11.00	V
3. Start up current	0.00	...	1.00	mA
4. Oper.supply current	0.00	...	17.00	mA
5. Ref.output voltage	4.95	...	5.05	V
6. Line regulation	0.0	...	20.0	mV
7. Load regulation	0.0	...	25.0	mV
8. Output short circuit	-180.0	...	-30.0	mA
9. Initial accuracy	47.0	...	57.0	kHz
10. Amplitude Vpin 4	0.50	...	2.50	V
11. Input voltage	2.45	...	2.55	V
12. Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13. Output sink current	2.00	...	40.00	mA
14. Outp. source current	-4.00	...	-0.50	mA
15. Vout high (Pin 1)	5.00	...	15.00	V
16. Vout low (Pin 1)	0.00	...	1.10	V
17. Max.cur.sense signal	0.90	...	1.10	V
18. Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19. Outp.low (Is=20mA)	0.00	...	0.40	V
20. Outp.low (Is=200mA)	0.00	...	2.20	V
21. Outp.high (Is=20mA)	13.00	...	15.00	V
22. Outp.high(Is=200mA)	12.00	...	15.00	V

	21	22	23	25	26
1.1 [V]	5.20	5.10	5.90	5.30	5.50
2.1 [V]	9.90	9.90	9.90	9.90	9.70
3.1 [mA]	0.81	0.81	0.83	0.79	0.79
4.1 [mA]	12.45	11.99	12.41	12.26	12.04
5.1 [V]	4.98	4.98	4.98	4.97	5.00
6.1 [mV]	6.1	5.6	6.7	6.5	6.5
7.1 [mV]	10.0	10.5	11.3	10.9	12.9
8.1 [mA]	-101.4	-101.7	-104.3	-102.6	-101.5
9.1 [kHz]	50.9	52.1	52.6	52.1	51.0
10.1 [V]	1.70	1.68	1.71	1.69	1.67
11.1 [V]	2.50	2.49	2.52	2.51	2.51
12.1 [uA]	-0.25	-0.28	-0.29	-0.31	-0.29
13.1 [mA]	9.70	9.34	9.52	9.76	9.79
14.1 [mA]	-0.75	-0.73	-0.85	-0.74	-0.75
15.1 [V]	5.67	5.63	5.66	5.65	5.67
16.1 [V]	0.81	0.82	0.81	0.81	0.81
17.1 [V]	1.01	0.99	1.02	1.01	1.01
18.1 [uA]	-4.49	-5.60	-5.66	-5.71	-5.67
19.1 [V]	0.09	0.10	0.10	0.09	0.09
20.1 [V]	1.60	1.63	1.63	1.61	1.61
21.1 [V]	13.55	13.55	13.55	13.55	13.55
22.1 [V]	13.34	13.33	13.33	13.33	13.34

SZ-TESTSYSTEME Statistics 03 Vers. 2.15 for TA11
RD255 UC1842J_POST ANNEAL_EMS / 1.00 / 05.01.98 / SMR

Results file : RD255_UC1842J_POST_ANNEAL_EMS from: 09.01.98 / 14:37:33
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD255
Order number : D/C 9145
Vendor : UNITRODE
: CONTROL 21 ; RAD 22.23.25.26
: POST 24HRS ANNEAL EMS
: UC 1842 J / 1.00 / 05.01.98 / SMR

Test steps

1. Start threshold	15.00	...	17.00	V
2. Min.operating volt.	9.00	...	11.00	V
3. Start up current	0.00	...	1.00	mA
4. Oper.supply current	0.00	...	17.00	mA
5. Ref.output voltage	4.95	...	5.05	V
6. Line regulation	0.0	...	20.0	mV
7. Load regulation	0.0	...	25.0	mV
8. Output short circuit	-100.0	...	-30.0	mA
9. Initial accuracy	47.0	...	57.0	kHz
10. Amplitude Vpin 4	0.80	...	2.80	V
11. Input voltage	2.45	...	2.55	V
12. Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13. Output sink current	2.00	...	40.00	mA
14. Outp. source current	-4.00	...	-0.50	mA
15. Vout high (Pin 1)	5.00	...	15.00	V
16. Vout low (Pin 1)	0.00	...	1.10	V
17. Max.cur.sense signal	0.90	...	1.10	V
18. Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19. Outp.low (Is=20mA)	0.00	...	0.40	V
20. Outp.low (Is=200mA)	0.00	...	2.20	V
21. Outp.high (Is=20mA)	13.00	...	15.00	V
22. Outp.high(Is=200mA)	12.00	...	15.00	V

	21	22	23	25	26
1.1 [V]	16.10	16.10	15.90	16.30	15.50
2.1 [V]	9.90	9.90	9.90	9.90	9.70
3.1 [mA]	0.93	0.93	0.93	0.80	0.78
4.1 [mA]	12.36	12.06	11.44	12.19	12.06
5.1 [V]	4.36	4.36	4.36	4.37	5.00
6.1 [mV]	6.0	6.0	6.0	6.0	6.0
7.1 [mV]	11.0	10.7	10.4	11.4	10.3
8.1 [mA]	-100.1	-101.4	-104.0	-101.0	-101.0
9.1 [kHz]	50.5	51.0	52.4	51.0	51.0
10.1 [V]	1.70	1.69	1.70	1.69	1.69
11.1 [V]	2.50	2.49	2.52	2.51	2.52
12.1 [uA]	-0.25	-0.27	-0.29	-0.30	-0.29
13.1 [mA]	0.85	0.39	0.53	0.85	0.85
14.1 [mA]	-0.75	-0.73	-0.95	-0.74	-0.75
15.1 [V]	5.56	5.53	5.56	5.55	5.57
16.1 [V]	0.91	0.92	0.91	0.91	0.91
17.1 [V]	1.01	1.00	1.02	1.01	1.01
18.1 [uA]	-4.50	-5.42	-5.52	-5.50	-5.40
19.1 [V]	0.09	0.10	0.09	0.09	0.09
20.1 [V]	1.58	1.63	1.63	1.60	1.60
21.1 [V]	13.56	13.55	13.55	13.55	13.56
22.1 [V]	13.34	13.33	13.33	13.34	13.34

SZ-TESTSYSTEME Statistics 03 Vers. 2.15 for TA11 :
RD255 UC1842J FINAL EMS / 1.00 / 05.01.88 / SMR

Results file : RD255 UC1842J FINAL EMS from: 10.12.87 / 11:53:09
Operator : PAUL RUSSELL
Part number : UC1842J
Lot number : RD255
Order number : D/C 9146
Vendor : UNITRODE
: CONTROL 21 ; RAD 22,23,25,26
: FINAL EMS
: UC 1842 J / 1.00 / 05.01.88 / SMR

Test steps

1. Start threshold	15.00	...	17.00	V
2. Min.operating volt.	9.00	...	11.00	V
3. Start up current	0.00	...	1.00	mA
4. Oper.supply current	0.00	...	17.00	mA
5. Ref.output voltage	4.95	...	5.05	V
6. Line regulation	0.0	...	20.0	mV
7. Load regulation	0.0	...	25.0	mV
8. Output short circuit	-100.0	...	-30.0	mA
9. Initial accuracy	47.0	...	57.0	kHz
10. Amplitude Vpin 4	0.50	...	2.50	V
11. Input voltage	2.45	...	2.55	V
12. Inp.bias curr.(Pin2)	-1.00	...	1.00	uA
13. Output sink current	2.00	...	40.00	mA
14. Outp. source current	-4.00	...	-0.50	mA
15. Vout high (Pin 1)	5.00	...	15.00	V
16. Vout low (Pin 1)	0.00	...	1.10	V
17. Max.cur.sense signal	0.90	...	1.10	V
18. Inp.bias curr.(Pin3)	-10.00	...	10.00	uA
19. Outp.low (Is=20mA)	0.00	...	0.40	V
20. Outp.low (Is=200mA)	0.00	...	2.20	V
21. Outp.high (Is=20mA)	13.00	...	15.00	V
22. Outp.high(Is=200mA)	12.00	...	15.00	V

	21	22	23	25	26
1.1 [V]	16.10	16.10	16.00	16.30	16.50
2.1 [V]	9.90	9.90	9.90	9.90	9.70
3.1 [mA]	0.81	0.83	0.85	0.78	0.80
4.1 [mA]	12.33	12.00	12.40	12.20	12.06
5.1 [V]	4.99	4.98	4.96	4.98	5.00
6.1 [mV]	5.1	5.7	6.0	5.6	6.2
7.1 [mV]	11.5	10.5	10.7	10.7	10.9
8.1 [mA]	-100.1	-101.4	-104.8	-101.9	-101.8
9.1 [kHz]	50.9	51.8	52.4	51.8	51.0
10.1 [V]	1.79	1.69	1.70	1.13	1.75
11.1 [V]	2.50	2.49	2.52	2.51	2.51
12.1 [uA]	-0.25	-0.25	-0.27	-0.27	-0.26
13.1 [mA]	0.85	0.43	0.57	0.89	0.87
14.1 [mA]	-0.75	-0.74	-0.85	-0.74	-0.76
15.1 [V]	5.66	5.63	5.66	5.65	5.67
16.1 [V]	0.81	0.82	0.81	0.81	0.81
17.1 [V]	1.01	1.00	1.02	1.01	1.01
18.1 [uA]	-4.49	-5.05	-5.24	-5.03	-5.05
19.1 [V]	0.09	0.10	0.09	0.09	0.09
20.1 [V]	1.58	1.63	1.63	1.60	1.60
21.1 [V]	13.56	13.56	13.56	13.56	13.56
22.1 [V]	13.34	13.33	13.33	13.34	13.34

TRAVEL VISUAL INSPECTION

RIR No. 79964 TASK: 1500 PART TYPE: MicroCircuit

ACTUAL PART MARKING: SIC01-009028 A U OB9146

DATE CODE/LOT IDENTITY: OB9146

PACKAGE MARKING: OB9146 M13804 LAT 2
KS106590 UC1842J/90007 SIC-009-02B

LEAD FINISH: SAT PART CONDITION: SAT PACKAGE CONDITION: SAT

COMMENTS: ~~YES/NO~~ MECHANICAL DIMENSIONS: SAT

INSPECTED BY: D. Tees DATE: 6-11-97

COMMENTS/OBSERVATIONS

SERIAL NUMBER CHECKLIST

LAT 2.
RIR 799964

KEY TO CODES USED ON THIS LIST

0	=	DELIVERED TO IGG	L3E	=	LAT3 ELECTRICAL MEASUREMENT
DPA	=	IGG DPA SAMPLE	L3D	=	LAT3 DESTRUCT
RM	=	REJECT AT MANUFACTURER	L2	=	LAT 2
NCR	=	REJECT AT IGG	INSPECTION STAMP	=	S/NO's DELIVERED TO USER

START No. .....

1	2	3	4	5	6	7	8	9	10
11	12	13	14	15	16	17	18	19	20
21	22	23	24	25	26	27	28	29	30
31	32	33	34	35	36	37	38	39	40
41	42	43	44	45	46	47	48	49	50
51	52	53	54	55	56	57	58	59	60
61	62	63	64	65	66	67	68	69	70
71	72	73	74	75	76	77	78	79	80
81	82	83	84	85	86	87	88	89	90
91	92	93	94	95	96	97	98	99	00