

TEST KEY; LM139A / RAD MNLM139A-X-RH REV 0A0						
			POST_RAD		PRE_RAD	
TEST#	TEST NAME	CONDITIONS	LIMITS		LIMITS	
			MIN	MAX	MIN	MAX
1	SUPPLY CURRENT, ICC	+VCC=5V, VCM=0V, RS=50 OHMS		2mA		2mA
2	SUPPLY CURRENT, ICC;	+VCC=30V,VCM=0V, RS=50 OHMS		2mA		2mA
1003,2003,3003,4003	INPUT OFFSET VOLTAGE, VIO	VCC=30V, VCM=0V	-2.5mV	2.5mV	-2mV	2mV
1004,2004,3004,4004	INPUT OFFSET VOLTAGE, VIO	+VCC=30V,VCM=28.5/28V,VO=1.5V	-2.5mV	2.5mV	-2mV	2mV
1005,2005,3005,4005	INPUT OFFSET VOLTAGE, VIO		-2.5mV	2.5mV	-2mV	2mV
1006,2006,3006,4006	COM MODE REJECT RATIO, CMRR	+VCC=30V, VCM=28.5V	60dB	*	60dB	*
1007,2007,3007,4007	PWR SUPLY REJ RATIO, PSRR	+VCC=5V TO 30V	60dB	*	60dB	*
1008,2008,3008,4008	+ INPUT BIAS CURRENT; +IIB	VO=1.5V	-110nA		-100nA	
1009,2009,3009,4009	INPUT BIAS CURRENT; -IIB	VO=1.5V	-110nA		-100nA	
1010,2010,3010,4010	INPUT OFFSET CURRENT, IIO	V0=1.5V	-25nA	25nA	-25nA	25nA
1011,2011,3011,4011	OUTPUT LEAKAGE CURRENT, ICEX	+VCC=30V, VO=30V		1uA		1uA
1012,2012,3012,4012	OUTPUT SINK CURRENT, ISINK	VO=1.5V	6mA		6mA	
1013,2013,3013,4013	OUTPUT SATURATION VOLTAGE, VSAT	ISINK=4MA		400mV		400mV
1014,2014,3014,4014	OPEN LOOP GAIN, AVS	+VCC=15V, RL=15K, VO=1V TO 11V	50V/mV		50V/mV	
1015,2015,3015,4015	RESPONSE TIME, TRLH	VOD=5MV		5uS		5uS
1016,2016,3016,4016	RESPONSE TIME, TRLH	VOD=50MV		0.9uS		0.8uS
1017,2017,3017,4017	RESPONSE TIME, TRHL	VOD=5MV		2.5uS		2.5uS
1018,2018,3018,4018	RESPONSE TIME, TRHL	VOD=50MV		0.8uS		0.8uS
1019,2019,3019,4019	DIFFERENTIAL INPUT VOLTAGE, VDIFF	VDIFF=36V	-500nA	500nA	-500nA	500nA

* There is no maximum in the program but the tester can only show up to 120db