

# ESA-Industry & Academia

## Meeting on

### *“Optical Interconnects for High Throughput On Board Processors”*

**9<sup>th</sup> February 2006**

**Newton meeting room, ESTEC**

#### Agenda

**09:30 – 10:00 Coffee and Welcome Address**

**10:00 – 10:30 1 - Interconnect Challenges For The Next Generation Of On-Board Processors**  
Lewis Farrugia (EADS Astrium)

**10:30 – 10:50 2 - Multi-Giga Bit Optical Interconnects For Digital Processors**  
Norbert Venet (Alcatel Space)

**10:50 – 11:10 3 - Planar Integrated Free-Space Optics As Interconnection Technology - Principle And Demonstrations**  
Manfred Jarczycki (Fern University of Hagen)

**11:10 – 11:30 4 - Optically Interconnected Computing**  
Gordon Russell (Heriot-Watt University)

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**11:30 – 11:40 Coffee Break**

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**11:40 – 12:00 5 - Micro-Optical Modules For Chip And Board Level Interconnects**  
Christof Debaes (Vrije Universiteit Brussel)

**12:00 – 12:20 6 - Silicon Photonics**  
Roel Baets (University of Ghent/IMEC)

**12:20 – 12:40 7 - WDM Switched Networks**  
Tom Farrell (InTune Technologies)

**12:40 – 12:50 8 - Optics On Future Printed Circuit Board In High Speed Data Transmission Applications**  
Antti Tanskanen (VTT)

**12:50 – 13:00 9 - Surface Normal Electro-Absorption Modulators**  
Bertrand Noharet (Acreo)

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**13:00 – 14:00 Lunch**

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**14:00 – 16:30 Round Table Discussion**

**16:30 - 17:00 Conclusions**

**17:00 End of Meeting**